

PROJECT SILO TECHNICAL MEMO #5

SUBJECT: BIASED MULTI-PATH STORAGE ELEMENT WITH COINCIDENT
CURRENT SELECTION

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ABSTRACT: The multi-path core structure described in this report was first conceived in the summer of 1955 by an IBM group investigating the technique of coincident-flux selection as a method of achieving a high-speed memory cycle. It has now been found that the use of a bias winding makes possible the operation of this structure in a high-speed memory without the serious back-voltage problem encountered in the coincident-flux method.

The contents of this report include 1) a description of the core structure, 2) the theory of the operation of the core and the use of bias, and 3) the results of experimental observations on the operation of the core.

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