

- dead end
- circuit improvements - ~~improvements~~
- circuit & logical improvements (w/la 5090)

possible plans:

(1) STRETCH is completed by DSD

Harvest " " " "

first 2 commercial machines " " "

subsequent production ^{& sales} to be done in FSD

- no improved version of machine to be developed in either FSD or DSD -- (note: until competition forces it eventually)

ie, STRETCH would be a dead end

(2) STR completed by DSD

Harvest " " "

Present STR-HVST	Improved STR Host	all new computer	
yes	none	none	dead end follow with approx
yes	yes	none	delay
yes	none	yes	dead end - jumps to next
yes	yes	yes	prob dead on all fronts

completion of ACC stretch	completion of Harvest	early commercial machines	later commercial machines	Machine Improvement Program	next generation program	preparation of STR + HVST	
						utility	application
DSD	DSD	DSD	DSD	DSD stretch	Research to DSD	DSD	DSD content
FSD (not desirable)	FSD	FSD	FSD	DSD non stretch	Research to FSD	DP (appl prog)	DP appl (ing)
				FSD stretch Plant dp	FSD with Research support	FSD content	FSD content
				FSD improved design	DSD only FSD only other divisions		ACC content