

R. Cooley
November 18, 1960
500/Kingston Eng. Lab.

Up-Graded STRETCH

Mr. E. V. Hofler
Dept. 595, Kgn

ccs - Mr. H. E. Cooley, Dept. 500, KEL
Mr. N. P. Edwards, FSD, Rockville, Md. <--

Mr. W. Buchholz of the Corporate Staff dropped in on November 16 and our conversation touched upon the future of the STRETCH program. As you know from our previous meeting on the Super-STRETCH machine, Mr. Buchholz is very interested in the possibility of up-grading the STRETCH performance and, at the same time, reducing costs so as to be more competitive with machines that can be expected during the next three to four years.

It is reported that Mr. M. Paley now feels he has a clear responsibility for planning such an up-grading of the STRETCH machine. His approach appears to be centered around the use of a 1/2 - 3/4 usec., 16K word memory and the so called 5 nanosecond circuits. Mr. Buchholz is inclined to feel that we should investigate the feasibility of a more up-to-date set of circuits in the 1 nanosecond region. He further feels that any updating program should be planned for completion within 2, or at the most, 2 1/2 years and that complete program compatibility with the present STRETCH machine should be maintained.

In view of the apparent responsibilities of Mr. Paley, we should:

- a. clarify our own desires for activity in this program
- b. as appropriate, develop our own concrete proposals for implementing such a program
- c. enter into discussions with DSD on this program at appropriate levels.

Your consideration of these factors and possibilities will be appreciated.

RJC:mc

R. J. Cypser

11/23/61

1931