

STRETCH SYSTEM SCHOOL

Monday, March 2, 1959

- 9:00 General Introduction to Programming System Projects:
- A. Preview of topics for the week (overall system philosophy)
 - B. Introduction to STRAP
- 9:30 STRAP Input Formats
- A. Coding Forms
 - B. Card Formats
- 10:30 Instruction Formats: Floating Point, Miscellaneous,
Indicator Branch, Direct and Immediate Index
- 12:30 Instruction Formats: Count and Branch, Branch on Bit,
Transmit, VFL
- 1:30 Problems
- 2:30 Data Pseudo Operations: Control of instructions through
the data description field; the form of data Operands.
- 3:30 Symbol Classes
- A. Programmer-designed symbols
 - B. System symbols

Tuesday, March 3, 1959

- 9:30 Number Systems: Bit Addresses and Integers
- A. Symbolization of integers through SYN
 - B. Symbolization of data description through SYN
 - C. Data Description over-ruling in the P mode

- 10:30 Further aspects of data entry and reservation:
- A. The reservation and naming of mathematical arrays
 - B. The significance of the integer in bit addresses distinction in an index field and in array naming
- 12:30 Entry Modes
- A. Instruction format entry modes
 - B. Data format entry modes
 - C. Usage of parenthetical instruction expressions
- 1:30 Problems
- 2:30 Data entries in an immediate address field
- 3:30 Arithmetic expressions
- A. STRAP (arithmetic)
 - B. Mixed integer bit address in SYN statement

Wednesday, March 4, 1959

- 8:30 Other Pseudo Operations (XW, VS, CS, RS)
- 9:30 Other Pseudo Operations (ORG, CNOP, ENB)
- 10:30 Problems
- 12:30 STRAP output formats
- 1:30 STRAP extension
- A. Heading and Data Blocking
- 2:30 Load and Go with SQUASH decks
- 3:30 Macro-instruction and symbolic connection mechanism

Thursday, March 5, 1959

- 8:30 Data Instruction Controls
- 9:30 Computation at Compile Time
- 10:30 Automatic Programming
- 12:30 Automatic Programming and Summary of STRAP spectrum
- 1:30 Primitive Supervisor
 - A. Loader (card formats)
 - B. Interrupt Handling
 - C. Dump and other Debugging
- 2:30 Unisupervisor
 - A. Communication Problems
 - B. Memory area required (protection)
 - C. Control of Input Output
 - D. Usage of Supervisor Subroutines by PP
 - E. Subroutine linkage
- 3:30 I/O Handling
 - A. I/O Macro
 - B. Editing
 - C. Interrupt Table
 - D. Peripheral Usage

Friday, March 6, 1959

- 8:30 Debugging
- A. Dump
 - B. Trace
- 9:30 Multi-Supervisor
- A. Protection
 - B. Priority
 - C. Debugging - IQS
 - D. Other Future Developments
- 10:30 Harvest Program - Part I
- 11:30 Harvest Program - Part II
- 1:30 to 4:30 To be announced