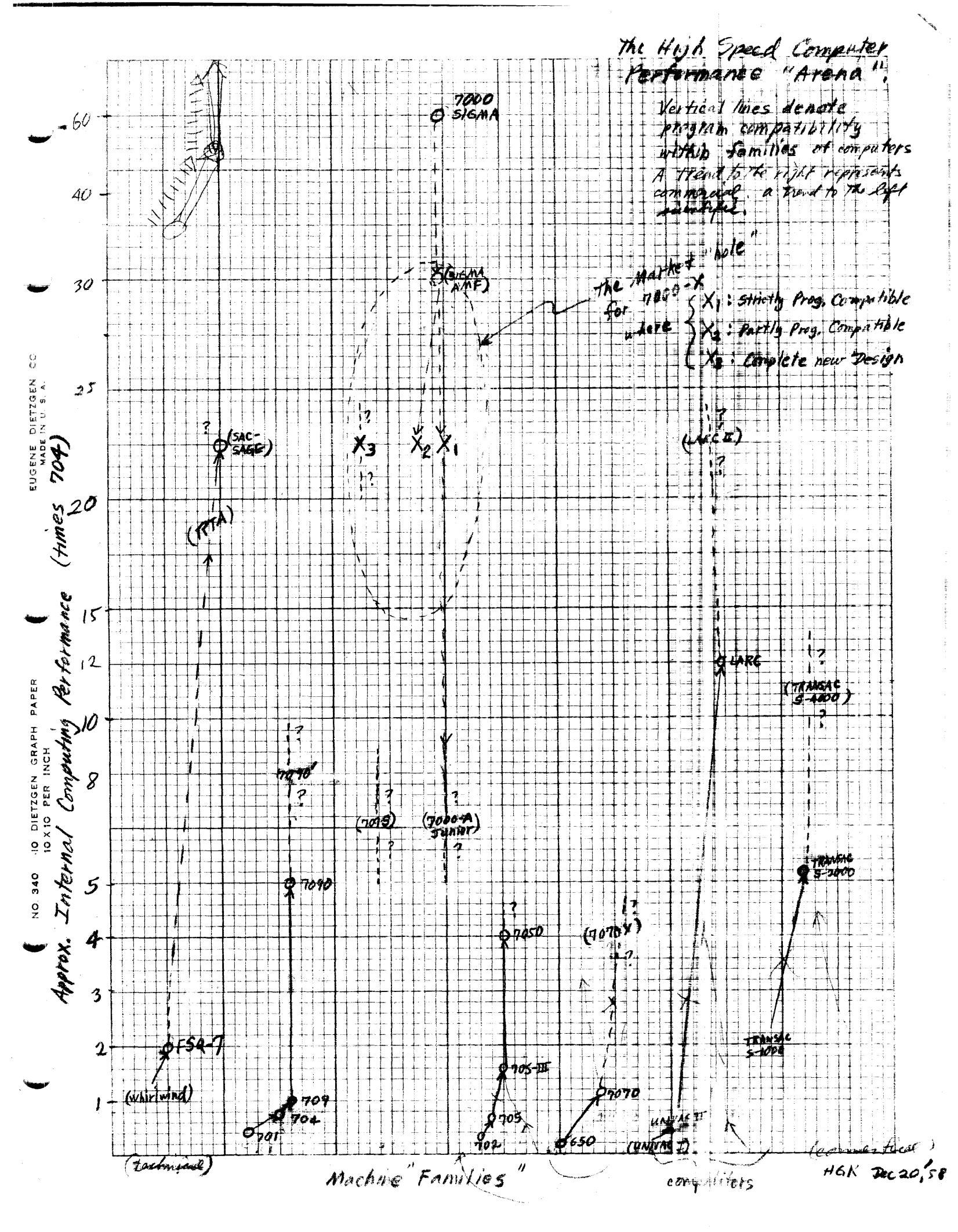
The High Speed Computer Performance "Arena".
Vertical lines denote program compatibility within families of computers
A trend to the right represents commercial, a trend to the left scientific.
The Market "hole" for 7000-X where
X1 : Strictly Prog. Compatible
X2 : Partly Prog. Compatible
X3 : Complete new Design
Approx. Internal Computing Performance (times 704)
60
40
30
25
20
15
12
10
8
5
4
3
2
1
7000 SIGMA
(SIGMA AMF)
(SAC-SAGE)
(RTA)
X3
X2
X1
(LARC II)
LARC
(TRANSAC S-4000)
7090'
(7095)
(7000-A Junior)
7090
7050
(7070X)
TRANSAC S-2000
TRANSAC S-1000
FSQ-7
(whirlwind)
709
704
701
705-III
705
702
7070
650
UNIVAC II
(UNIVAC I)
(technical)
(commercial)
Machine "Families"
compatibilities
HGK Dec 20, '58
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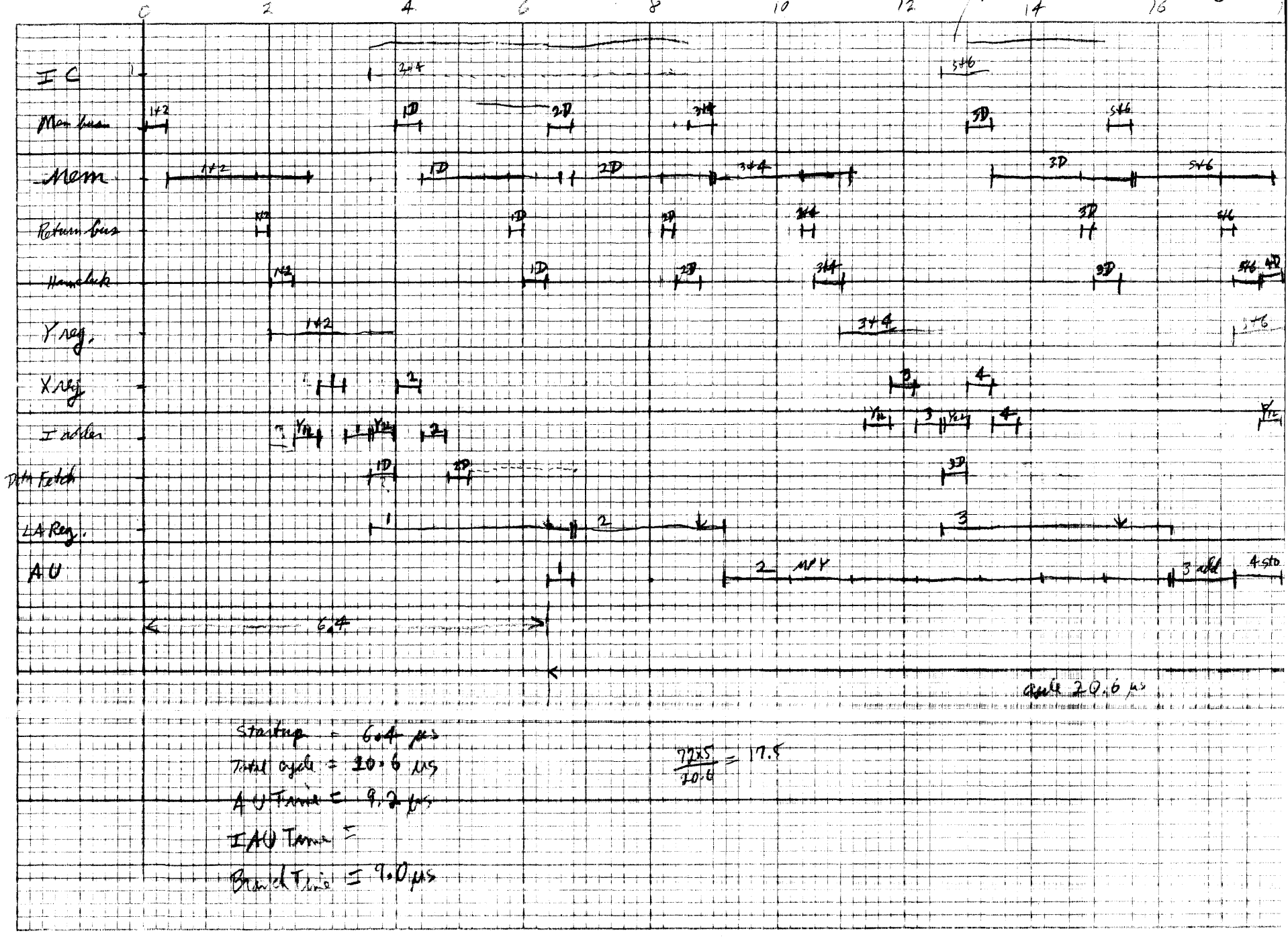

Startup = 6.4 μs
Total cycle = 20.6 μs
AU Time = 9.2 μs
IAU Time =
Branch Time = 9.0 μs

$\frac{77 \times 5}{20.6} = 17.5$

13 μs to be 20 ×

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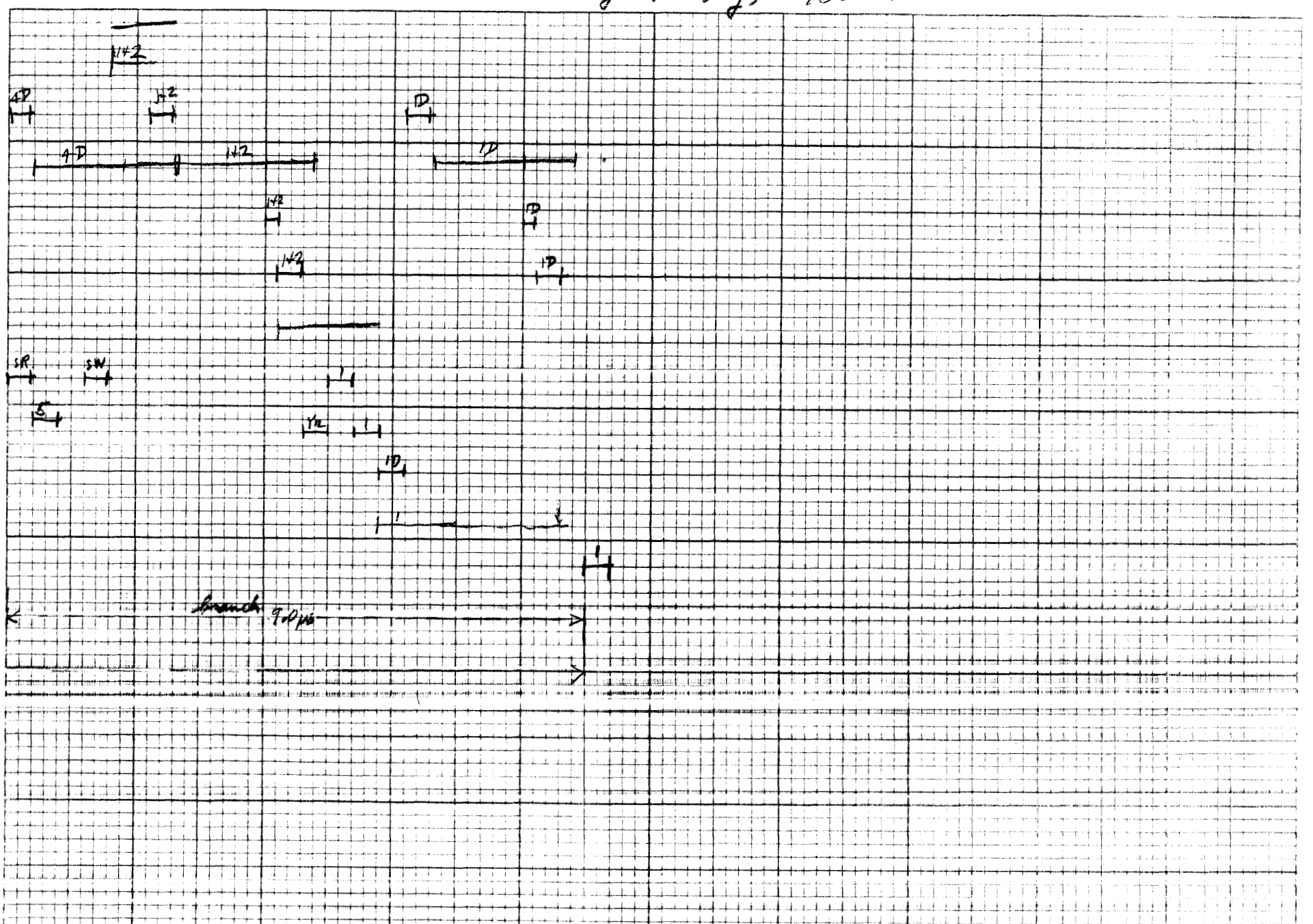
18

Single memory. 7000-X

(2)