

Meeting with Holleran, Blosk, Buchhaltz et al  
 Concerning I-Box simplification

Jan 13, 59

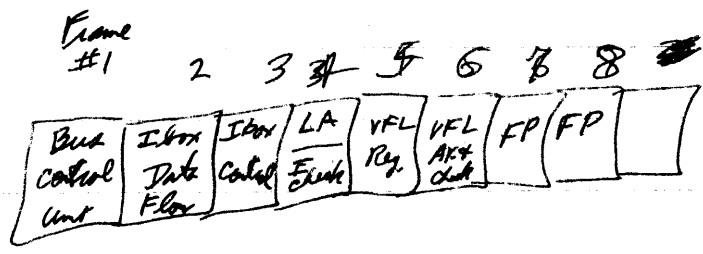
Control Card estimate was finished last week  
 average control stage estimate, extrapolated for  
 Sequencing  
 Encoders  
 Decoders  
 Or-circuits for gates.

90-out gates } also need latching  
 60-in gates }

Result: I-Box has exceeded frame, got 5 gates instead of 4,

Need to:  
 - simpler design - make double use - merge controls (general sequencers, etc)  
 - remove from machine some features eg, refill, rename

merging 3 stages to 1 seems to save only 1 stage's worth of cards.



80% packing  
 750 cards/gate  
 3000 cards for controls.

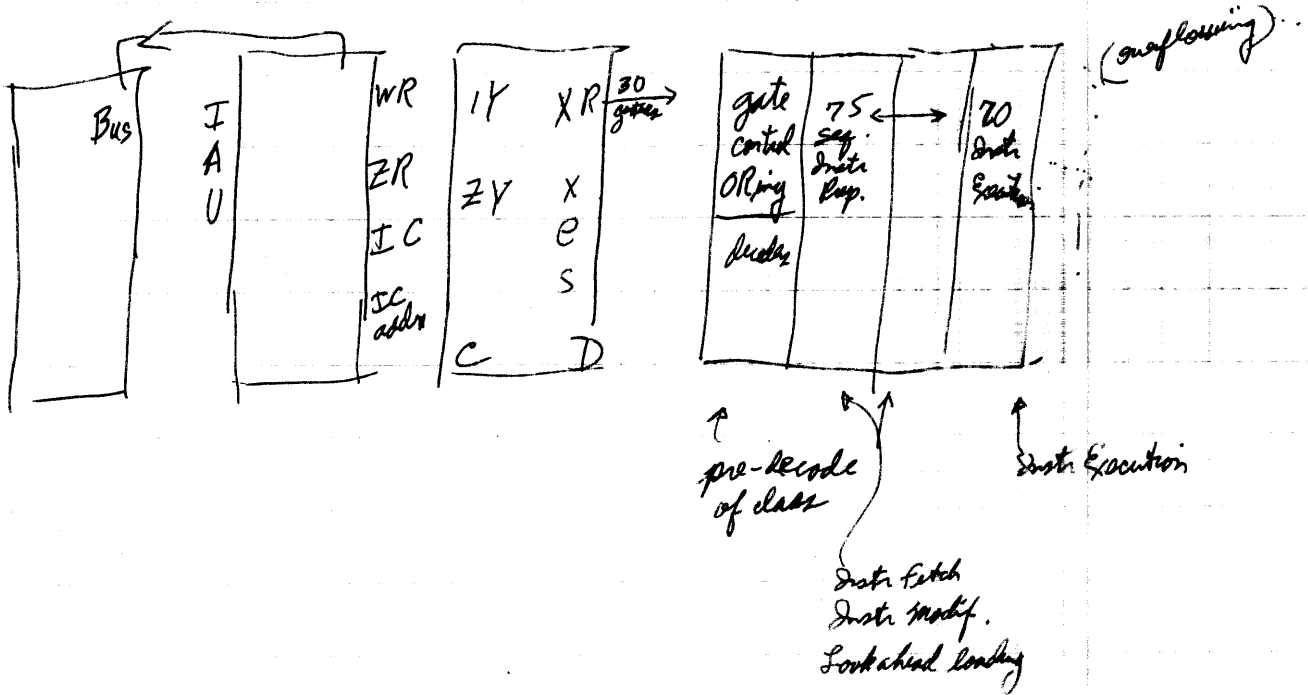
Refill:  
 - perhaps VFL could be.

e.g. Request sequencer ECC disk.  
 fetch  
 memory

refill address can be in 1 of 3 diff. registers. ... not all same.

18 cards for control  
 only 1 is sequencer  
 rest are in's & out's

IBox Slide



Power driver is equiv to  $2\frac{1}{2}$  logical levels.

Another example:

TMT, Swap from { Internal Regs (3 types)  
 External mem.  
 Instr. Mem.

case STIC is internal regs.---

TMT: Fetch, Fetch, Sts, Sts (can't overlap in addresses)