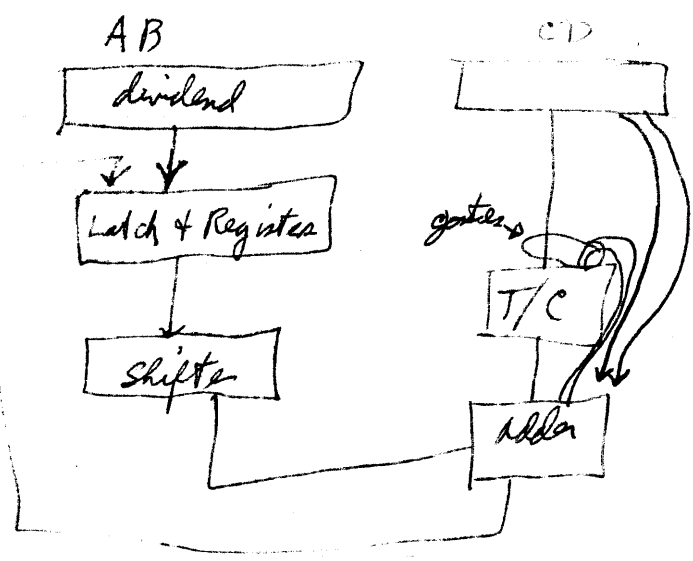


July 10, '58

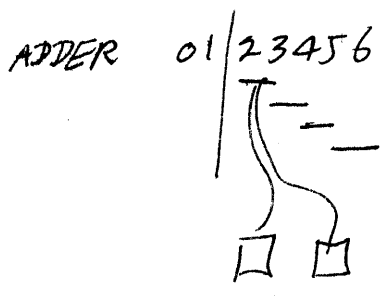
Fast Divide Scheme



no of shifts

00	101	110	111
100	101	110	111

generate 3 times the division
gate in CD twice (offset)
selection of True or comp.



form exclusive registers
go to left xor & right xor registers,
in latch.
- then enable ^{one set} ~~three~~ which correspond to amt of shift.
eg 45, 56
- picks quadrant
Then divide one of gates,

bring down from ~18 cycles to ~14 cycles.

no one shifts except 1st one 1, then 2's.