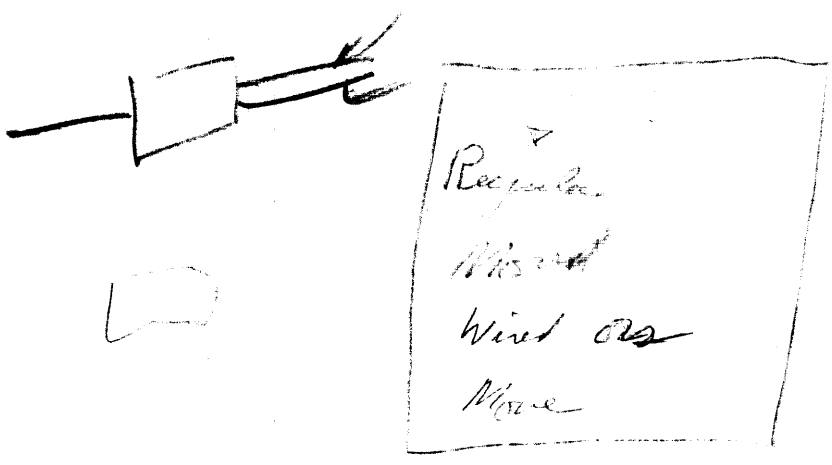


- 8
- 7 Choose
- 6 and 1
- 5 or 2
- 4 + 3
- 3 Delay or Invert. 4
- 2 ~~FE~~ 5

Source of Error or Dues SI So

1- Wired or  
 Inputs &  
 Outputs.



Set 0  
 set 1

$$(A \cup B) \cap \frac{A \cap B}{A \cup B}$$



$$A \cap B$$

AL

①

AL  
 CI  
 TL  
 OI

11214-2  
 A 24

HAR

Adv. Inc. type  
 (SF), -ALV/1/

~~1000~~ - (1000) ?  
 - ~~1000~~  
 - (1000)  
 - (1001)  
 - (1000)

(ADV) - (70)

Z1 OCT 777777, 777, 777

Z0 HTR 0,0,0

K1 CLA 0,1

PDX 0,4,0

TXI K2, 1, 4

K2 TRA 0,4

A1 SXD TEST1, 1

PAX 0,6

TXI K3, 2, -1

K3 CAL Z1

ANA\* 0,2,

TXI TEST1, 2, -1

TEST1 TXH K3+1, 2

SLW 0, 2

TXI K4, 2, -1

K4 COM

SLW 0, 2, 0

PXD 0, 4, 0

PDX 0, 1, 0

TRA K1

A 2 SXD TEST2, 1

PAX 0, 6

TXI K5, 2, -1

115 CLM

Spine loss

ORA\* 0, 2

TXI, TEST2, 2, -1

TEST2 TXH K5+1, 2

SLW 0, 2

TXI K6, 2, -1

K6 COM

SLW 0, 2, 0

PDX 0, 4, 0

PXD 0, 1, 0

TRA KI

A3 SXD TEST3, 2

PAX 0, 4

TXI K7, 2, -1

K7 CLM } (Intel.)

ERA\* 0, 2

TXI TEST3, 2 -1

TEST3 TXH K7+1, 2

SLW 0, 2

TXI K8, 2, -1

K8 COM

SLW 0, 2, 0

PDX 0, 4, 0

PXD 0, 1, 0

TRA KI

$$X = C \text{ of } T$$

~~A~~(1)

- 2
- 3
- 4
- 5
- 6
- 7
- 8
- 9
- 10
- 11
- 12
- 13
- 14
- 15

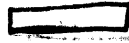
A1

C1  
T1  
T2

HTR

TYPE

A2



$X_{e4}$   
 $\Phi_{ir}$

C2

45  
50  
55  
60  
65  
70  
75  
80  
85  
90  
95  
100

A2

C2  
T2  
T2

LHR

$X_{in}$   
 $\Phi_{in}$

A3

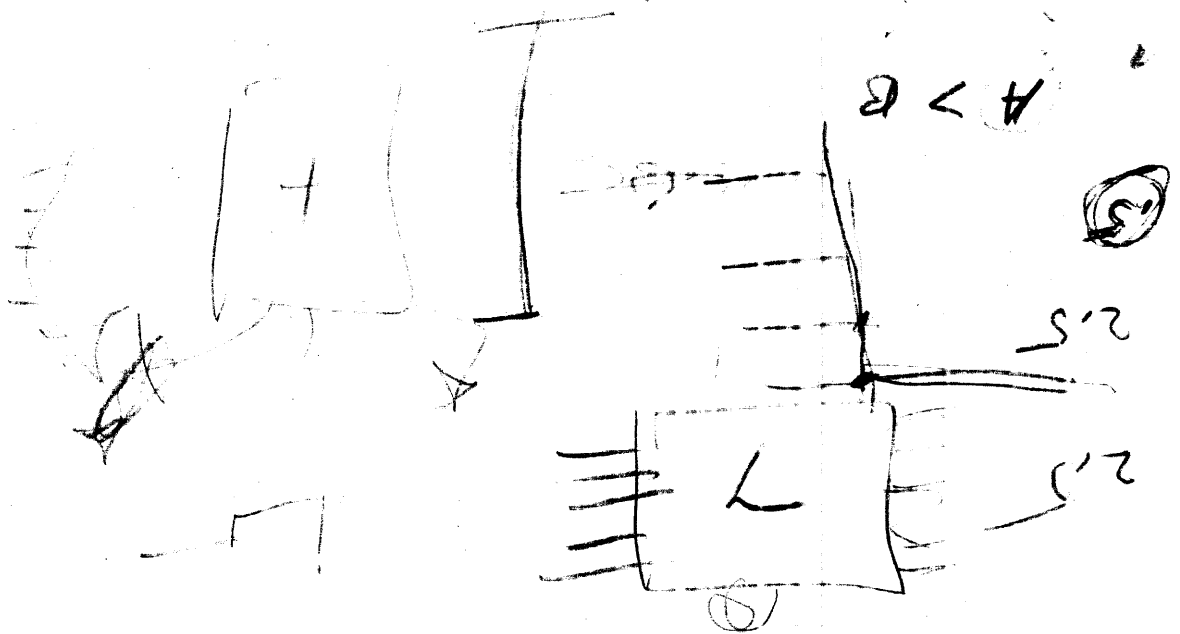
|||||

LEI

A > B

2.5

2.5



Z1  
Z0  
~~Z4~~

HTR 0,1,0  
HTR 0,0,0  
HTR

K2 And  
K3 Or

K1

CLA, 1, 0  
PAX, 2, 0  
PDX, 4, 0  
TRA, 2, 0

K4 +  
K5 Delay  
K6

K2

Adm