7 Chose

and 1

or 2

4 ⊕ 3

3 Delay or Invert. 4

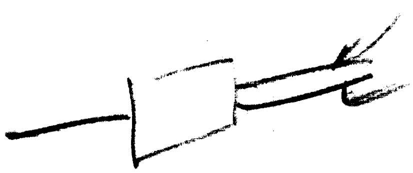
2 F.F. 5

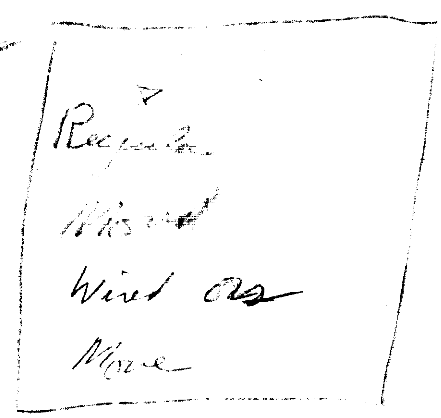
Source of Zero or One S1 S0

1 Wired or 6

Inputs.

Outputs.





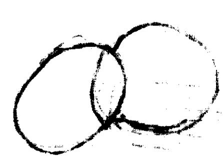
Set 0

Set 1

$\overline{A \oplus B}$

$(A \cup B) \cap \overline{(A \cap B)}$

$\overline{A} \cup \overline{B}$



$A \cap B$

A1

A L HAR (F), A L

O i

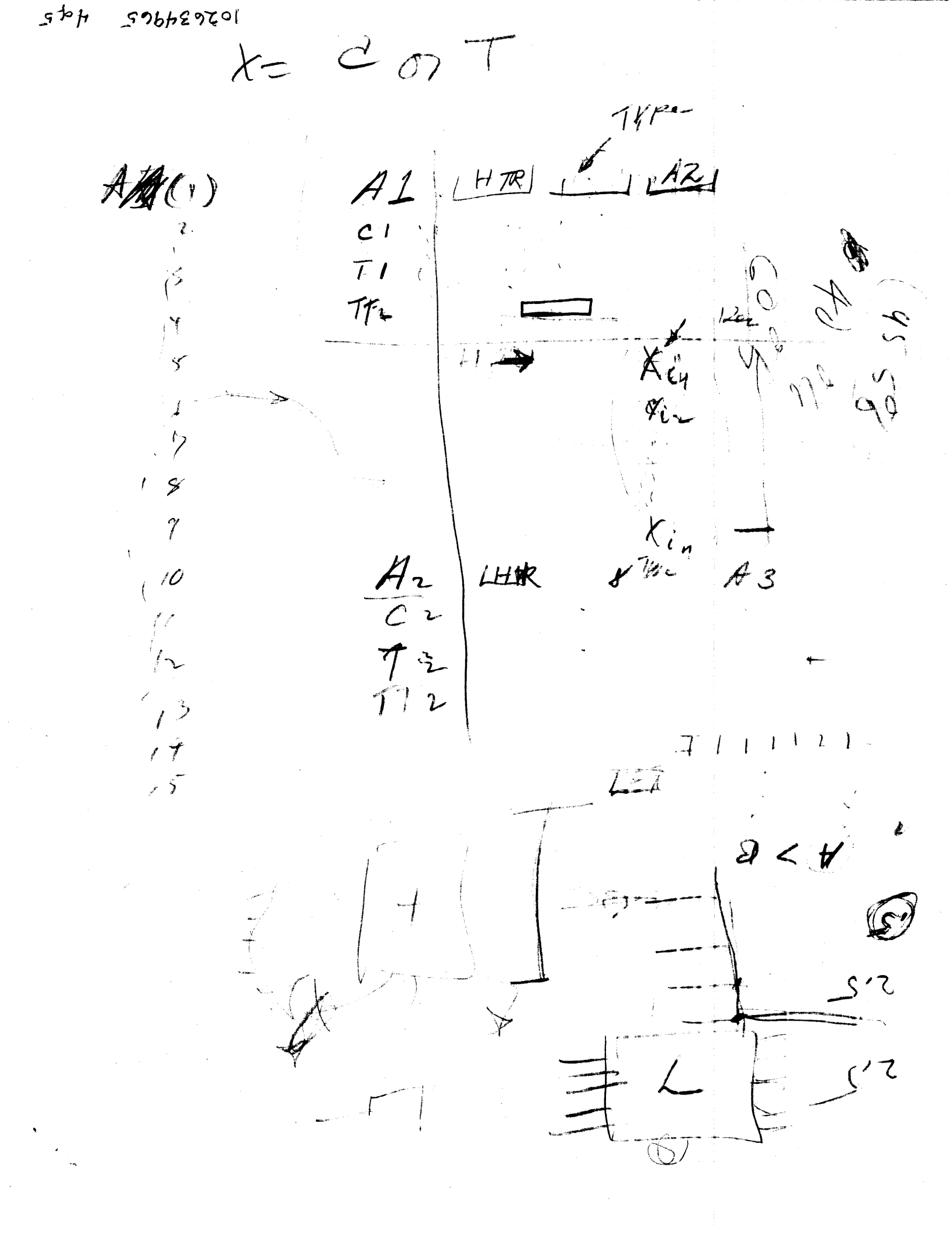
— (1001)

— (1000)

(ADV) — (7…)

```
        Z1   OCT  777777,777777
        Z0   HTR  0,0,0
K1      CLA  0,1
        PDX  0,4,
        TXI  K2,1(4)
K2      TRA  0,4
A1      SXD  TEST1,1
        PAX  0,6
        TXI  K3,2,-1
K3      CAL  Z1
        ANA* 0,2,
        TXI  TEST1,2,-1
TEST1   TXH  K3+1,2,
        SLW  0,2
        TXI  K4,2,-1
K4      COM
        SLW  0,2,0
        PXD  0,4,0
        PDX  0,1,-0
        TRA  K1
A2      SXD  TEST2,1
        PAX  0,6
        TXI  K5,2,-1
K5      CLM
```

```
        ORA* 0,2
        TXI, TEST2, 2, -1
TEST2   TXH  K5+1, 2
        SLW  0, 2
        TXI  K6, 2, -1
K6      COM
        SLW  0, 2, 1
        PDX  0, 4, 0
        PXD  0, 1, 0
        TRA  K1
A3      SXD  TEST3, 2
        PAX  0, 6
        TXI  K7, 2, -1
K7      CLM
        ERA* 0, 2
        TXI  TEST3, 2, -1
TEST3   TXH  K7+1, 2
        SLW  0, 2
        TXI  K8, 2, -1
K8      COM
        SLW  0, 2, 1
        PDX  0, 4, 0
        PXD  0, 1, 0
        TRA  K1
```


A1
C1
T1
TF2
HTR
A2
A3
C2
T2
TF2
LHTR
LED

```
Z1      HTR   0,1,0
Z0      HTR   0,0,0
L4      HTR

K1      CLA , 1 , 0
        PAX , 2 , 0
        PDX , 4 , 0
        TRA , 2 , 0

K2      Add
```

K2 And

K5 Or

K4 +

K5 Data

K6