Digicomp Research Corporation was founded in 1975 by several highly qualified engineers, all of whom are interested in astrology. The idea of creating the DR-70 astrology computer was the spark which set our company moving. We have expanded greatly since the DR-70 hit the market 3 years ago. Although many of our employees are involved in the DR-70 project, we have broadened our research and development activities into other areas of the electronic and computer science field. Among the most important are the following:

1. Development of "Trusted Computer Systems".

As any programmer knows, the development of absolutely reliable software is a very difficult and expensive process. (Digicomp spent several years designing software for the DR-70). No one wants a computer system that he cannot trust. This problem is really an acute one with regard to military systems. The military requires "secure" computer systems whose data cannot be tampered with by any unauthorized person. Digicomp has recently been awarded a million dollar contract by the Air Force to study and evaluate design techniques for developing reliable software. This will have a particular application to the design of secure operating systems and data bases.
2. Microcomputer Products.

Digicomp is active in designing and manufacturing "board-level" components for general purpose $\mathrm{S}-100$ microcomputers. Among these products are Pascal-100, a plug-in central processing unit with a unique dual processor design, and the MFIO Multi-Function Input/Output controller. It utilizes the latest in large scale integrated circuitry to provide a large number of functions on a single board. Digicomp also packages these products into complete microcomputer systems that are typically used in small businesses and as "personal computers".
3. Use of modern computer technology to speed up radar signal processing algorithms. Computer systems are now available that can process many different numbers simultaneously (e.g., perform arithmatic operations like add, subtract, multiply and divide) as opposed to previous "sequential" processing of one number at a time. These computers are called "parallel processing" or "array processing" systems. The analyzing of radar data consists of processing a very large quantity of numbers. The sequential processing of these signals takes an incredibly long time and thus limits their usage to only laboratory studies. Digicomp has been hired by the USAF to speed up these algorithms by using array processing techniques. The objective is to achieve real time speeds for tactical operations.
4. Air Force computer specification for the 1990 's. The US Army has published the specifications for computer architecture that is expected to serve its needs in the 90 's. Because the USAF's needs are different than those of the Army, Digicomp will be studying the Army's specifications in light of the USAF's requirements.

The benefits to the astrology community of Digicomp's expansion are two-fold. The first is that Digicomp will continue to be on top of changes in the computer field and will apply the knowledge gained to the DR-70. Secondly, because Digicomp Research is not a one-product company, we will be able to support the DR-70 for many years to come.

The DR-70 is a stand-alone desktop microcomputer whose keyboard, display and firmware are entirely dedicated to astrological functions. This portable computer is manufactured by Digicomp Research Corporation, and is scheduled for delivery in March of this year. . The heart of the DR-70 is a high speed 8-bit microprocessor CPU. The programs for this unit are resident on 28 k bytes of metal-mask ROM, which is accompanied by $\mathrm{l} k$ bytes RAM scratch pad and user storage area. For the sake of speed and compactness, the majority of the programming was done in assembly language. Digicomp Research developed its own high speed floating point routines, which effectively reduced the planetary computation time. The DR-70 uses several different interrupt service subroutines that are conditional with respect to a keyboard status indicator. This allows the microprocessor to alternately refresh the LED display panel, parse the keyboard, continue projected calculations, and transfer information to the printer. By a complex parsing/interrupt interface, the DR-70 software is able to anticipate the key sequence in advance of the user, thus beginning the calculation requested before the user has finished entering it from the keyboard. This noticeably reduces turnaround time on most entries. Of course, this technique has a cross-check to the full keyboard entry, so that the user need never worry about the computer misreading the key sequence.

From the astrological point of view, the DR-70 keyboard and display have been designed to present results in an astrological framework by using the familiar signs and symbols of astrology wherever they are needed to identify values. If the position of the natal sun is desired, the user simply presses NATAL 0; for the progressed sun, the entry becomes SECondary 0 .

For persons familiar with computers and computer science, the keyboard offers a tremendous flexibility in accessing the available data. Since the history of the previous keyboard entries is maintained as long as the machine is on, the experienced user may minimize all key sequences so that they implicitly incorporate keys set in earlier sequences. On the other hand, for the astrologer who is unfamiliar with flags, bits, and bytes, all the internal information is easily availible to him through straight-forward, common sense entries. In either case, the display panel is engineered to indicate the status of all internal banks and registers at the touch of a button.

Because of the wide range of uses, Digicomp Research has spent considerable time developing the instruction manual. This has been written in a self-teaching style, with the more complex possibilities being presented a step at a time. The astrological and computer terminologies are defined as introduced, and the techniques have been illustrated with example horoscopes. In the back of the manual, there are two reference sections, one for common key sequences, and one for the more complex entries.

The DR-70 weighs six pounds, is $10^{\prime \prime} \times 13^{\frac{1}{2}} \times 5 \frac{1}{2 \prime}$, and requires 115 Vac $\pm 10 \mathrm{v}, 60 \mathrm{~Hz}$ connected through the supplied power cord. All internal components are powered by a single 5 volt supply and cooled by a small fan mounted in the rear of the case. It is shipped inside a specially designed luggage case, which can be adjusted to carry the printer as well.

The companion printer for the DR-70 is scheduled for production in July. This unit interfaces with the microcomputer through a single cable, and has its own power supply. Its functions range from sorting and listing all the various chart information to simply generating hard copy of all keyboard entries. Furthemor information on the printer and its capabilities are available from Digicomp Research Corp., Ithaca, New York.

## Dear DR-70 Owner,

This newsletter contains important information regarding the service of your DR-70 and printer. I have a number of different points to discuss with you so settle down with a cup of tea or coffee and give me about 15 minutes of your time.

1. When you call Digicomp please use 607-273-5900. We also have an 8779 number which was advertized for a long time but it is often busy now with our outgoing calls.
2. Whenever you call Digicomp with machine problems or questions about key sequences it will save time and money if you set up your computer by the phone before calling.
3. If you are having mechanical problems with your DR-70 it is best to call us before sending in your computer. Often the problem is not difficult to repair and can be done over the phone or by you or another local person. We will be able to determine this when you call.
4. When you return your machine, enclose a note which describes the problem you are having. Include return address and phone number so we can contact you if necessary If you have print-outs which illustrate your problem, please enclose these with your package.
5. When you transport the DR-70 in its carrying case lay it on the seat of your car or in the trunk in its operating position. This will help prevent memory chips from jarring loose. To help you tell which side of your case is up you might mark it with some tape.
6. When machines come in to us for repair (and not under warrantee) our charges are as follows (as of 8/12/81):

- $\$ 75$ minimum
- $\$ 25, \$ 35$, or $\$ 50$ per hour labor depending on the level of engineer required.
- parts as needed

Most repairs are in the $\$ 75-\$ 125$ range but we are finding that some are well over $\$ 100$ and some over $\$ 200$. As your'machine ages you should consider protecting yourself against costly repairs by obtaining one of two service contracts that we offer:
a. $\$ 35$ per year and $\$ 95$ maximum per repair.
b. $\$ 180$ per year for all repairs

Neither of these include shipping but both cover all parts and labor as specified. The contracts are only for the $D R-70$. All repairs are guaranteed for 2 months. If
a DR-70 is returned to you in non-operative condition, call us. We will cover return postage in this case. Sometimes machines develop problems during shippment.
7. We have established a relationship with several computer repair centers in the US. If you prefer, you may use them. They have our schematics which make it possible for them to repair DR-70s. However, they are independent of us in all other ways.

Chicago: Kaltronics
3665 Woodhead Dr.
Northbrooke, Ill. 60062 312-291-1220 attn. Gary Shideler
Los Angeles: Computer Service Center
4226
$\begin{array}{ll}4226 \text { West l64th St. } \\ \text { Lawndale, CA } 90260 & 213-557-1770 \text { days }\end{array}$


Dr. B. K. Roy
1785 N. Partridge
Anaheim, CA 92806
Va. Beach: Starborn Electronics
3707 Virginia Beach Blvd.
Suite 206
Va. Beach, VA 23452 804-486-5825 attn. Victor Winborn
8. I am enclosing a small repair manual that will be of some help to many DR-70 owners. There are some types of problems that you will be able to handle yourself. It would still be wise to contact Digicomp before trying any of them yourself, however, because of the warrantee restrictions.
9. Please do not over oil the printer. Usually one drop of oil on the metal bar and a small amount of petroleum jelly in the groove (see manual) is all that is required. Also, it would be better if you did not re-ink the ribbons as it can make a mess in the printer.

Now for a couple of MINI - TRICKS: see Key Sepwence seetw

1. If you press $Q+$ HSE 1 ENTER you will get house positions in degrees and decimal fraction.
If you press 60B $0+$ HSE 1 ENTER you will get house positions in degrees, minutes,

This also applies to planets:


Also, you can cycle through houses and planets using the ENTER key.

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- \$75 minimum
- $\$ 25$, $\$ 35$, or $\$ 50$ per hour labor depending on the level of engineer required.
- parts as needed

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Chicago: Kaltronics 3665 Woodhead Dr. Northbrooke, Ill. 60062 312-291-1220
attn. Gary Shideler
Los Angeles: Computer Service Center 4226 West 164th St. Lawndale, CA 90260

213-557-1770 days 213-371-9662 nights
attǹ. Roger Lewis
Dr. B. K. Roy 1785 N. Partridge Anaheim, CA 92806

Va. Beach: Starborn Electronics 3707 Virginia Beach Blvd. Suite 206
Va. Beach, VA 23452 804-486-5825
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Now for a couple of MINI - TRICKS:

1. If you press $0+$ HSE 1 ENTER
you will get house positions in degrees and decimal fraction.

If you press $60 B 0+$ HSE 1 ENTER you will get house positions in degrees, minutes, and seconds.

This also applies to planets:

$$
\begin{aligned}
& 0+0 \text { ENTER or/ } \\
& 60 B \quad 0+0 \text { ENTER }
\end{aligned}
$$

Also, you can cycle through houses and planets using the ENTER key.
. Declinations can NOT be calculated from sidereal positions. The sidereal positions are treated as if they were tropical by the DR-70. For example, the Sun at $0^{\circ}$ Aries sidereal will give a declination of $0^{\circ}$ which would be correct for tropical Aries only. To calculate the declinations of sidereal planets, first convert to tropical and then calculate them. Declinations of the planets should not change because the longitude: is sidereal rather than tropical.
3. When entering a GMT what do you do when you subtract for DST or for time zones east of Greenwich and you arrive at a time which is on the previous day? Use CHS and enter a negative time! You won't have to change the date this way.

For example:
Date 3.21.1981
Time 3 AM LMT
Lat 20 N
Long 60E

Recommended tools for $D R-70$ repair
(1) If hex driver or Alan wrench
(2) small flat screwdriver
(3) chip extractors
(4) soldering iron and solder
(5) solder wick or solder removal tool
(5) 1/4" socket wrench or spanner
(7) wire cutters
(8) Freon solvent
(9) isopropal alcohol

## Simple DR-70 Repair Techniques

## NOTE - OPENING THE MACHINE WILL VOID YOUR WARRANTY UNLESS OTHERWISE INSTRUCTED BY DIGICOMP.

## A. Opening the machine

1. The DR-70 is sensitive to static electricity, so when working on the machine do not wear sweaters, nylon shirts, rubber-soled shoes or material that creates static charges.
2. A \#6 hex driver is used to undo the 6 stress bolts located around the aluminum valence and fan. (Note: 1 bolt located under warning sticker).
3. Tape over name plate to avoid scratching.
4. Lift top cover up and lean it towards the back - it should be stabilized in this position.
B. Techniques of repair for intermittant failures or lost data (but machine responds to reset).
5. Check operators manual for proper key sequence.
6. Follow opening instructions (\#A). Once open, firmly press each chip to reseat their contacts in the sockets.
7. Replace cover and observe behavior. If intermittant behavior still occurs, repeat procedure \#1.
8. If machine continues to behave intermittantly, purchase some spray contact cleaner (a freon solvent which leaves no residue or oil). Again open the machine. With chip extractors or a small screwdriver, lift the chips partially out of their sockets. Spray the contact cleaner on the chips legs and reseat the chips by pushing them in. Check that none of the chips legs are bent or out of the socket. (Note: if a chip should accidently come out of its' socket, it must be replaced in the same direction).
9. Replace cover and observe machines behavior.
C. Replacement of LEDS
10. Disconnect 50 pin card edge connector from keyboard - avoid stressing cable. The epoxy seal can be broken with a small screwdriver or knife.
11. Lay top upside down. Four sets of $\boldsymbol{l}^{\prime \prime}$ nuts should now be seen on the back side of the display PC board. Remove the nuts and washers. IMPORTANT: Note the number of, and order of the spacing washers. (See
12. Turn top cover over. Mylar display screen may now be lifted off. Locate defective LED.
13. Now, unsolder old LED and discard. Noting that the direction of the notch in LED lens aligns with the other LEDS (towards the left, looking from the top). Solder LED in FLUSH to the PC board. Do not hold solder iron on the LED lead for longer than 2 seconds. Overheating will cause internal damage to the LED.
14. Screw the display screen and PC board back together with the same exact number of washers (see diagram below). If the LEDS get too close to the screen, heat from LED will cause the mylar to melt.

15. Replace ribbon cable, set top down on bottom in place. Turn machine on, then verify that new LED is working. Turn machine off, then close the machine (see "Sealing up the machine").
D. Replacement of keyboard switches
16. Disconnect 50 pin card edge connector from keyboard - avoid stressing the ribbon cable. (See Cl).
17. Remove keytop on suspect key. Usually a small screwdriver working from the bottom of keytop, pulling up, will do.
18. After locating key on backside of PC board, remove the solder from the two contact holes. Free key by pulling from top. Make sure both the contact holes and the mounting guide holes (2) are free of obstructions.
19. Gently ease new key switch into hole and solder. It is important to note the following:
(i) the switch contacts should be centered in PC board holes and the switch must be seated flat to prevent the key tops from rubbing against one another.
(ii) Hold solder iron on switch leads for no longer than 2 seconds as the internal mechanism can overheat.
20. Replace ribbon cable and keytop. Verify proper functioning of keyswitch. Seal machine, if working.
E. Procedure if füse blows repeatedly
21. Check that fuse is $3 / 4$ amp AGC fuse.
22. Open machine (See \#A). Carefully look for loose shards in $+5 v$ circuitry on motherboard. Check l20v wiring for loose or shorting connections. Check operation.
23. Cut $120 v$ wire to fan. Check operation. If it works-replace fan (all parts available through Digicomp). If not - reconnect wire.
24. Cut $120 v$ wire to power supply. Check operation by observing fan.
(i) If fan runs, go to \#5.
(ii) If fan doesn't run - disconnect line filter and bypass it. Reconnect power supply and check operation. Should it work, replace filter. If it doesn't, recheck wiring for shorts.
25. Cut yellow wire to 7805 regulator. Check operation of fan. If now working, replace 7805. If not, check power supply PC board and motherboard for shards in $+5 v$ power busses.
26. If the machine continues to blow fuses, call Digicomp at (607) 273-5900.

## F. Sealing machines

## PLEASE FOLLOW THIS CHECKLIST

1. Make sure power supply, motherboard, keyboard, and display are all screwed down tight.
2. Check power supply output. It should be $+5.05 v$. If not, adjust pot.
3. Check master clock (only if you have a calibrated oscilloscope). It should have a period of 1.25 us. The clock pot is located on the power supply side of the CPU module.
4. Vacuum machine thoroughly to remove any loose debris. If inside is dusty, wipe it clean using isopropal alcohol.
5. While the machine is open an easy and helpful maintenance procedure is to push down on all the chips to ensure that they are seated firmly in their sockets.
6. Fit top and bottom together. Pick up and shake machine, meanwhile listening to hear loose material inside enclosure.
7. With cover still on, check for proper functioning. Then insert and tighten stress bolts.
1.0 The Model 7040 printers are designed to require a minimum of maintenance and service. This section provides instructions and procedures for both cleaning and maintenance of the printer as well as a troubleshooting quide to aid in problem analysis.
1.1 Required Tools and Supplies

The following items are recommended for printer maintenance and major repairs. Common hand tools are also required.

1. Volt-Ohm Meter
2. Feeler Gauges
3. Grease (IBM \#23 or Lubriplate \#70)
4. Isopropyl alcohol
5. Cleaning Rags
6. Cotton Swabs

### 2.0 Maintenance

The frequency at which normal maintenance (cleaning, lubrication and inspection) should be performed when the printer is installed in a clean office environnent should normally be one year. However, if the printer is subjected to heavy usage and/or a dirty environment, the frequency should be increased as required.

### 2.1 Cleaning

A. Clean printer thoroughly using alcohol, rags and cotton swabs to remove hardened grease, ink and dirt.
B. Rotate platen and clean with an alcohol soaked rag.
C. Remove head and clean head and head shaft with alcohol.
D. Reassemble head to printer.
E. Apply a sinall amount of recommended grease to head shaft.

### 2.2 Inspection

A. Check for print wire movement on all seven solenoids by depressing wire end and noting return to rest position.
B. Check for easy movement of the head assembly on the print head assembly shaft.
C. Inspect the ribbon reverse mechanism for proper movernent. Manually operate the ribbon feed pawl back and forth to check operation. Be sure that both springs are securely in place and of equal tension.
D. Inspect the printer for any loose or missing screws.
E. Check for excessive vertical movement of print head drive cam. Usual cause is a loose paper feed motor. Rotate the drive can toward the platen and listen for an audible click of the home micro switch.
(1) The position of the switch arm roller at the click should be half-way up/down the high dwell ramp of the can.
(2) The switch activating arm should be straight with no kinks.
F. Check the paper feed cam for tightness on motor shaft (looseness may be a result of a bad cam or improper motor shaft configuration).

G．Rotate the paper feed cam clockwise（toward platen）and check for smooth operation．Binding indicated bad fit between cam and pawl arm．Listen to the paper feed micro switch for an audible click． （1）The position of the switch arm roller at the click should be half－way up／down the high dwell ramp of the cam．
（2）The switch activating arm should be straight with no kinks．
H．Check that right end of print platen is not loose with regard to platen drum．
I．Check that the platen spins freely when paper release lever is de－ pressed and ratchet pawl is disengaged．
J．Check that the plastic paper guide strip edge touches the platen drum over its entire length．
K．Check that the plastic paper guide strip is secured to the extrusion mounting surface．
L．Check that the print head moves freely along its shaft and within the drive cam groove while turning the drive cam toward the platen．
14．Place the ribbon in place and check that it does riot snag throughout its path．Check ribbon spool drag．
N．Depress paper release lever and check that bar and feed roll leave the print platen．
0 ．Check that two ply form may be loaded when paper release lever is depressed．
P．Check that paper guides are not loose on main frame extrusion and that they may be moved with moderate pressure for adjustment．
Q．Inside guide edges should be flush with inside edges of sides of supply mechanism．

## 2．3 Lubrication

Recommended lubricants are IBM \＃23 or Lubriplate $⿰ ⿰ 三 丨 ⿰ 丨 三 ⿻ ⿻ 一 𠃋 十 一 八$ ．After cleaning， the following areas should require lubrication：
a．Head shaft
b．Groove in main drive cam
c．Guide in extrusion in which the＂tail＂of the print head travels． Only a small amount of lubricant，enough to create a film，is needed in each location．
The following areas may also require lubrication，if the area has been wiped free of lubricant：
a．Paper feed cam and pawl
b．Ribbon feed pawl
c．Platen detent teeth
d．Platen ratchet teeth
e．Pressure roll release lever

## 3．0 Repair and Adjustments

## 3．1 Troubleshooting Guide

The Mechanism Troubleshooting Table should provide a means of determining the cause of any malfunction and the proper corrective action．

3．2 Repair Techniques

### 3.2.1 Head Replacement and Adjustments

Remove the old head by removing the two screws which hold the head shaft in place and then pulling the head shaft and head up out of the mechanism. To free the head cable the cable tie must be cut.
Remove shaft from old head and slide it into the bearings on the replacement head. The head assembly should move freely along the shaft. To check for free movement hold the head assembly with the shaft vertical and release the shaft. It should drop through the head assembly. If it does not move freely, hold the head assembly on one end of the shaft and tap the other end firmly on a hard surface. This will cause the head assembly bushings to seat perpendicular to the shaft. The replacement head can now be installed in the printer in the reverse manner of its removal. When tightening the screws in the head shaft, hold the shaft forward to minimize the distance between it and the platen. Insure that the drive pin on the bottoll of the head mates with the groove in the drive cam and that the "tail" on the head is placed in the guide slot on the extrusion. Lubricate according to paragraph 2.3 prior to operation.

### 3.2.2 Switch Replacement

3.2.2.1 Line Feed Switch (FPF Mechanism Only)

To replace the line feed switch it is first necessary to remove the left side frame. This is done by first removing the twirler from the platen. Then remove the three screws which secure the side frame to the main extrusion as well as the screw in the left side of the head shaft. Once these screws are removed the left side frame can be pulled away from the printer. The line feed switch can now be removed from the side frame by lifting it off its mounting tabs. The replacement switch is then pressed onto those tabs and a piece of adhesive-backed foam is placed on the switch where it contacts the main extrusion. The side frame can now be replaced taking care to align the platen pressure roll support bracket, head shaft and main drive cam to their proper positions.

### 3.2.2.2 Print Switch

To replace the print switch it is first necessary to remove the right side frame. This is done by removing the three screws that secure the side frame to the main extrusion as well as removing the screw in the right hand side of the head shaft. The side frame can now be removed, taking care to disengage the motor pulley from the belt. Holding the detent lever down (forward) will aid in the removal of the frame. The print switch can now be removed by lifting it off its mounting tabs. The replacement switch is then pressed onto these tabs and adhesivebacked foam is applied to the switch where it contacts the main extrusion. The printer is then reassembled taking care to align the main drive can and platen properly. During reassembly the detent lever must be held down to avoid interference with the ratchet teeth on the platen, and the motor pulley must have the belt placed back on it.

### 3.2.3 Belt Tension

Drive belt tension is adjusted by loosening the two motor mounting screws and moving the motor forwards or backwards to decrease or increase the belt tension. The belt tension should be set so that when the head is prevented from moving, the motor stalls and the belt does not $\operatorname{cog}$ over the motor pulley. Excessive belt tension will cause the drive cam to bind and the motor to stall. Too little belt tension will result in excessive variation in character widths.
3.2.4 Head to Platen Air Gap

If the printhead has been replaced due to solenoid failure or if the printer has been disassembled, the printhead to platen air gap should be checked prior to using the printer. Improper air gap can result in damage to the printhead. The printhead to platen air gap may be checked by using feeler gauges. Prior to checking the air gap it is necessary to remove the paper and ribbon from the printer.
The head to platen air gap should be . $012^{\prime \prime}$ to . $015^{\prime \prime}$. If the air gap is out of the allowable range, some adjustment can be made by loosening the screws in the head shaft and pushing it in the desired direction. If this does not yield sufficient change then it is necessary to loosen the side frames and move them upward or downward in relation to the main extrusion

MECHANISM TROUBLESHOOTING GUIDE

| SYMPTOM | POSSIBLE CAUSE | CORRECTIVE ACTION |
| :---: | :---: | :---: |
| 1. Print motor starts but doesn't stop. No printing occurs. | 1. Broken print switch <br> 2. Broken switch wire <br> 3. Misadjusted print switch | 1. Replace switch <br> 2. Check all wires on print switch for electrical continuity <br> 3. Verify switch transfers when roller on switch actuator transfers between high and low positions on the cam pulley. |
| 2. Once started, the line feed motor never stops | 1. Broken line feed switch <br> 2. Broken switch wire <br> 3. Misadjusted line feed switch | 1. Replace switch <br> 2. Check all wires for electrical continuity <br> 3. Verify switch transfers when roller on switch actuator transfers between high and low positions on switch eccentric. |
| ```3. Print Motor stalls``` | 1. Excessive Belt tension <br> 2. Bad crimp on one or more A.C. motor wires <br> 3. Bad A.C. motor | 1. Adjust belt tension <br> 2. Visually verify crimps are good <br> 3. Replace motor |
| 4. Occasional missed line feed | 1. Bad line feed switch <br> 2. Misadjusted line feed switch | 1. Replace switch <br> 2. Verify switch roller rides on lower surface of paper feed eccentric when in the home position, and that switch transfers when switch roller moves from low surface of the paper feed eccentric |
| 5. Uneven line feeds | 1. Paper slippage <br> 2. Metal wear plate broken off line feed pawl <br> 3. Platen not detenting properly | 1. Clean platen and/or replace pressure roll springs <br> 2. Replace pawl <br> 3. Check for defective detent mechanism. |

MECHANISHI TROUBLESHOOTING GUIDE (Continued)

| SYMPTOM | POSSIBLE CAUSE | CORRECTIVE ACTION |
| :---: | :---: | :---: |
| 6. Line feed motor stalls | 1. Bad A.C. motor <br> 2. Line feed pawl binds on eccentric <br> 3. Bad wire to pin connections on A.C. motor wires <br> 4. Platen won't turn or turns with difficulty | 1. Replace A.C. motor <br> 2. Replace pawl or eccentric <br> 3. Check crimp on wire <br> 4. Inspect detent mechanism for source of jam and replace or repair as required |
| 7. Missing dots | 1. Ribbon not properly installed <br> 2. Bad print solenoid <br> 3. Head to platen gap excessive | 1. Instail ribbon correctly <br> 2. Replace head <br> 3. Verify head to platen gap is $.015^{\prime \prime} \pm .003^{\prime \prime}$ |
| 8. Excessive variation in character size | 1. Belt tension too loose <br> 2. Start to print distance (distance from lst dot position to inside of side frame) is out of tolerance (.965 $\left.\pm .060^{\prime \prime}\right)$ <br> 3. Head binding on shaft | 1. Tighten belt tension <br> 2. Adjust print switch to correct start to print location. <br> 3. Align head bearings on shaft. Lubricate shaft. |


| ITEM NO. | PART NO. | DESCRIPTION QU | QUANTITY |
| :---: | :---: | :---: | :---: |
| 1. | 101411-004 | ASSY-A.C. MOTOR* | 1 |
| 2. | 100576 | TWIRLER | 1 |
| 3. | 101528 | BRACKET-RIBBON TENSION | 1 |
| 4. | 101225 | SPACER-RIBBON POST | 2 |
| 5. | 102378 | SPRING-RIBBON TENSION | 1 |
| 6. | 101383 | ASSY-FRAME LEFT SIDE | 1 |
| 7. | 100625 | ASSY-RIBBON POST | 2 |
| 8. | 102539 | SPRING-RIBBON SPOOL BRAKE | 2 |
| 9. | 101221 | ASSY-RIBBON \& SPOOLS | 1 |
| 10. | 101231-005 | SPRING-EXTENSION | 1 |
| 11. | 101035 | ECCENTRIC PAPER FEED \& SWITCH* | * |
| 12. | 101077 | ASSY-PAWL FEED | 1 |
| 13. | 100272-005 | SWITCH-ELECTRICAL | 2 |
| 14. | 102414-001 | FOAM-TAPE | A/R |
| 15. | 101349 | SHIM WASHER | 1 |
| 16. | 101013 | MODIF ICAT ION-PLATEN | 1 |
| 17. | 101073 | ASSY-CAM DRIVE | 1 |
| 18. | 101065 | GUIDE-PAPER | 2 |
| 19. | 101051 | MODIF ICATION-EXTRUSION | 1 |
| 20. | 101392 | DECAL | 1 |
| 21. | 100543-004 | TAPE | A/R |
| 22. | 101057 | GUIDE-PAPER | 1 |
| 23. | 101918 | SPRING-PRESSURE ROLL | 2 |
| 24. | 101003 | SUPPORT-PRESSURE ROLL | 1 |
| 25. | 101067 | ASSY-ROLLER PRESSURE | 1 |
| 26. | 101525 | SHAFT-HEAD | 1 |
| 27. | 101117 | RIBBON FEED PAWL | 1 |
| 28. | 100559 | SPRING-RIBBON FEED PAWL | 2 |
| 29. | 101137-002 | ASSY-HEAD 7 WIRE | 1 |
| 30. | 102409 | DEFLECTOR-RIBBON HEAD | 1 |
| 31. | 101005 | BLADE-TEAR OFF 40 COL . | 1 |
| 32. | 101086 | PULLEY-DRIVE MOTOR | 1 |
| 33. | 101229-001 | BELT | 1 |


| IIEM NO. | PART NO. | DESCRIPTION | QUANTITY |
| :---: | :---: | :---: | :---: |
| 34. | 101008 | PULLEY CAM $1 \frac{1}{4}$ LPS | 1 |
| 35. | 101281 | ASSY-FRAME RICHI SIDE | 1 |
| 36. | 101411-001 | ASSY-A.C. MOTOR | 1 |
| 37. | 102361 | SPRING-CONICAL COMPRESSION | 1 |
| 38. | 101069 | DETENT | 1 |
| 39. | 101068 | ROLLER-DETENT | 1 |
| 40. | 101433 | MANDREL-40 COLUM SUPPLY | 1 |
| 41. | 3030-021 | RIGHT PAPER BRACKET | 1 |
| 42. | 3030-020 | LEFT PAPER BRACKET | 1 |
| 43. | 100173-042 | SCREW ( $4-40 \times .218$ ) | 2 |
| 44. | 100173-050 | SCREW ( $4-40 \times .187$ ) | 1 |
| 45. | 100173-076 | SCREW ( $8-32 \times .375$ ) | 1 |
| 46. | 100172-002 | WASHER (\#4 EXT. LOCK) | 1 |
| 47. | 100173-081 | SCREW ( $4-40 \times .218$ ) | 4 |

* Use on Fast Paper Feed version only.



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$$



NOTE - OPENING THE MACHINE WILL VOID YOUR WARRANTY Simple DR-70 Repair Techniques

Opening the machine
(1) A \#6 hex driver is used to undo the 6 stress bolts located around the aluminum valence. (Note: l'bolt located under warning sticker).
(2) Tape over name plate to avoid scratching.
(3) Lift top cover up and lean it towards the back - it should be stabilized in this position.

Replacement of LEDS
(1) Disconnect 50 pin card edge connector from keyboard - avoid stressing cable.
(2) Lay top upside down. Four sets of $\frac{1}{4}$ " nuts should now be seen on the back side of the display PC board. Remove the nuts and washers. IMPORTANT: Note the number of, and order of the spacing washers.
(3) Turn top cover over. Mylar display screen may now be lifted off. Locate defective LED.
(4) Now, unsolder old LED and discard. Noting that the direction of the notch in LED lens aligns with the other LEDs (towards the left, looking from the top). Solder LED in FLUSH to the $P C$ board. Do not hold solder iron on the LED lead for longer than 2 seconds. Overheating will cause internal damaye to the LED.
(5) Screw the display screen and PC board back together with the same exact number of washers (see diagram below). Should the LEDs get too close to the screen, heat from LED will cause the mylar to melt.

(6) Replace ribbon cable, then verify new LED working. Then, close the machine (see "Sealing up the machine').

## Changing keyboard switches

(1). Disconnect 50 pin card edge connector from keyboard - avoid stressing the ribbon cable.
(2) Remove keytop on suspect key. Usually a small screwdriver working from the bottom of keytop, pulling up, will do.
(3) After locating key on backside of PC board, solder stuck the 2 contact holes clean. Free key by pulling from top. Make sure both the contact holes and the mounting guide holes(2) are free of obsrtuctions.
(1) Gently ease new key switch into hole and solder. It is important to note the following:
(i) the switch contacts should be well centered in PC board holes such that no side pressure is placed on the contacts
(ii) Hold solder iron on switch leads for no longer than 2 seconds as the internal mechanism is prone to overheating.
(5) Replace ribbon cable and keytop. Verify proper functioning of keyswitch. Seal machine, if working.

## Blown fuses

(1) Check that fuse is $3 / 4$ amp AGC fuse.
(2) Open machine. Carefully look for loose shards in $+5 v$ circuitry on motherboard. Check l20v wiring for loose or shorting connections. Check operation.
(3) Cut l20v wire to fan. Check operation. If it works - replace fan (all parts available through Digicomp). If not - reconnect wire.
(4) Cut l20v wire to power supply. Check operation by observing fan.
(i) If fan runs, go to \#5.
(ii) If fan doesn't run - disconnect line filter and bypass it. Reconnect power supply and check operation. Should it work, replace filter. If it doesn't, recheck wiring for shorts.
(5) Cut yellow wire to 7805 regulator. Check operation of fan. If now working, replace 7805. If not, check power supply PC board and motherboard for shards in +5 v power busses.
(6) if you still have no luck, call Digicomp (607) 273-5900. Sealing machines PLEASE FOLLOW THIS CHECKLIST
(l) Make sure power supply, motherboard, keyboard, and display are all screwed down tight.
(2) Check power supply output. It should be $+5.05 v$. If not, adjust pot.
(3) Check master clock (only if you have a calibrated oscilloscope). It should have a period of $1.25 \mu \mathrm{~s}$. The clock pot is located on the power supply side of the CPU module.
(4) Vacuum machine thoroughly to remove any loose debris. If inside is dusty, wipe it clean using isopropal alcohol.
(5) Push all IC's down manually.
(6) Fit top and bottom together. Pick up and shake machine, meanwhile listening to hear loose material inside enclosure.
(7) With cover still on, check for proper functioning. Then insert and tighten stress bolts.

Recommended tools for DR-70 repair
(1) $\# 6$ hex driver or Alan wrench
(2) small flat screwdriver
(3) chip extractors
(4) soldering iron and solder
(5) solder wick or solder removal tool
(5) $1 / 4^{\prime \prime}$ socket wrench or spanner
(7) wire cutters
(8) Freon solvent
(9) isopropal alcohol

## Simple DR-70 Repair Techniques

NOTE - OPENING THE MACHINE WILL VOID YOUR WARRANTY UNLESS OTHERWISE INSTRUCTED BY DIGICOMP.

## A. Opening the machine

1. The DR-70 is sensitive to static electricity, so when working on the machine do not wear sweaters, nylon shirts, rubber-soled shoes or material that creates static charges.
2. A \#6 hex driver is used to undo the 6 stress bolts located around the aluminum valence and fan. (Note: $\underline{1}$ bolt located under warning sticker).
3. Tape over name plate to avoid scratching.
4. Lift top cover up and lean it towards the back - it should be stabilized in this position.
B. Techniques of repair for intermittant failures or lost data (but machine responds to reset).
5. Check operators manual for proper key sequence.
6. Follow opening instructions (\#A). Once open, firmly press each chip to reseat their contacts in the sockets.
7. Replace cover and observe behavior. If intermittant behavior still occurs, repeat procedure \#l.
8. If machine continues to behave intermittantly, purchase some spray contact cleaner (a freon solvent which leaves no residue or oil). Again open the machine. With chip extractors or a small screwdriver, lift the chips partially out of their sockets. Spray the contact cleaner on the chips legs and reseat the chips by pushing them in. Check that none of the chips legs are bent or out of the socket. (Note: if a chip should accidently come out of its' socket, it must be replaced in the same direction).
9. Replace cover and observe machines behavior.
C. Replacement of LEDS
10. Disconnect 50 pin card edge connector from keyboard - avoid stressing cable. The epoxy seal can be broken with a small screwdriver or knife.
11. Lay top upside down. Four sets of $\frac{1}{4}$ " nuts should now be seen on the back side of the display PC board. Remove the nuts and washers. IMPORTANT: Note the number of, and order of the spacing washers. (See \#5)
12. Turn top cover over. Mylar display screen may now be lifted off. Locate defective LED.
13. Now, unsolder old LED and discard. Noting that the direction of the notch in LED lens aligns with the other LEDS (towards the left, looking from the top). Solder LED in FLUSH to the PC board. Do not hold solder iron on the LED lead for longer than 2 seconds. Overheating will cause internal damage to the LED.
14. Screw the display screen and PC board back together with the same exact number of washers (see diagram below). If the LEDS get too close to the screen, heat from LED will cause the mylar to melt.

15. Replace ribbon cable, set top down on bottom in place. Turn machine on, then verify that new LED is working. Turn machine off, then close the machine (see "Sealing up the machine").

## D. Replacement of keyboard switches

1. Disconnect 50 pin card edge connector from keyboard - avoid stressing the ribbon cable. (See Cl).
2. Remove keytop on suspect key. Usually a small screwdriver working from the bottom of keytop, pulling up, will do.
3. After locating key on backside of PC board, remove the solder from the two contact holes. Free key by pulling from top. Make sure both the contact holes and the mounting guide holes (2) are free of obstructions.
4. Gently ease new key switch into hole and solder. It is important to note the following:
(i) the switch contacts should be centered in PC board holes and the switch must be seated flat to prevent the key tops from rubbing against one another.
(ii) Hold solder iron on switch leads for no longer than 2 seconds as the internal mechanism can overheat.
5. Replace ribbon cable and keytop. Verify proper functioning of keyswitch. Seal machine, if working.

## E. Procedure if fuise blows repeatediy

1. Check that fuse is $3 / 4$ amp AGC fuse.
2. Open machine (See \#A). Carefully look for loose shards in $+5 v$ circuitry on motherboard. Check l20v wiring for loose or shorting connections. Check operation.
3. Cut 120 v wire to fan. Check operation. If it works-replace fan (all parts available through Digicomp). If not - reconnect wire.
4. Cut 120 v wire to power supply. Check operation by observing fan.
(i) If fan runs, go to \#5.
(ii) If fan doesn't run - disconnect line filter and bypass it. Reconnect power supply and check operation. Should it work, replace filter. If it doesn't, recheck wiring for shorts.
5. Cut yellow wire to 7805 regulator. Check operation of fan. If now working, replace 7805. If not, check power supply PC board and motherboard for shards in $+5 v$ power busses.
6. If the machine continues to blow fuses, call Digicomp at (607) 273-5900.
F. Sealing machines

PLEASE FOLLOW THIS CHECKLIST

1. Make sure power supply, motherboard, keyboard, and display are all screwed down tight.
2. Check power supply output. It should be $+5.05 v$. If not, adjust pot.
3. Check master clock (only if you have a calibrated oscilloscope). It should have a period of 1.25 us. The clock pot is located on the power supply side of the CPU module.
4. Vacuum machine thoroughly to remove any loose debris. If inside is dusty, wipe it clean using isopropal alcohol.
5. While the machine is open an easy and helpful maintenance procedure is to push down on all the chips to ensure that they are seated firmly in their sockets.
6. Fit top and bottom together. Pick up and shake machine, meanwhile listening to hear loose material inside enclosure.
7. With cover still on, check for proper functioning. Then insert and tighten stress bolts.

## DR -70 Printer - Mechanism <br> MAINTENANCE AND REPAIR

1.0 The Model 7040 printers are designed ta require a minimum of maintenance and service. This section provides instructions and procedures for both cleaning and maintenance of the printer as well as a troubleshooting guide to aid in problem analysis.
1.1 Required Tools and Supplies

The following items are recommended for printer maintenance and major repairs. Common hand tools are also required.

1. Volt-Ohm Meter
2. Feeler Gauges
3. Grease (IBM \#23 or Lubriplate $\# 70$ )
4. Isopropyl alcohol
5. Cleaning Rags
6. Cotton Swabs

### 2.0 Maintenance

The frequency at which normal maintenance (cleaning, lubrication and inspection) should be performed when the printer is installed in a clean office environment should normally be one year. However, if the printer is subjected to heavy usage and/or a dirty environment, the frequency should be increased as required.

### 2.1 Cleaning

A. Clean printer thoroughly using alcohol, rags and cotton swabs to remove hardened grease, ink and dirt.
B. Rotate platen and clean with an alcohol soaked rag.
C. Remove head and clean head and head shaft with alcohol.
D. Reassemble head to printer.
E. Apply a small amount of recommended grease to head shaft.

### 2.2 Inspection

A. Check for print wire movement on all seven solenoids by depressing wire end and noting return to rest position.
8. Check for easy movement of the head assembly on the print head assembly shaft.
C. Inspect the ribbon reverse mechanism for proper movement. Manually operate the ribbon feed pawl back and forth to check operation. Be sure that both springs are securely in place and of equal tension.
D. Inspect the printer for any loose or missing screws.
E. Check for excessive vertical movement of print head drive cam. Usual cause is a loose paper feed motor. Rotate the drive cam toward the platen and listen for an audible click of the home micro switch.
(1) The position of the switch arm roller at the click should be half-way up/down the high dwell ramp of the cam.
(2) The switch activating arm should be straight with no kinks.
F. Check the paper feed cam for tightness on motor shaft (looseness may be a result of a bad cam or improper motor shaft configuration).
G. Rotate the paper feed cam clockwise (toward platen) and check for smooth operation. Binding indicated bad fit between cam and pawl arm. Listen to the paper feed micro switch for an audible click. (1) The position of the switch arm roller at the click should be half-way up/down the high dwell ramp of the cam.
(2) The switch activating arm should be straight with no kinks.
H. Check that right end of print platen is not loose with regard to platen drum.
I. Check that the platen spins freely when paper release lever is depressed and ratchet pawl is disengaged.
J. Check that the plastic paper guide strip edge touches the platen drum over its entire length.
K. Check that the plastic paper guide strip is secured to the extrusion mounting surface.
L. Check that the print head moves freely along its shaft and within the drive cam groove while turning the drive cam toward the platen.
M. Place the ribbon in place and check that it does not snag throughout its path. Check ribbon spool drag.
N. Depress paper release lever and check that bar and feed roll leave the print platen.
0. Check that two ply form may be loaded when paper release lever is depressed.
P. Check that paper guides are not loose on main frame extrusion and that they may be moved with moderate pressure for adjustment.
Q. Inside guide edges should be flush with inside edges of sides of supply mechanism.

### 2.3 Lubrication

Recommended lubricants are IBM \#23 or Lubriplate \#70. After cleaning, the following areas should require lubrication:
a. Head shaft
b. Groove in main drive cam
c. Guide in extrusion in which the "tail" of the print head travels. Only a small amount of lubricant, enough to create a film; is needed in each location.
The following areas may also require lubrication, if the area has been wiped free of lubricant:
a. Paper feed cam and pawl
b. Ribbon feed pawl
c. Platen detent teeth
d. Platen ratchet teeth
e. Pressure roll release lever

### 3.0 Repair and Adjustments

### 3.1 Troubleshooting Guide

The Mechanism Troubleshooting Table should provide a means of determining the cause of any malfunction and the proper corrective action.

### 3.2 Repair Techniques

### 3.2.1 Head Replacement and Adjustments

Remove the old head by removing the two screws which hold the head shaft in place and then pulling the head shaft and head up out of the mechanism. To free the head cable the cable tie must be cut.
Remove shaft from old head and slide it into the bearings on the replacement head. The head assembly should move freely along the shaft. To check for free movement hold the head assembly with the shaft vertical and release the shaft. It should drop through the head assembly. If it does not move freely, hold the head assembly on one end of the shaft and tap the other end firmly on a hard surface. This will cause the head assembly bushings to seat perpendicular to the shaft. The replacement head can now be installed in the printer in the reverse manner of its removal. When tightening the screws in the head shaft, hold the shaft forward to minimize the distance between it and the platen. Insure that the drive pin on the bottom of the head mates with the groove in the drive cam and that the "tail" on the head is placed in the guide slot on the extrusion. Lubricate according to paragraph 2.3 prior to operation.

### 3.2.2 Switch Replacement

3.2.2.1 Line Feed Switch (FPF Mechanism Only)

To replace the line feed switch it is first necessary to remove the left side frame. This is done by first removing the twirler from the platen. Then remove the three screws which secure the side frame to the main extrusion as well as the screw in the left side of the head shaft. Once these screws are removed the left side frame can be pulled away from the printer. The line feed switch can now be removed from the side frame by lifting it off its mounting tabs. The replacement switch is then pressed onto those tabs and a piece of adhesive-backed foam is placed on the switch where it contacts the main extrusion. The side frame can now be replaced taking care to align the platen pressure roll support bracket, head shaft and main drive cam to their proper positions.

### 3.2.2.2 Print Switch

To replace the print switch it is first necessary to remove the right side frame. This is done by removing the three screws that secure the side frame to the main extrusion as well as removing the screw in the right hand side of the head shaft. The side frame can now be removed, taking care to disengage the motor pulley from the belt. Holding the detent lever down (forward) will aid in the removal of the frame. The print switch can now be removed by lifting it off its mounting tabs. The replacement switch is then pressed onto these tabs and adhesivebacked foam is applied to the switch where it contacts the main extrusion. The printer is then reassembled taking care to align the main drive cam and platen properly. During reassembly the detent lever must be held down to avoid interference with the ratchet teeth on the platen, and the motor pulley must have the belt placed back on it.

### 3.2.3 Belt Tension

Drive belt tension is adjusted by loosening the two motor mounting screws and moving the motor forwards or backwards to decrease or increase the belt tension. The belt tension should be set so that when the head is prevented from moving, the motor stalls and the belt does not cog over the motor pulley. Excessive belt tension will cause the drive cam to bind and the motor to stall. Too little belt tension will result in excessive variation in character widths.

### 3.2.4 Head to Platen Air Gap

If the printhead has been replaced due to solenoid failure or if the printer has been disassembled, the printhead to platen air gap should be checked prior to using the printer. Improper air gap can result in damage to the printhead. The printhead to platen air gap may be checked by using feeler gauges. Prior to checking the air gap it is necessary to remove the paper and ribbon from the printer.
The head to platen air gap should be .O12" to .015". If the air gap is out of the allowable range, some adjustment can be made by loosening the screws in the head shaft and pushing it in the desired direction. If this does not yield sufficient change then it is necessary to loosen the side frames and move them upward or downward in relation to the main extrusion

MECHANISM TROUBLESHOOTING GUIDE

| SYMPTOM | POSSIBLE CAUSE | CORRECTIVE ACTION |
| :---: | :---: | :---: |
| 1. Print motor starts but doesn't stop. No printing occurs. | 1. Broken print switch <br> 2. Broken switch wire <br> 3. Misadjusted print switch | 1. Replace switch <br> 2. Check all wires on print switch for electrical continuity <br> 3. Verify switch transfers when roller on.șwitch actuator transfers between high and low positions on the cam pulley. |
| 2. Once started, the line feed motor never stops | 1. Broken line feed switch <br> 2. Broken switch wire <br> 3. Misadjusted line feed switch | 1. Replace switch <br> 2. Check all wires for electrical continuity <br> 3. Verify switch transfers when roller on switch actuator transfers between high and low positions on switch eccentric. |
| 3. Print Motor stalls | 1. Excessive Belt tension <br> 2. Bad crimp on one or more A.C. motor wires <br> 3. Bad A.C. motor | 1. Adjust belt tension <br> 2. Visually verify crimps are good <br> 3. Replace motor |
| 4. Occasional <br> missed line feed | 1. Bad line feed switch <br> 2. Misadjusted line feed switch | 1. Replace switch <br> 2. Verify switch roller rides on lower surface of paper feed eccentric when in the home position, and that switch transfers when switch roller moves from low surface of the paper feed eccentric |
| 5. Uneven line feeds | 1. Paper slippage <br> 2. Metal wear plate broken off line feed pawl <br> 3. Platen not detenting properly | 1. Clean platen and/or replace pressure roll springs <br> 2. Replace pawl <br> 3. Check for defective detent mechanism. |


| SYMPTOM | POSSIBLE CAUSE | CORRECTIVE ACTION |
| :---: | :---: | :---: |
| 6. Line feed motor stalls | 1. Bad A.C. motor <br> 2. Line feed pawl binds on eccentric <br> 3. Bad wire to pin connections on A.C. motor wires <br> 4. Platen won't turn or turns with difficulty | 1. Replace A.C. motor <br> 2. Replace pawl or eccentric <br> 3. Check crimp on wire <br> 4. Inspect detent mechanism for source of jam and replace or repair as required |
| 7. Missing dots | 1. Ribbon not properly installed <br> 2. Bad print solenoid <br> 3. Head to platen gap excessive | 1. Instail ribbon correctly <br> 2. Replace head <br> 3. Verify head to platen gap is .015" $\pm .003$ " |
| 8. Excessive variation in character size | 1. Belt tension too loose <br> 2. Start to print distance (distance from lst dot position to inside of side frame) is out of tolerance (. $965 \pm .060^{\prime \prime}$ ) <br> 3. Head binding on shaft | 1. Tighten belt tension <br> 2. Adjust print switch to correct start to print location. <br> 3. Align head bearings on shaft. Lubricate shaft. |


| ITEM VO. | PART NO. | DESCRIPTION QU | QUANTITY |
| :---: | :---: | :---: | :---: |
| 1. | 101411-004 | ASSY-A.C. MOTOR* | 1 |
| 2. | 100576 | TWIRLER | 1 |
| 3. | 101528 | BRACKET-RIBBON TENSION | 1 |
| 4. | 101225 | SPACER-RIBBON POST | 2 |
| 5. | 102378 | SPRING-RIBBON TENSION | 1 |
| 6. | 101383 | ASSY-FRAME LEFT SIDE | 1 |
| 7. | 100625 | ASSY-RIBBON POST | 2 |
| 8. | 102539 | SPRING-RIBBON SPOOL BRAKE | 2 |
| 9. | 101221 | ASSY-RIBBON \& SPOOLS | 1 |
| 10. | 101231-005 | SPRING-EXTENSION | 1 |
| 11. | 101035 | ECCENTRIC PAPER FEED \& SWITCH** | ${ }^{+} 1$ |
| 12. | 101077 | ASSY-PAWL FEED | 1 |
| 13. | 100272-005 | SWITCH-ELECTRICAL | 2 |
| 14. | 102414-001 | FOAM-TAPE | $A / R$ |
| 15. | 101349 | SHIM WASHER | 1 |
| 16. | 101013 | MODIFICATION-PLATEN | 1 |
| 17. | 101073 | ASSY-CAM DRIVE | 1 |
| 18. | 101065 | GUIDE-PAPER | 2 |
| 19. | 101051 | MODIF ICATION-EXTRUSICN | 1 |
| 20. | 101392 | decal | 1 |
| 21. | 100543-004 | TAPE | $A / R$ |
| 22. | 101057 | GUIDE-PAPER | 1 |
| 23. | 101918 | SPRING-PRESSURE ROLL | 2 |
| 24. | 101003 | SUPPORT-PRESSURE ROLL | 1 |
| 25. | 101067 | ASSY-ROLLER PRESSURE | 1 |
| 26. | 101525 | SHAFT-HEAD | 1 |
| 27. | 101117 | RIBBON FEED PAWL | 1 |
| 28. | 100559 | SPRING-RIBBON FEED PAWL | 2 |
| 29. | 101137-002 | ASSY-HEAD 7 WIRE | 1 |
| 30. | 102409 | DEFLECTOR-RIBEON HEAD | 1 |
| 31. | 101005 | BLADE-TEAR OFF 40 COL . | 1 |
| 32. | 101086 | PULLEY-DRIVE MOTOR | 1 |
| 33. | 101229-001 | BELT | 1 |


| ITEM NO. | PART NO. | DESCRIPTION | QUANTITY |
| :---: | :---: | :---: | :---: |
| 34. | 101008 | PULLE $Y$ CAM $1 \frac{1}{4}$ LPS | 1 |
| 35. | 101281 | ASSY-FRAME RIGHT SIDE | 1 |
| 36. | 101411-001 | ASSY-A.C. MOTOR | 1 |
| 37. | 102361 | SPRING-CONICAL COMPRESSION | 1 |
| 38. | 101069 | DETENT | 1 |
| 39. | 101068 | ROLLER-DETENT | 1 |
| 40. | 101433 | MANDREL-40 COLUMN SUPPLY | 1 |
| 41. | 3030-021 | RIGHT PAPER BRACKET | 1 |
| 42. | 3030-020 | LEFT PAPER BRACKET | 1 |
| 43. | 100173-042 | SCREW ( $4-40 \times .218$ ) | 2 |
| 44. | 100173-050 | SCREW ( $4-40 \times .187$ ) | 1 |
| 45. | 100173-076 | SCREW (8-32 $\times .375$ ) | 1 |
| 46. | 100172-002 | WASHER (\#\# EXT. LOCK) | 1 |
| 47. | 100173-081 | SCREW ( $4-40 \times .218$ ) | 4 |

* Use on Fast Paper Feed version only.
(2)



IC ${ }^{\circ}$,

| Part ■ | Desuipt |
| :---: | :---: |
| $U 1$ | 7402 |
| $U 2$ | 7402 |
| $U 3$ | 7406 |
| $U 4$ | 7402 |
| $U 5$ | 7474 |
| $U 6$ | 555 |


| Fuses |  |
| :---: | :---: |
| Part $=$ | Descriptic |
| F1-7 | 1 amp |
| F8 | $1.5 \mathrm{amp}:$ |

Misc.

| Part $=$ | Description |
| :---: | :--- |
| $J 1$ | 20 pinheaded |
| $J 2$ | print conned |
| $K 1$ | relay |


DR-70 P Printer : Schematic no. 2




110 v Printer Wiring Diagram

December 14, 1981

Dear DR-70 Owner,
I have a few reminders for you:

1. When you travel with the printer, be sure to remove the roll of paper. Otherwise, it can damage the PC board in the rear of the printer.
2. Try to avoid X-rays in the airport. When you go through the security area ask the inspector to "hand check please". I have found that they are usually happy to do it.
3. High humidity is detrimental to computers. It is safe and helpful to place a bag of crystals in the carrying case when you store the DR-70 or Printer.
4. Extra ribbons for the printer should be stored in an airtight can and placed in a cool place (refrigerator). This will previent them from drying out.

Best wishes,
Jeffreylor
Jeffrey Cox

Dear DR-70 Owner,
Before you begin your DR-70 adventure, I would like to suggest a path for you to follow through the maze of instructions which are found in this manual. Your resources consist of a DR-70 User's Manual, Primer, a mini-manual called the BASICS, Fourth Quadrant Newsletters, your local representative (if you have one) and Digicomp. You will probably use all of them at one time or another. However, there is a logical order which if followed should make the learning process painless. I want to outline this for you. You will be using the DR-70 for a long time and it will become an important part of your astrological life. The effort you put into learning the DR-70's capabilities will make it possible to take full advantage of this powerful tool and to expand your astrological know-how.

1. First turn to Section II-01 of the User's Manual where you will learn how to hook up and test the DR-70. After checking the Sun position...
2. Turn to Section $\overline{X I X}-02$ to 05 where you will find out how to set up the Printer. If you do not have a Printer go to step \#4.
3. Now pull out the BASICS from the pocket of your User's Manual and work through it step-by-step. When you have completed all the steps go back to those techniques that interest you most and repeat them for other charts you have hand-calculated. If you have any questions, turn to the index in the rear of the Manual and go to those sections which further elucidate that technique. Even if you have a Printer, I highly recommend that you try step \#4.
4. If you do not own a Printer, then I suggest that you work through the Primer. Then familiarize yourself with the structure of the Manual by reading page i \& ii in the beginning of the Manual. Also check the index for specific techniques you would like to try.

By reading through the index, you will begin to appreciate all the different techniques the DR-70 performs and where to find them. To master the DR-70, you will want to work through as much of the learning resources as interests you. It is important that you appreciate why you are pressing certain keys if you want to learn how to use the DR-70 without simply memorizing the keys you have to press. Feel free to experiment, you may discover short-cuts. Use a familiar chart so you can quickly see when you have gone astray. If you run into any bottlenecks please feel free to contact your local representative or Digicomp. Happy computing!

Sincerely,
Jofeglot

Jeffrey Cox<br>Astrological Sales



)
1 Installing PRoms

1) open machine
inspect braid: which if 2 perm maps insect braid : which is it Gillow?
2) lift the old version of as e (yellow) prom
out of its socket
carefully, alternately,
prying up a little ot each end w/a
small screwdriver.
place old prom in
black antip-static
foam, replace NEW prom in socket $\rightarrow$ push down.
Check to see that prom is in right direction tall legs are in sockets.
3) repeat for each of the remaining 3
Ba) PRoms.
(i) close machine short software check: dues if cilc. the $\Theta$ ?
Does if do $* 75.01$ ENTER ( $r$ ), ENTER

> INSTRUCTIONS FOR SPEEDING UP DR- $\quad$ OO SYSTEM CLOCK

1. Open up the DR-70 by removing the 4 side screws and the two screws above the cooling fan in the rear; using a 5/64" hex wrench.
2. Plug in the DR-70 and turn on power.
3. Ground your oscilloscope to the Motherboard.
4. Touch the scope probe to pin 6 of chip $U 64$ to get a readout of the DR-70 system clock. (746504)
5. Tune the frequency of the clock to 1.5 MHz by adjusting pot Pl . Drop a piece of inut way on the pot to seal it.
6. Remove the probe and replace the top of the case.
7. Try this test routine to check the speed of $D R-70$ 's calculations:

RESET
DATE RECALL (12.31.1899) ,
DATE ENTER (r12.31.1899)
(SHIFT) on: $\odot$ ENTER
The answer (10.09.38.36) should appear in 5 or 6 seconds from the time you pressed $\odot$ ENTER.
8. Replace the 6 screws.

> For complete
> boaud layout map see Repair Manual


Ration
DR-70 P Printer: Schematic no. 2

DR-70 P Printer: Schematic no. 2

Current DR-T0's $6 / 82$
PROM map for (NE WBOARD)

Board v. 9
New Motherboard with Transit Search Option:

Board v. 9
New Motherboard without T Search:

TS mem. v. 14 with Transit search



NTS mem.v. 12 without TS

DR-70 COMPUTER \& PRINTER SERVICE MANUAL

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## DR-70 Overview

## I. Introduction

The $\quad 0 R-70$ astrological computer is a MCS6502 based microcomputer. The MCS6502 is an eight bit microprocessor originally manufactured by mos technology. It is an n-channel, sinyle suiply ( +5 V ) device operated at 1.0 mhz. It contains an eight bit, bi-directional data bus and a separate sixteen bit address bus capable of addressing 64 K of memory. Input/output is memory mapped.

Projram storage is on roughly 40 K of Read only Memory made up of ROM's and EPROM's (Electronic Arrays EAS316 $2 \mathrm{~K} \times 8$ ROM, MOS Technology $23324 \mathrm{~K} X 8$ ROM, Intel 2716 2K $X$ \& EPROM). IK or 2 K oE Random Access Memory is used, depending on the program version (eight $21021 \mathrm{~K} \times 1$ RAM and $41181 \mathrm{~K} \times 8$ RAM). At 1.0 Mhz operation, the access time for these devices can be as long as 490 ns.

The keyboard and display are interrupt driven. The digital display is a ten digit display stick TI-TIL-804-10. The LED's are $F[V-140$. Keyboard key switches are manufactured by Mechanical Enterprises Inc. The keyboard and display are accessed through memory locations in the 6502 's 54 K address space.

The printer is operated through a parallel port within the 64 K address space. The port is eight bits wide with two handshake lines and an additional line for examining if the printer is connected.

The $+5 V$ power source comes from a 3 amp power supply. The autotransforiner input can be configured to operate with 110 V or 230 V line voltage, $+/-10 \%, 47-440 \mathrm{~Hz}$. A separate +5 V Lm7805 (or Lm340 T-5) is used to power the display.
II. Operation of the MCS5502

The MCS¢502 is mounted on a small PC board connected to the Mother board via two, 20 pin SIP sockets. Also on this board are two bipolar PROMs used for encoding/decoding to offer some software protection. This hoard is also encased in thermally conductive epoxy for the same reason. By token of the
decoding/encoding PROMs, this board is refered to as a decoder board. This replacement part is available through Digicomp Reseach.

The clock for the 6502 is derived from a $R C$ network (Cl and Pl). The look potentiometer Pl is tuned until a 1 Mhz is achieved on pin 39 of the processor (pin 39 of the decoder board) or U64p6.

The keyboard and display are driven by an interrupt routine. The interrupt is initiated by a low level on the interrupt request pin (IRQ, decoder board pin 4). The display must be updated every 2.2 ms to provide flicker-free operation. To generate the periodic interrupt signal, a 556 is used. The 556 uses a RC network to achieve the timing. The lM potentiometer P3 adjusts the frequency of the interrupts while the look potentiometer P? adjusts the duty cycle. The potentiometer P3 must be adjusted to provide the required 2.2 ms interrupt timing and $P 2$ must be adjusted so that the low level on the interrupt pin is at least 20 microseconds in duration. Failure to provide the mininium 20 microsecond duration will "bomb" the processor. The interrupt signal is also available on $465 p 6$.

Since the MCS6502 microprocessor has no input/output instructions, all input/output must be done through memory addresses. This means that inputs from the keyboard and outputs to the display and printer are accessed through physical memory locations in the $64 K$ address space. The exact locations of these are indicated on the DR-70 Address Map.

The data and address buses from the processor do not provide sufficient drive power for the entire data and address buses on the DR-70. The address bits AO - All are buffered by three $74 L s l 25 s(U 5,6 \& 7)$. The address buffers are always enabled. The data bus $D 0-D 7$ is driven by four $74 \mathrm{Ls} 125 \mathrm{~s}(\mathrm{U} 8,9,10 \& 11)$. Since the data bus is bi-directional, the 6502 Read/Write* (R/W* pin 34 or $U 20 p 5$ ) signal is used to control the direction of data transfer. $R / W^{*}$ is used to enable individual 74Lsl25 drivers so that the data transfer flows in the proper direction.

Reseting the processor (RESET*, decoder board pin 40) occurs in two manners: (l) a power on reset occurs when the mother board is initially powered up, and (2) a reset occurs when the RESET key on the keyboard is depressed. When +5 V is applied to the board, pin 40 should be held low until all the transients settle. This will start the processor in a known state. The delay for this reset is done with C2 and a 47 K resistor. When power is applied, $C 2$ charges to +5 V via the 47 K resistor. Until C 2 charges to the input threshold of U2p2, reset is asserted low.

When the keyboard RESET key is depressed, it is first debounced with a "NAND gate flip-flop" (U2). It is then qated with the
power on reset signal. The reset pin of the processor should be held low as long as the key is depressed.
III. Memory usage and address decoding

There are five diferent types of memory components in the DR-70: 4118, 2102, 2716, 8316, and 2332. The 4118 and 2102's are Random Access Memories (RAM) and are used in storing intermediary results and answers. 2716, 8316, and 2332's are Read Only Memories used for program storage. Address and physical location information is contained on the Address Map sheet.

The address decoding for all these devices is done with All Alb. This allows the 64 K address space to be broken into 2 K sections. All - Alt are fed unbuffered to a 74154 demultiplexer (Ul7p20-23). The demultiplexer breaks the address space into 4 K segments. The addresses are further broken down with All. The active low outputs of the 74154 are gated appropriately with All or All* (U18,19,20, and 21). This produces active low chips selects (active in 2 K segments). These signals are tied to the active low Chip Selects of the 2716 (pin $18 \& 20$ ), 8316 (pin $18 \& 20$ ), 4118 (pin $18 \& 20$ ), and 2102's (pin 13). The 4 K 2332 (pin $20 \& 21$ ) are selected by the active low outputs of the demultiplexer.

The internal address requirements of each memory device are: AO - All for 2716 and 8315 , A0 - A9 for the 4118 and 2102 , and $A 0$ All for the 2332.

Access time for memory and periphrals must be less than 490 ns . For a $\zeta 502$ at 1 Mhz , access time must be about 575 ns . However, the delay time in encoding/decoding PROMS and buffers bring the access time to 490 ns. If replacement parts are used they must have access times faster than 490 ns .
IV. Keyboard and Display

The keyboard is accessed through the address $30 x \mathrm{x}$. The individual keys are arranged in rows and columns. Raising a particular address bit in the lower byte will enable a row of keys. Depression of a key in that row shorts the raised address bit to the data bus. The processor then knows which key is depressed by the raised bit in the data bus. Thus the entire keyboard is read by successively raising all the address bits, one by one. The house dial (the rotary switch) is treated as another row in the keyboard. The position of the switch shorts the address bit to the appropriate data bit. Inablity to read this switch will produce an "Error" condition whenever
houses or charts are requested.
The display is managed in much the same way as the keyboard. A particular LED is lit by raising its associated address bit (in the address $40 \times X$ ) and lowering the associated data bit. The digits are operated one digit at a time. By raising of a particular address bit in the address $50 x \mathrm{x}$, a digit is grounded. The segments in that digit are lit by lowering data bits (a segement $=$ bit $0, b$ segement $=$ bit 1 , $c$ seqement $=$ bit $2, \ldots$ ). Thus the whole display is lit by writing the proper data to all the rows in the digits and LEDs.
V. DR-70 Printer interface

The $D R-70$ printer interface utilizes a 74100 (U33) eight bit latch and a flip-flop. Columns of dots are put in the latch. The printer prints one column at a time allowing any character to be assembled by outputting the proper sequence of columns. Handshaking is done with two lines: Ready and Acknowledge (marked RDY and ACK on schematic). The Ready line is a signal from the $D R-70$ telling the printer to start the motor, print a line, do a line feed, and stop at home. When the printer starts a line, it sends Acknowledge signals (active high) to indicate that it is ready to print a new column. Upon receipt of an Acknowledge, the $D R-70$ latches a new byte into the 74100 . The latch control is enabled by writing to the address 6800. The Examine is a signal which is grounded when the cable is plugged into the printer. If Examine is high, the DR-70 will respond with "Error" if required to print. The Examine and Acknowledge signals are read by reading at the address 6800.
VI. $+5 V$ power Supplies

DR-70 sıpplies are 3 amp linear power supplies. The line voltage input is wired to a autotransformer which permits the supply to be run with a llov or 230 V input. The transformer can be hard-wired to run at either voltage or run at both with the aid of a switch. If the $D R-70$ is wired for $110 / 230 \mathrm{~V}$ operation, the line voltage must be known and the switch set appropriately before plugging in the machine. Failure to do so can result in damage to the supply's semiconductors, or a blown transformer. The $110 / 230 \mathrm{~V}$ machines uses two fuses, one lamp fuse for 110 V operation and one 0.317 used in 230 V operation.

To reduce noise on the board, the display draws it's power from a separate regulator. Input power to the regulator is drawn from the secondary winding of the transformer (the actual wiring is connected to the plus side of the filter capacitor). Failure of this regulator will only stop power for the LEDs and digits

## creating a situation where the computer is still running but no lights :work.

The $110 / 230$ VAC is first passed through a line filter before going to the power supply. The filter is a Corcom 8042L line filter, rated at $115 / 250$ VAC $50-400 \mathrm{~Hz}$. Power is turned to the filter when the power keyswitch is turned on. Power for the fan is not drawn from the filter.

General In Field Debug Overview

## I. Power Failure

Power failure can occur in three ways. First, the machine can continuously blow fuses. Refer to section E of Simple DR-70 Repair Techniques if this is the problem. The last two power problems are failures in the +5 V circuitry: a failure in the +5 V to the mother board; or failure in the +5 V to the display (on older machines this separate supply may not be present). To isolate which circuit is failing:
i. Check to see if the shift key lights or measure the output of the Lm7805 mounted on the side of the power supply. If +5 V is not present measure the input to the regulator, it should be ll-13V.

If input power is present, (1) check for power shorts in the display area and display board; (2) check that the Lm7805 is operating properly (disconnect the output and see if it holds the voltage at the rated 1 Amp load).

If input power is absent, (1) check that 115 VAC (or $230 \mathrm{VAC})$ is present on the primary of the transformer, if not refer to section $E$ of the Simple DR-70 Repair Techniques. (2) The primary or secondary winding of the transformer could be blown. The impedence of the secondary should be in the $1-5$ ohm range. The primary should measure about 9-11 ohms.
ii. If power is lacking or poor on the mother board, first disconnect the supply output from the board and check it's output at about 2.5 Amps. If the voltage drops, first check that the transformer is receiving a proper input voltage, if not, refer to (I.i) above. If the input voltage is acceptable, the power supply will have to he repaired or replaced. Contact Digicomp for replacements or schematics.

If the supply holds $+5 V$ at near rated current, the mother board most likely has a short. Check for loose shards of wire or hardware. Also check if any components are getting excessively hot. A blown component can short out the whole hoard. Power can be cut to parts of the board at a time by noting the power patches on the List of Cuts and Patches.

## I. Does Not Respond to Reset

The failure of the processor to reset properly can be the result of any number of problems. This particular condition can be indentified by:
i. The display comes up in an improbable state and will not respond to keyboad requests.
ii. No display at all, no responce from keyboard input but the shift key still lights when depressed.
iii. If when the machine is open, the processor stalls at one address after a reset.

This symptom is associated with three conditions. (1) Improper adjustment of system and interrupt clocks. (2) Failure to properly access memory. (3) Component failure.

1. Adjust the clocks. The system clock (U64p5) must be adjusted to 1 Mhz by potentiometer Pl. If a l Mhz signal can not be achieved, check Cl (50 pF) and Pl (l00K) for proper value and function. Also check for shorts.

The interrupt clock (U65p6) must go low every 2.2 ms and MUST be low for at least 20 microseconds. P3 adjusts the frequency of the interrupts while P2 adjusts the duration of the low level. Failure to produce this signal may be the of failure in discreet components or the 556 (see schematic).

The clocks should be checked first upon the the occurance of this problem.
2. Improper memory use is harder to define. First clean/press down all sockets. Corrosion is the number one cause of this type of failure.

Check the $R / W^{*}$ signal to all RAMs. $R / W^{*}$ is "anded" with the system clock at U2nph. Make sure that at every RAM is getting this signal.

Check the gating of the data bus. Since the data bus is bi-directional, only half of the drivers in U8,9,l0 \& ll should operate at a time (one half for either direction). The enable for these drivers comes from the $R / W^{*}$ (or inversion of $R / W^{*}$ ) of the 5502 . Check that $R / W^{*}$ makes it through the inverters on U55pl3 \& $12,11 \& 10$. Also check
the drivers $48,9,10 \& 11$.
Check the Chip selects. Check that the 74154 (Ul) is functioning. Make sure $W 17$ receives proper and good levels on it's inputs Al - Al 5 (which come directly from the processor). Also check that it lowers the proper output (see appendix on 74154).

Finally check that all the Chip Selects at the PROMS operate and trace any supicious ones back to the 74154.

Sometimes slow or speed degraded 74 Ls 125 s on the address bus An - All will cause promblems (U5,6 \& 7). Replace any supicious drivers.
3. Component failures will usually occur in the memory components or the processor. Faulty 2102 s should be replaced first and then check that all "unenabled" PROMS/ROMS are not trying to drive the hus (this can done by removing all unenabled chips one by one until the proper data is seen on the bus).

The first eight bytes of every $P R O M / R O M$ are given on a separate sheet (see coded PROM contents). This can provide a useful test of the selection of memory componets if it is possible for the tester to jam the appropriate addresses on the address bus.

Faulty 6502 's are difficult to diagnose unless the debugger has a known working decoder board. Spare decoder boards are available to authorized servicers through Digicomp Reseach.

Other component failures will require diagnosing with the schematic.
III. Error condition when asked to print.

Error conditions which occur during printing arise from three main sources. "Error" will be displayed if:
(l) the processor reads the Examine line high
(2) or if the is some form of software error in the PROM/ROMS
(3) or if the "house dail" (on the keyboard) cannot be read.

The ablity to read the house daily can easily be checked. Many of the printing routines require the knowledge of which house system to use. Errors of this type are common if the rotary
switch which selects the house system cannot be read. To check this, first put in the key sequence: RESET PRINT DATE RECALL. This should print the date l2.31.1899. If this doesn't work refer, to the next paragraph. However, if it does work, then try the sequence: <Shift> CHART ENTER. If this produces an Error condition, then the processor was unable to read the house dail. Check wiring and action of the rotary switch. If it still won't print a chart (but will recall a date), then refer to the schematic.

The DR-70 uses a signal called "Examine" to determine if the cable to the printer is attached. The Examine line (pin 21) is pulled up on the DR-70 mother board. When this signal is low the DR-70 knows that the printer is connected; if its high, then it indicates with "Error" that the printer is not connected. This line is grounded when the printer cable is plugged into the printer (regardless to whether the printer is powered). The line is sensed on data bit 6 of the $D R-70$ by reading location 6800 (Ul6p9,8 \& 10). Check that the 74 Ls 125 (Ul6p4,5 \& 6;8,9 \& 10) driver is being enable by the address and also check the driver for proper functioning.

The last possibility is a software error. Check that all the Chip Selects work. Contact Digicomp for any possible software errors.

## V. Bombs when Printing

Bombing during printing arises from four principal sources.
(1) The DR-70 is receiving no Acknowledges from the printer.
(2) The printer cable is broken
(3) The Printer is not responding to the DR-70's Ready signal or the printer is broken.
(4) Software problem

During printing, the display interrupts are masked. So it is possible that the $D R-70$ is in fact running normally if it appears to bomb when printing but that the $D R-70$ is trying to finish a line of print. Thus what may appear to be a problem with the $D R-70$ may in fact be a problem with the printer.

Check that the printer will do a line feed upon receipt of a ready signal from the DR-70. If it won't do the line feed, move the print head to the right of the home position and turn the power on. The printer should come to rest at the home postion.

Refer to the printer debug instructions if the printer will not do the line feed.

Next check that the acknowledge pulses are coming from the printer (USSpll). Meanwhile check that Ul6p5 \& $s$ are also receiving acknowledges.

Finally the software driving the printer could be in error. See if any of the print functions (i.e. PRINT DATE RECL, CHART ENTER, TABLE ENTER, etc.). All possible soteware promblems will have to be fixed by Digicomp.
IV. Display Jams in a Meaningful State when Reset

If the display jams into a meaningful state on Reset (see DR-70 Users Manual) a key is probably shorted. One way to isolate key is to turn on the DR-70 and Reset. Note the lights that come on. Now lock "ON" the shift key and Reset. By comparing the lights that come on when shifted and unshifted, usually the shorted key can be deduced (i.e. by noting the shifted and unshifted meaning of each key). Another method is to check each key with a meter or bug-light to see which key might be shorted.

```
Simple DR-70 Repair techinques
```

Recommended tools for DR-70 repair
(1) \#6 hex driver or Alan wrench
(2) small flat screwdriver
(3) chip extractors
(4) soldering iron and solder
(5) solder wick or solder removal tool
(5) $1 / 4$ " socket wrench or spanner
(7) wire cutters
(8) Freon solvent
(9) isopropal alcohol

NOTE - OPENING THE MACHINE WILL VOID YOUR WARRANTY UNLESS OTHERWISE INSTRUCTED BY DIGICOMP.

## A. Opening the machine

1. The DR-70 is sensitive to static electricity, so when working on the machine do not wear sweaters, nylon shirts, rubber-soled shoes or material that creates static charges.
2. A \#6 hex driver is used to undo the 6 stress bolts located around the aluminum valence and fan. (Note: 1 bolt located under warning sticker).
3. Tape over name plate to avoid scratching.
4. Lift top cover up and lean it towards the back - it should be stabilized in this position.
B. Techniques of repair for intermittant failures or lost data (but machine responds to reset).
5. Check operators manual for proper key sequence.
6. Follow opening instructions (\#A). Once open, firmly press each chip to reseat their contacts in the sockets.
7. Replace cover and observe behavior. If intermittant behavior still occurs, repeat procedure \#1.
8. If machine continues to behave intermittantly, purchase some spray contact cleaner (a freon solvent which leaves no residue or oil). Again open the machine. With chip extractors or a small screwdriver, lift the chips partially out of their sockets. Spray the contact cleaner on the chips legs and reseat the chips by pushing them in. Check that none of the chips legs are bent or out of the socket. (Note: if a chip should accidently come out of its' socket, it must be replaced in the same direction).
9. Replace cover and observe machines behavior.
C. Replacement of LEDS
10. Disconnect 50 pin card edge connector from keyboard - avoid stressing cable. The epoxy seal can be broken with a small screwdriver or knife.
11. Lay top upside down. Four sets of $\frac{1}{4}$ " nuts should now be seen on the back side of the display PC board. Remove the nuts and washers. IMPORTANT: Note the number of, and order of the spacing washers. (See \#5)
12. Turn top cover over. Mylar display screen may now be lifted off. Locate defective LED.
13. Now, unsolder old LED and discard. Noting that the direction of the notch in LED lens aligns with the other LEDS (towards the left, looking from the top). Solder LED in FLUSH to the PC board. Do not hold solder iron on the LED lead for longer than 2 seconds. Overheating will cause internal damage to the LED.
14. Screw the display screen and PC board back together with the same exact number of washers (see diagram below). If the LEDS get too close to the screen, heat from LED will cause the mylar to melt.

15. Replace ribbon cable, set top down on bottom in place. Turn machine on, then verify that new LED is working. Turn machine off, then close the machine (see "Sealing up the machine").

## D. Replacement of keyboard switches

1. Disconnect 50 pin card edge connector from keyboard - avoid stressing the ribbon cable. (See Cl).
2. Remove keytop on suspect key. Usually a small screwdriver working from the bottom of keytop, pulling up, will do.
3. After locating key on backside of PC board, remove the solder from the two contact holes. Free key by pulling from top. Make sure both the contact holes and the mounting guide holes (2) are free of obstructions.
4. Gently ease new key switch into hole and solder. It is important to note the following:
(i) the switch contacts should be centered in PC board holes and the switch must be seated flat to prevent the key tops from rubbing against one another.
(ii) Hold solder iron on switch leads for no longer than 2 seconds as the internal mechanism can overheat.
5. Replace ribbon cable and keytop. Verify proper functioning of keyswitch. Seal machine, if working.

## E. Procedure if füse blaws repeatediỳ

1. Check that fuse is $3 / 4$ amp AGC fuse.
2. Open machine (See \#A). Carefully look for loose shards in $+5 v$ circuitry on motherboard. Check 120 v wiring for loose or shorting connections. Check operation.
3. Cut 120 v wire to fan. Check operation. If it works-replace fan (all parts available through Digicomp). If not - reconnect wire.
4. Cut 120 v wire to power supply. Check operation by observing fan.
(i) If fan runs, go to \#5.
(ii) If fan doesn't run - disconnect line filter and bypass it. Reconnect power supply and check operation. Should it work, replace filter. If it doesn't, recheck wiring for shorts.
5. Cut yellow wire to 7805 regulator. Check operation of fan. If now working, replace 7805. If not, check power supply PC board and motherboard for shards in $+5 v$ power busses.
6. If the machine continues to blow fuses, call Digicomp at (607) 273-5900.
F. Sealing machines

PLEASE FOLLOW THIS CHECKLIST

1. Make sure power supply, motherboard, keyboard, and display are all screwed down tight.
2. Check power supply output. It should be $+5.05 v$. If not, adjust pot.
3. Check master clock (only if you have a calibrated oscilloscope). It should have a period of 1.25 us. The clock pot is located on the power supply side of the CPU module.
4. Vacuum machine thoroughly to remove any loose debris. If inside is dusty, wipe it clean using isopropal alcohol.
5. While the machine is open an easy and helpful maintenance procedure is to push down on all the chips to ensure that they are seated firmly in their sockets.
6. Fit top and bottom together. Pick up and shake machine, meanwhile listening to hear loose material inside enclosure.
7. With cover still on, check for proper functioning. Then insert and tighten stress bolts.

## DR-70 Parts List revision B


U5； ..... 2.71 ！
1157 ..... 2.71 ！
＇15？．．．．．．．．．．．．．．．．．．．．．． 2.715
1150 ．．．．．．．．．．．．．．．．． $2.711^{\circ}$
1in ．．．．．．．．．．．．．．．．．．． 2715
：1\％1 ..... 2.716
リフィ ..... $74 \cap 4$
リー5 ..... 7404
！ちた。7414




## All Versions

```
(1) Cut trace from Ulp3 to pot Plp3
    Patch pot Pip3 to U64p6
(2) Cut trace from U65p2 to U40-47p3
    Patch U65pl3 to U40-47p3
(3) Cut trace from Ul5pl3 to Ul5pll
    Patch Ul5pl3 to Ul5p7
(4) Cut +5v trace to pin l of the 50 pin connector
    Patch 22 Ohm resistor from +5V to pin l of the 50 pin connector
(5) Cut trace to Ul6p6
    Patch U9p3 to Ul6p6
```

Version 6B (non-Transit search)
(6) Cut trace from U2lp6 to U60p20 \& 18
(7) Cut trace to U2lp9
Patch U2lp9 to U2lplo
(8) Patch U17p4 to U60p2O \& 18

## Version 8B (Transit Search)

(6) Cut trace from U2lp6 to U60p20 \& 18
(7) Cut trace to U21p9

Patch U2lp9 to U2lp10
(8) Cut trace from U54p20 to U54p18

Cut trace from U54p20 to U19p3
Cut trace from U54p2l to U54p24
Patch Ul7pl5 to U54p20 \& 21
(9) Cut trace from U49pl8 \& 20 to U19pll

Patch U17p3 to U49p20 \& 18
(10) Cut trace from U17pl to U43pl3

Patch U43pl3 to U19pll
(11) Cut trace from Ul7pl5 to U19p2 \& 13

Patch Ul7pl to Ul9p2 \& 3
Version 8B Patches con't
(12) Cut trace from Ul5pl8 to U5lp20
Patch U19p3 to U51p20
(13) Cut trace from U51p24 to U51p21
Patch U43p3 to U5lp21
(14) Cut trace from U5lpl8 to Ul8pll
Patch U5lpl8 to U5lp24
(15) Cut trace from U56p24 toU56p21
Cut trace from U56pl8 to U56p20
Patch Ul7p10 to U56p20 \& ..... 21
(16) Cut trace from U61p21 to U6lp24
Cut trace from U6lp20 to U6lpl8
Patch U6lp20 to U6lp21
(17) Cut trace from U60p2l to U60p24
Cut trace from U60p20 to U60pl8
Cut trace from Ul7pl4 toUl9p5 \& ..... 10
Patch Ul7pl4 to U60p20 \& 21
(18) 'Patch U17p4 to Ul9p5 ..... 10
(19) Patch U18pl2 to U60pl8 to U61pl8 to U56pl8 to U54pl8

## DR-70 address to PROM correlation Version 8B (Transit Search)

| Address | physical | assig | ment |
| :---: | :---: | :---: | :---: |
| 0000-03FF | U40-47 | RAM | (2102) |
| 0400-0BFF | unused |  |  |
| OC00-0FFF | U51 | RAM | (4118) |
| 1000-17FF | U52 | PROM |  |
| 1800-1FFF | U57 | PROM |  |
| 2000-27FF | unused |  |  |
| 2800-2FFF | U49 | PROM |  |
| $3000-37 \mathrm{FF}$ | U50 | PROM |  |
| $3800-3 \mathrm{FFF}$ | U55 | PROM |  |
| 4000-4FFF | display |  |  |
| 5000-5FFF | display |  |  |
| $6000-67 \mathrm{FF}$ | unused |  |  |
| $6800-6 \mathrm{FFF}$ | printer |  |  |
| 7000-7FFF | unused |  |  |
| 7000-7FFF | unused |  |  |
| $8000-8 \mathrm{FFF}$ | keyboard |  |  |
| 9000-9FFF | U56 | ROM |  |
| A $000-\mathrm{A} 7 \mathrm{FF}$ | U58 | PROM |  |
| A800-AFFF | U59 | PROM |  |
| B000-BFFF | U61 | ROM |  |
| C000-CFFF | U60 | ROM |  |
| D000-DFFF | U54 | PROM |  |
| E000-E7FF | U48 | PROM |  |
| E800-EFFF | U53 | PROM |  |
| F000-FFFF | mapped i | FFF |  |

## DR-70 PROM Map

DR-70 Motherboard<br>Top View<br>Version 8.B<br>Transit Search



DR-70 PROM Map

DR-70 Motherboard Top View
Version 6.B non-Transit Srarch

5ydons 3 mod


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1.0 The Model 7040 printers are designed to require a minimum of maintenance and service. This section provides instructions and procedures for both cleaning and maintenance of the printer as well as a troubleshooting guide to aid in problem analysis.
1.1 Required Tools and Supplies

The following items are recommended for printer maintenance and major repairs. Common hand tools are also required.

1. Volt-Ohm Meter
2. Feeler Gauges
3. Grease (IBM \#23 or Lubriplate \#70)
4. Isopropyl alcohol
5. Cleaning Rags
6. Cotton Swabs

### 2.0 Maintenance

The frequency at which normal maintenance (cleaning, lubrication and inspection) should be performed when the printer is installed in a clean office environment should normally be one year. However, if the printer is subjected to heavy usage and/or a dirty environment, the frequency should be increased as required.
2.1 Cleaning
A. Clean printer thoroughly using alcohol, rags and cotton swabs to remove hardened grease, ink and dirt.
B. Rotate platen and clean with an alcohol soaked rag.
C. Remove head and clean head and head shaft with alcohol.
D. Reassemble head to printer.
E. Apply a small amount of recommended grease to head shaft.

### 2.2 Inspection

A. Check for print wire movement on all seven solenoids by depressing wire end and noting return to rest position.
B. Check for easy movement of the head assembly on the print head assembly shaft.
C. Inspect the ribbon reverse mechanism for proper movement. Manually operate the ribbon feed pawl back and forth to check operation. Be sure that both springs are securely in place and of equal tension.
D. Inspect the printer for any loose or missing screws.
E. Check for excessive vertical movement of print head drive cam. Usual cause is a loose paper feed motor. Rotate the drive cam toward the platen and listen for an audible click of the home micro switch.
(1) The position of the switch arm roller at the click should be half-way up/down the high dwell ramp of the cam.
(2) The switch activating arm should be straight with no kinks.
F. Check the paper feed cam for tightness on motor shaft (looseness may be a result of a bad cam or improper motor shaft configuration).
G. Rotate the paper feed cam clockwise (toward platen) and check for smooth operation. Binding indicated bad fit between cam and pawl arm. Listen to the paper feed micro switch for an audible click. (1) The position of the switch arm roller at the click should be half-way up/down the high dwell ramp of the cam.
(2) The switch activating arm should be straight with no kinks.
H. Check that right end of print platen is not loose with regard to platen drum.
I. Check that the platen spins freely when paper release lever is depressed and ratchet pawl is disengaged.
J. Check that the plastic paper guide strip edge touches the platen drum over its entire length.
K. Check that the plastic paper guide strip is secured to the extrusion mounting surface.
L. Check that the print head moves freely along its shaft and within the drive cam groove while turning the drive cam toward the platen.
M. Place the ribbon in place and check that it does not snag throughout its path. Check ribbon spool drag.
N. Depress paper release lever and check that bar and feed roll leave the print platen.
0 . Check that two ply form may be loaded when paper release lever is depressed.
P. Check that paper guides are not loose on main frame extrusion and that they may be moved with moderate pressure for adjustment.
Q. Inside guide edges should be flush with inside edges of sides of supply mechanism.

### 2.3 Lubrication

Recommended lubricants are IBM \#23 or Lubriplate $\# 70$. After cleaning, the following areas should require lubrication:
a. Head shaft
b. Groove in main drive cam
c. Guide in extrusion in which the "tail" of the print head travels. Only a small amount of lubricant, enough to create a film, is needed in each location.
The following areas may also require lubrication, if the area has been wiped free of lubricant:
a. Paper feed cam and pawl
b. Ribbon feed pawl
c. Platen detent teeth
d. Platen ratchet teeth
e. Pressure roll release lever

### 3.0 Repair and Adjustments

### 3.1 Troubleshooting Guide <br> The Mechanism Troubleshooting Table should provide a means of determining the cause of any malfunction and the proper corrective action.

### 3.2 Repair Techniques

### 3.2.1 Head Replacement and Adjustments

Remove the old head by removing the two screws which hold the head shaft in place and then pulling the head shaft and head up out of the mechanism. To free the head cable the cable tie must be cut.
Remove shaft from old head and slide it into the bearings on the replacement head. The head assembly should move freely along the shaft. To check for free movement hold the head assembly with the shaft vertical and release the shaft. It should drop through the head assembly. If it does not move freely, hold the head assembly on one end of the shaft and tap the other end firmly on a hard surface. This will cause the head assembly bushings to seat perpendicular to the shaft. The replacement head can now be installed in the printer in the reverse manner of its removal. When tightening the screws in the head shaft, hold the shaft forward to minimize the distance between it and the platen. Insure that the drive pin on the bottom of the head mates with the groove in the drive cam and that the "tail" on the head is placed in the guide slot on the extrusion. Lubricate according to paragraph 2.3 prior to operation.

### 3.2.2 Switch Replacement

3.2.2.1 Line Feed Switch (FPF Mechanism Only)

To replace the line feed switch it is first necessary to remove the left side frame. This is done by first removing the twirler from the platen. Then remove the three screws which secure the side frame to the main extrusion as well as the screw in the left side of the head shaft. Once these screws are removed the left side frame can be pulled away from the printer. The line feed switch can now be removed from the side frame by lifting it off its mounting tabs. The replacement switch is then pressed onto those tabs and a piece of adhesive-backed foam is placed on the switch where it contacts the main extrusion. The side frame can now be replaced taking care to align the platen pressure roll support bracket, head shaft and main drive cam to their proper positions.

### 3.2.2.2 Print Switch

To replace the print switch it is first necessary to remove the right side frame. This is done by removing the three screws that secure the side frame to the main extrusion as well as removing the screw in the right hand side of the head shaft. The side frame can now be removed, taking care to disengage the motor pulley from the belt. Holding the detent lever down (forward) will aid in the removal of the frame. The print switch can now be removed by lifting it off its mounting tabs. The replacement switch is then pressed onto these tabs and adhesivebacked foam is applied to the switch where it contacts the main extrusion. The printer is then reassembled taking care to align the main drive cam and platen properly. During reassembly the detent lever must be held down to avoid interference with the ratchet teeth on the platen, and the motor pulley must have the belt placed back on it.
3.2.3 Belt TensionDrive belt tension is adjusted by loosening the two motor mountingscrews and moving the motor forwards or backwards to decrease orincrease the belt tension. The belt tension should be set so thatwhen the head is prevented from moving, the motor stalls and thebelt does not $\operatorname{cog}$ over the motor pulley. Excessive belt tensionwill cause the drive cam to bind and the motor to stall. Too littlebelt tension will result in excessive variation in character widths.

### 3.2.4 Head to Platen Air Gap

If the printhead has been replaced due to solenoid failure or if the printer has been disassembled, the printhead to platen air gap should be checked prior to using the printer. Improper air gap can result in damage to the printhead. The printhead to platen air gap may be checked by using feeler gauges. Prior to checking the air gap it is necessary to remove the paper and ribbon from the printer.
The head to platen air gap should be .012" to .015". If the air gap is out of the allowable range, some adjustment can be made by loosening the screws in the head shaft and pushing it in the desired direction. If this does not yield sufficient change then it is necessary to loosen the side frames and move them upward or downward in relation to the main extrusion

MECHANISM TROUBLESHOOTING GUIDE

| SYMPTOM | POSSIBLE CAUSE | CORRECTIVE ACTION |
| :---: | :---: | :---: |
| 1. Print motor starts but doesn't stop. No printing occurs. | 1. Broken print switch <br> 2. Broken switch wire <br> 3. Misadjusted print switch | 1. Replace switch <br> 2. Check all wires on print switch for electrical continuity <br> 3. Verify switch transfers when roller on switch actuator transfers between high and low positions on the cam pulley. |
| 2. Once started, the line feed motor never stops | 1. Broken line feed switch <br> 2. Broken switch wire <br> 3. Misadjusted line feed switch | 1. Replace switch <br> 2. Check all wires for electrical continuity <br> 3. Verify switch transfers when roller on switch actuator transfers between high and low positions on switch eccentric. |
| ```3. Print Motor stalls``` | 1. Excessive Belt tension <br> 2. Bad crimp on one or more A.C. motor wires <br> 3. Bad A.C. motor | 1. Adjust belt tension <br> 2. Visually verify crimps are good <br> 3. Replace motor |
| 4. Occasional missed line feed | 1. Bad line feed switch <br> 2. Misadjusted line feed switch | 1. Replace switch <br> 2. Verify switch roller rides on lower surface of paper feed eccentric when in the home position, and that switch transfers when switch roller moves from low surface of the paper feed eccentric |
| 5. Uneven line feeds | 1. Paper slippage <br> 2. Metal wear plate broken off line feed pawl <br> 3. Platen not detenting properly | 1. Clean platen and/or replace pressure roll springs <br> 2. Replace pawl <br> 3. Check for defective detent mechanism. |

MECHANISM TROUBLESHOOTING GUIDE (Continued)

| SYMPTOM | POSSIBLE CAUSE | CORRECTIVE ACTION |
| :---: | :---: | :---: |
| 6. Line feed motor stalls | 1. Bad A.C. motor <br> 2. Line feed pawl binds on eccentric <br> 3. Bad wire to pin connections on A.C. motor wires <br> 4. Platen won't turn or turns with difficulty | 1. Replace A.C. motor <br> 2. Replace pawl or eccentric <br> 3. Check crimp on wire <br> 4. Inspect detent mechanism for source of jam and replace or repair as required |
| 7. Missing dots | 1. Ribbon not properly installed <br> 2. Bad print solenoid <br> 3. Head to platen gap excessive | 1. Instail ribbon correctly <br> 2. Replace head <br> 3. Verify head to platen gap is . $015^{\prime \prime} \pm .003^{\prime \prime}$ |
| 8. Excessive variation in character size | 1. Belt tension too loose <br> 2. Start to print distance (distance from lst dot position to inside of side frame) is out of tolerance (.965 $\pm .060^{\prime \prime}$ ) <br> 3. Head binding on shaft | 1. Tighten belt tension <br> 2. Adjust print switch to correct start to print location. <br> 3. Align head bearings on shaft. Lubricate shaft. |

PART LIST 7040 J

| ITEM NO. | PART NO. | DESCRIPTION Qu | QUANTITY |
| :---: | :---: | :---: | :---: |
| 1. | 101411-004 | ASSY-A.C. MOTOR* | 1 |
| 2. | 100576 | TWIRLER | 1 |
| 3. | 101528 | BRACKET-RIBBON TENSION | 1 |
| 4. | 101225 | SPACER-RIBBON POST | 2 |
| 5. | 102378 | SPRING-RIBBON TENSION | 1 |
| 6. | 101383 | ASSY-FRAME LEFT SIDE | 1 |
| 7. | 100625 | ASSY-RIBBON POST | 2 |
| 8. | 102539 | SPRING-RIBBON SPOOL BRAKE | 2 |
| 9. | 101221 | ASSY-RIBBON \& SPOOLS | 1 |
| 10. | 101231-005 | SPRING-EXTENSION | 1 |
| 11. | 101035 | ECCENTRIC PAPER FEED \& SWITCH* | ${ }^{*} 1$ |
| 12. | 101077 | ASSY-PAWL FEED | 1 |
| 13. | 100272-005 | SWITCH-ELECTRICAL | 2 |
| 14. | 102414-001 | FOAM-TAPE | A/R |
| 15. | 101349 | SHIM WASHER | 1 |
| 16. | 101013 | MODIFICATION-PLATEN | 1 |
| 17. | 101073 | ASSY-CAM DRIVE | 1 |
| 18. | 101065 | GUIDE-PAPER | 2 |
| 19. | 101051 | MODIFICATION-EXTRUSION | 1 |
| 20. | 101392 | DECAL | 1 |
| 21. | 100543-004 | TAPE | A/R |
| 22. | 101057 | GUIDE-PAPER | 1 |
| 23. | 101918 | SPRING-PRESSURE ROLL | 2 |
| 24. | 101003 | SUPPORT-PRESSURE ROLL | 1 |
| 25. | 101067 | ASSY-ROLLER PRESSURE | 1 |
| 26. | 101525 | SHAFT-HEAD | 1 |
| 27. | 101117 | RIBBON FEED PAWL | 1 |
| 28. | 100559 | SPRING-RIBBON FEED PAWL | 2 |
| 29. | 101137-002 | ASSY-HEAD 7 WIRE | 1 |
| 30. | 102409 | DEFLECTOR-RIBBON HEAD | 1 |
| 31. | 101005 | BLADE-TEAR OFF 40 COL . | 1 |
| 32. | 101086 | PULLEY-DRIVE MOTOR | 1 |
| 33. | 101229-001 | BELT | 1 |

## PART LIST 7040 J (CONT INUED)



* Use on Fast Paper Feed version only.






| IC s |  |
| :---: | :---: |
| Part II | Desuption |
| Ul | 7402 |
| $U 2$ | 7402 |
| $U 3$ | 7406 |
| $U 4$ | 7402 |
| $U 5$ | 7474 |
| $U 6$ | 555 |

Fuses

| Part $=$ | Description |
| :---: | :---: |
| F1-7 | 1 amp |
| F8 | 1.5 amps |

Misc.

Semiconductors

| art | Description |
| :--- | :--- |
| $1-7$ | $2 N 6039$ |
| $Q 8$ | LM 340 |
| 29 | TQ4-0063 |





Ken's Printer Wiring Diagram


## DATA SHEET

## R6500 MICROPROCESSORS (CPU's)

## SYSTEM ABSTRACT

The 8 -bit R6500 microcomputer system is produced with N Channel, Silicon Gate technology. Its performance speeds are enhanced by advanced sẏstem architecture which enables multiple addressing. Its innovative architecture results in smaller chips the semiconductor threshold to cost-effectivity. System costeffectivity is further enhanced by providing a family of 10 softwarecompatible microprocessor (CPU) devices which are described in this document. Rockwell also provides memory and I/O devices that further enhance the cost-effectivity of the R6500 microcomputer system . . . as well as low-cost design aids and documentation.

R6500 MICROPROCESSOR (CPU) CONCEPT
Ten CPU devices are available. All are software-compatible. They provide options of addressable memory, interrupt input, on-chip clock oscillators and drivers. All are bus-compatible with earlier generation microprocessors like the M6800 devices.

The family includes six microprocessors with on-board clock oscillators and drivers and four microprocessors driven by external clocks. The on-chip clock versions are aimed at high performance, low cost applications where single phase inputs, crystal or RC inputs provide the time base. The external clock versions are geared for the multiprocessor system applications where maximum timing control is mandatory. All versions of the microprocessors are available in 1 MHz and 2 MHz maximum operating frequencies.

MEMBERS OF THE R6500 MICROPROCESSOR (CPU) FAMILY


FEATURES

- Single five voit supply
- N channel, silicon gate, depletion load technology
- Eight bit parallel processing
- 56 Instructions
- Decimal and binary arithmetic
- Thirteen addressing modes
- True indexing capability
- Programmable stack pointer
- Variable length stack
- Interrupt capability
- Non-maskable interrupt
- Use with any type or speed memory
- Bidirectional Data Bus
- Instruction decoding and control
- Addressable memory range of up to 65 K bytes
- "Ready" input
- Direct memory access capability
- Bus compatible with M6800
- Choice of external or on-board ciocks
- 1 MHz and 2 MHz operation
- On-the-chip clock options
- External single clock input
- RC time base input
- Crystal time base input
- 40 and 28 pin package versions
- Pipeline architecture


## Ordering Information

| Order Number | Package Type | Frequency Option | Temperature Range |
| :---: | :---: | :---: | :---: |
| R65 X $\times$ P | Plastic | 1 MHz | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |
| R65 $\times$ XC | Ceramic | 1 MHz | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |
| R65 $\times$ PAP | Plastic | 2 MHz | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |
| R65 $\times$ PAC | Ceramic | 2 MHz | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |

Where $X X$ is $02,03,04,05,06,07,12,13,14$, or 15 depending upon the features desired.

## R6500 Signal Description

## Clocks $\left(\phi_{1}, \phi_{2}\right)$

The R651X requires a two phase non-overlapping clock that runs at the $V_{C C}$ voltage level.
The R650X clocks are supplied with an internal clock generator. The frequency of these clocks is externally controlled. Details of this feature are discussed in the R6502 portion of this data sheet.

Address Bus ( $A_{0}-A_{15}$ ) (See sections on each micro for respective address lines on those devices.)
These outputs are TTL compatible, capable of driving one standard TTL load and 130 pF .

## Data Bus ( $D_{0}-D_{7}$ )

Eight pins are used for the data bus. This is a bidirectional bus, transferring data to and from the device and peripherals. The outputs are tri-state buffers capable of driving one standard TTL load and 130 pF .

## Data Bus Enable (DBE)

This TTL compatible input allows external control of the tri-state data output buffers and will enable the microprocessor bus driver when in the high state. In normal operation DBE would be driven by the phase two $\left(\phi_{2}\right)$ clock, thus allowing data output from microprocessor only during $\phi_{2}$. During the read cycle, the data bus drivers are internally disabled, becoming essentially an open circuit. To disable data bus drivers externally, DBE should be held low.

## Ready (RDY)

This input signal allows the user to single cycle the microprocessor on all cycles except write cycles. A negative transition to the low state during or coincident with phase one $\left(\phi_{1}\right)$ will halt the microprocessor with the output address lines reflecting the current address being fetched. This condition will remain through a subsequent phase two $\left(\phi_{2}\right)$ in which the Ready signal is low. This feature allows microprocessor interfacing with low speed PROMS as well as fast (max. 2 cycle) Direct Memory Access (DMA). If Ready is low during a write cycle, it is ignored until the following read operation.

## Interrupt Request ( $\overline{\mathbf{R O}}$ )

This TTL level input requests that an interrupt sequence begin within the microprocessor. The microprocessor will complete the current instruction being executed before recognizing the request. At that time, the interrupt mask bit in the Status Code Register will be examined. If the interrupt mask flag is not set, the microprocessor will begin an interrupt sequence. The Program Counter and Processor Status Register are stored in the stack. The microprocessor will then set the interrupt mask flag high so that no further interrupts may occur. At the end of this cycie, the program counter low will be loaded from address FFFE, and program counter high from location FFFF, therefore transferring program control to the memory vector located at these addresses. The RDY signal must be in the high state for any interrupt to be recognized. A $3 K \Omega$ external resistor should be used for proper wire-OR operation.

## Non-Maskable Interrupt (NMI)

A negative going edge on this input requests that a non-maskable interrupt sequence be generated within the microprocessor.
$\overline{\text { NMI }}$ is an unconditional interrupt. Following completion of the current instruction, the sequence of operations defined for IRQ will be performed, regardless of the state interrupt mask flag. The vector address loaded into the program counter, low and high, are locations FFFA and FFFB respectively, thereby transferring program control to the memory vector located at these addresses. The instructions loaded at these locations cause the microprocessor to branch to a non-maskable interrupt routine in memory.
$\overline{\text { NMI }}$ aiso requires an external $3 \mathrm{~K} \Omega$ register to $V_{\mathrm{CC}}$ for proper wire-OR operations.
Inputs $\overline{\mathrm{RQ}}$ and $\overline{\mathrm{NMI}}$ are hardware interrupts lines that are sampled during $\phi_{2}$ (phase 2) and will begin the appropriate interrupt routine on the $\phi_{1}$ (phase 1) following the completion of the current instruction.

## Set Overflow Flag (S.O.)

A NEGATIVE going edge on this input sets the overflow bit in the Status Code Register. This signal is sampled on the trailing edge of $\phi_{1}$.

## SYNC

This output line is provided to identify those cycles in which the microprocessor is doing an OP CODE fetch. The SYNC line goes high during $\phi_{1}$ of an OP CODE fetch and stays high for the remainder of that cycle. If the RDY line is pulled low during the $\phi_{1}$ clock pulse in which SYNC went high, the processor will stop in its current state and will remain in the state until the RDY line goes high. In this manner, the SYNC signal can be used to control RDY to cause single instruction execution.

## Reset

This input is used to reset or start the microprocessor from a power down condition. During the time that this line is heid low, writing to or from the microprocessor is inhibited. When a positive edge is detected on the input, the microprocessor will immediately begin the reset sequence.
After a system initialization time of six clock cycles, the mask interrupt flag will be set and the microprocessor will load the program counter from the memory vector locations FFFC and FFFD. This is the start location for program control.
After $V_{C C}$ reaches 4.75 volts in a power up routine, reset must be held low for at least two clock cycles. At this time the R/W and (SYNC) signal will become valid.
When the reset signal goes high following these two clock cycles, the microprocessor will proceed with the normal reset procedure detailed above.

ACCUMULATOR ADDRESSING - This form of addressing is represented with a one byte instruction, implying an operation on the accumulator.

IMMEDIATE ADDRESSING - In immediate addressing, the operand is contained in the second byte of the instruction, with no further memory addressing required.

ABSOLUTE ADDRESSING - In absolute addressing, the second byte of the instruction specifies the eight low order bits of the effective address while the third byte specifies the eight high order bits. Thus, the absolute addressing mode allows access to the entire 65 K bytes of addressable memory.

ZERO PAGE ADDRESSING - The zero page instructions allow for shorter code and execution times by only fetching the second byte of the instruction and assuming a zero high address byte. Careful use of the zero page can result in significant increase in code efficiency.

INDEXED ZERO PAGE ADDRESSING - ( $X, Y$ indexing ) - This form of addressing is used in conjunction with the index register and is referred to as "Zero Page, $X$ " or "Zero Page, $Y$ ". The effective address is calculated by adding the second byte to the contents of the index register. Since this is a form of "Zero Page" addressing, the content of the second byte references a location in page zero. Additionally due to the "Zero Page" addressing nature of this mode, no carry is added to the high order 8 bits of memory and crossing of page boundaries does not occur.

INDEXED ABSOLUTE ADDRESSING - ( $X, Y$ indexing $)$ - This form of addressing is used in conjunction with $X$ and $Y$ index register and is referred to as "Absolute, $X$ ", and "Absolute, $Y$ ". The effective address is formed by adding the contents of $X$ or $Y$ to the address contained in the second and third bytes of the instruction. This mode allows the index register to contain the index or count value and the instruction to contain the base address. This type of indexing allows any location referencing and the index to modify multiple fields resulting in reduced coding and execution time.

IMPLIED ADDRESSING - In the implied addressing mode, the address containing the operand is implicitly stated in the operation code of the instruction.

RELATIVE ADDRESSING - Relative addressing is used only with branch instructions and establishes a destination for the conditional branch.

The second byte of the instruction becomes the operand which is an "Offset" added to the contents of the lower eight bits of the program counter when the counter is set at the next instruction. The range of the offset is -128 to +127 bytes from the next instruction.

INDEXED INDIRECT ADDRESSING - In indexed indirect addressing (referred to as (Indirect, X)), the second byte of the instruction is added to the contents of the $X$ index register, discarding the carry. The resuit of this addition points to a memory location on page zero whose contents is the low order eight bits of the effective address. The next memory location in page zero contains the high order eight bits of the effective address. Both memory locations specifying the high and low order bytes of the effective address must be in page zero.
INDIRECT INDEXED ADDRESSING - in indirect indexed addressing (referred to as (Indirect), Y), the second byte of the instruction points to a memory location in page zero. The contents of this memory location is added to the contents of the $Y$ index register, the result being the low order eight bits of the effective address. The carry from this addition is added to the contents of the next page zero memory location, the result being the high order eight bits of the effective address.

ABSOLUTE INDIRECT - The second byte of the instruction contains the low order eight bits of a memory location. The high order eight bits of that memory location is contained in the third byte of the instruction. The contents of the fully specified mernory location is the low order byte of the effective address. The next memory location contains the high order byte of the effective address which is loaded into the sixteen bits of the program counter.

## INSTRUCTION SET - ALPHABETIC SEQUENCE

ADC Add Memory to Accumulator with Carry
AND "AND" Mernory with Accumulator
ASL Shift left One Bit (Memory or Accumulator)
BCC Branch on Carry Clear
BCS Branch on Carry Set
BEQ Branch on Result Zero
BIT Test Bits in Memory with Accumulator
BMI Branch on Result Minus
BNE Branch on Result not Zero
BPL Branch on Resuit Plus
BRK Force Break
BVC Branch on Overflow Clear
BVS Branch on Overflow Set

CLC Clear Carry Flag
CLD Clear Decimal Mode
CLI Clear Interrupt Disable Bit
CLV Clear Overflow Flag
CMP Compare Memory and Accumulator
CPX Compare Memory and Index $X$
CPY Compare Memory and Index $Y$

DEC Decrement Memory by One
DEX Decrement Index $X$ by One
DEY Decrement Index $Y$ by One .
EOR "Exclusive-or" Memory with Accumulator
INC Increment Memory by One
INX Increment Index $X$ by One
INY Increment Index $Y$ by One

JMP Jump to New Location
JSR Jump to New Location Saving Return Address
LDA Load Accumulator with Memory
LDX Load Index $X$ with Memory
LDY Load Index $Y$ with Memory
LSR Shift One Bit Right (Memory or Accumulator)
NOP No Operation
ORA "OR Memory with Accumulator
PHA Push Accumulator on Stack
PHP Push Processor Status on Stack
PLA Pull Accumulator from Stack
PLP Pull Processor Status from Stack
ROL Rotate One Bit Left (Memory or Accumulator)
ROR Rotate One Bit Right (Memory or Accumulator)
RT1 Return from Interrupt
RTS Return from Subroutine
SBC Subtract Memory from Accumulator with Borrow
SEC Set Carry Flag
SED Set Decimal Mode
SEI Set Interrupt Disable Status
STA Store Accumulator in Memary
STX Store Index $X$ in Memory
STY Store Index $Y$ in Memory
TAX Transfer Accumulator to Index $X$
TAY Transfer Accumulator to Index $Y$
TSX Transfer Stack Pointer to Index $X$
TXA Transfer Index $X$ to Accumulator
TXS Transfer Index $X$ to Stack Register
TYA Transfer Index $Y$ to Accumulator


TIME BASE GENERATION OF INPUT CLOCK
R6502
R6503, R6504, R6505, R6506, R6507



R6502 Series Mode Crystal Controlled Oscillator


R6503, 04, 05, 06, 07 Series Mode Crystal Controlled Oscillator


R6502 Time Base Generator - RC Network


R6503, 04, 05, 06, 07 Time Base Generation RC Network

Clock Timing - R6502, 03, 04, 05, 06, 07


Clock Timing - R6512, 13, 14, 15


Note: "REF." means Reference Points on clocks.


PROGRAMMING MODEL


## 1 MHz Timing

2 MHz Timing

Clock Timing - R6502, 03, 04, 05, 06, 07

| Chersternts | Symeal | Min | Tvo | Men | Unisa |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Creer Time | ${ }^{\text {Terc }}$ | 1000 | - | - | " |
| $\theta_{\text {alini }}$ Puite Widin imenured at 15 VI | PWHO | 450 | - | 520 | 9 |
| $O_{\text {outin) }}{ }^{\text {Pase, Fail Time }}$ | TR90. ${ }_{0} \mathrm{TFO}_{0}$ | - | - | 10 | 4 |
| Desey Time Berween Coces imentured al I SVI | ${ }^{T}$ | 5 | - | - | n |
| $o_{\text {siouti Puise Widin imasured al I SVi }}$ | PWH0, |  | - | WWH0 oL | ${ }^{*}$ |
|  | $\mathrm{PWHO}_{2}$ | TNH9 $\mathrm{OH}^{40}$ | - | - $\mathrm{WHO}_{\text {a }} \mathrm{ar}^{-10}$ | n* |
| $\theta_{\text {nouti }} \theta_{\text {2IOUT1 }}$ मase, Fall T.me 1 meanured al 0 oV to 2.0 VI <br> (Load + $30 \mathrm{oF}-1 \mathrm{TTL})$ | $\mathrm{T}_{\text {R }} \cdot \mathrm{T}_{\mathrm{F}}$ | - | - | 25 | 9 |

Clock Timing - R6502, 03, 04, 05, 06, 07

| Chersetwietic | 5 rmbat | Min | Tve | Nax | Unis |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Creie Time | ${ }^{\text {Tcre }}$ | 500 | - | - | $n$ |
|  | +WH0 | 240 | - | 250 | ns |
| -olini Pase. Fait Time | TRA ${ }_{0}$ TFA。 | - | - | 10 | $n$ |
| Deisy Time ilermeen Clocks Imenured at 15 V I | ${ }^{T}$ | 5 | - | - | n* |
| O$_{\text {1IOUT1 }}$ Pulse Widith (menured at 1.5 V ) | Рнне, | WWH ${ }_{\text {ol }}{ }^{20}$ | - | WWHol | n* |
|  | ${ }^{\text {WNHO }}$ | TWH ${ }_{\text {On }}{ }^{-0}$ | - | WNH a $_{\text {a }}{ }^{-10}$ | * |
| $\theta_{\text {IIOUT1 }}{ }^{\theta_{210 U T I}}$ Fine. Fall Time (meanured at 0.8 BV to 2.0 V ) <br> (Lasd 30 pFF - 1 TTL) |  | - | - | 25 | * |

Clock Timing - R6512, 13, 14, 15

| Cherseternis | Symbal | Min | Tvo | Mes | Unis |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Cvele T.me | ${ }^{T} \mathrm{Cre}$ | 1000 | - | - | пия |
| Clock Puise Wiath ol | PWH or | 430 | - | - | nue |
| i Meatura at vee 0.2VI 02 | PWH or | 470 |  |  |  |
| Eel T.me imeaurea trom 0.2 V to Vee $\cdot 0.2 \mathrm{Vi}$ | $\mathrm{T}_{F}$ | - | - | 25 | nse |
| Deiay Time between Clocks Mensurec at 02 V 1 | ${ }^{1} 0$ | 0 | - | - | nıe |

Clock Timing - R6512, 13, 14, 15

| Chenctimatic | Symeol | Min | Tv | Max | Unm |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Cvele Time | ${ }^{\text {TCYC }}$ | 500 | - | - | nuec |
| Clock Puise Widin ol <br> (Meawied at Vec-0.2V) ot | оWH OI WH O2 | $\begin{aligned} & 215 \\ & 225 \end{aligned}$ | . - | - | nue. |
| Fall Tume <br> imemured trom 0.2 V to Vee -0.2 VI | $\mathrm{T}_{\mathrm{F}}$ | - | - | 12 | n** |
| Deitey Tume detween Clocks imenured al 0.2V) | $T_{0}$ | 0 | - | - | ater |

Read/Write Timing

| Charmetwime | Symbel | Min | Tye | Man | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Aesa/Werte Setve Time from R6500 | ${ }^{\text {raws }}$ | - | 100 | 200 | $\cdots$ |
| Adgren Setus Time from 月6500 | $T_{A D S}$ | - | 100 | 200 | ns |
| Memorv Aeso Accers Time | ${ }^{\tau_{A C C}}$ | - | - | 575 | a |
| Oata Stabitity Time Period | Tosu | 100 | - | - | $n$ |
| Dasa Hold Time - Mead | ${ }^{7} \mathrm{HR}$ | 10 | - | - | $n$ |
| Dasa Mola Time - Werte | ${ }^{T}$ Hw | 30 | 60 | - | * |
| Data Setivo Time trom a6500 | $T_{\text {mos }}$ | - | 150 | 200 | n |
| ADV, SO Setuo Time | Thoy | 100 | - | - | $n$ |
| SYNC Setua Time fram a6500 | Tsync | - | - | 250 | n* |
| Addess Moia Time | ${ }^{T} \mathrm{Ha}$ | 30 | 60 | - | m |
| A/w Hold T-me | $T_{\text {HRW }}$ | 30 | 60 | - | nt |

Read/Nrite Timing

| Charsetaratie | Sivmbat | Min | Tre | Max | Unis |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Aema/Wrie Setup Time from R6500A | ${ }^{\text {anws }}$ | - | 100 | 150 | m |
| Addreu Setue Time from A6500A | ${ }^{T}$ AOS | - | 100 | 150 | * |
| Memory Read Accens Time | ${ }^{\text {r }}$ ACC | - | - | 300 | $n$ |
| Oata Stability Time Period | ${ }^{\text {rosu }}$ | so | - | - | $\because$ |
| Data Hold Time - Aasd | $T_{\text {He }}$ | 10 | - | - | $*$ |
| Data Hold Time - Winte | ${ }^{\text {rew }}$ | 30 | 60 | - | * |
| Data Setue Time from r6500a | ${ }^{T}$ mos | - | 75 | 100 | $n$ |
| poy s.o. Salud Time | ${ }^{T}$ mor | 50 | - | - | $n$ |
| SYNC Setua Time from R6500A | Tsrnc | - | - | 175 | \% |
| Adders noid Time | ${ }^{\text {HAA }}$ | 20 | 60 | - | * |
| A/w Hota Time | THAW | 30 | 60 | - | m |



## SPECIFICATIONS

## Maximum Ratings

| Rating | Symbol | Value | Unit |
| :--- | :--- | :--- | :--- |
| Supply Voltage | $V_{\text {CC }}$ | -0.3 to +7.0 | Vdc |
| Input Voltage | $V_{\text {in }}$ | -0.3 to +7.0 |  |
| Operating Temperature | $T_{A}$ | 0 to +70 | ${ }^{\circ}$ |
| Storage Temperature | $T_{\text {STG }}$ | -55 to +150 | ${ }^{\circ}$ |
| ${ }^{\circ} \mathrm{C}$ |  |  |  |

This device contains input protection against damage due to high static voltages or electric fields; however, precautions should be taken to avoid application of voltages higher than the maximum rating.

## Electrical Characteristics

$\left(\mathrm{V}_{\mathrm{CC}}=5.0 \pm 5 \%, \mathrm{~V}_{\mathrm{SS}}=0 . \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right)$
$\phi_{1}, \phi_{2}$ applies to R6512, 13, 14, 15, $\phi_{o(i n)}$ applies to R6502, 03, 04, 05, 06 and 07.

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input High Voltage <br> Logic, $\phi_{\text {o(in) }}$ <br> $\phi_{1}, \phi_{2}$ | $\mathrm{V}_{\text {IH }}$ | $\begin{aligned} & v_{\mathrm{SS}}+2.4 \\ & \mathrm{v}_{\mathrm{CC}}-0.2 \end{aligned}$ |  | $\begin{gathered} v_{C C} \\ v_{c C}+0.25 \end{gathered}$ | Vdc |
| $\begin{aligned} & \hline \text { Input Low Voltage } \\ & \qquad \begin{array}{l} \text { Logic, } \phi_{\text {o(in) }} \\ \phi_{1}, \phi_{2} \end{array} \end{aligned}$ | $\mathrm{V}_{\text {IL }}$ | $\begin{aligned} & \mathrm{v}_{\mathrm{SS}} \cdot 0.3 \\ & \mathrm{v}_{\mathrm{SS}}-0.3 \end{aligned}$ |  | $\begin{aligned} & \mathrm{v}_{\mathrm{SS}}+0.4 \\ & \mathrm{v}_{\mathrm{ss}}+0.2 \end{aligned}$ | Vdc |
| Input High Threshold Voltage हES, $\overline{\text { NMI, RDY, } \overline{\mathrm{RO}}, \text { Data, S.O. }}$ | $\mathrm{V}_{\text {IHT }}$ | $\mathrm{V}_{\text {SS }}+2.0$ | - | - | Vdc |
| Input Low Threshold Voltage $\overline{\text { RES }}, \overline{\text { NMI, ROY, }} \overline{\mathrm{RO}}$, Data, S.O. | $\mathrm{V}_{\text {ILT }}$ | - | - | $\mathrm{V}_{\text {SS }}+0.8$ | Vdc |
| Input Leakage Current $\begin{aligned} &\left(\mathrm{v}_{\text {in }}=0 \text { to } 5.25 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=0\right) \\ &\text { Logic (Excl. RDY, S.O. }) \\ & \phi_{1}, \Phi_{2} \\ & \phi_{\text {o(in) }} \end{aligned}$ | $\mathrm{I}_{\text {in }}$ | - | - | $\begin{aligned} & 2.5 \\ & 100 \\ & 10.0 \end{aligned}$ | $\mu \mathrm{A}$ <br> $\mu A$ $\dot{\mu} A$ |
| Three-State (Off State) Input Current $\left(\mathrm{V}_{\text {in }}=0.4 \text { to } 2.4 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=5.25 \mathrm{~V}\right)$ | 'TSI | - | - | 10 | $\mu \mathrm{A}$ |
| Output High Voltage $\begin{aligned} &\left({ }^{(1} \text { LOAD }=-100 \mu \mathrm{Adc}, \mathrm{~V}_{C C}=4.75 \mathrm{~V}\right) \\ & \text { SYNC, Data, AO-A15, R/W } \end{aligned}$ | $v_{\mathrm{OH}}$ | $\mathrm{V}_{\mathrm{SS}}+2.4$ | - | - | Vdc |
| Output Low Voltage $\begin{aligned} { }^{(1} \text { LOAD }= & \left.1.6 \mathrm{mAdc}, \mathrm{~V}_{C C}=4.75 \mathrm{~V}\right) \\ & \text { SYNC, Data, AO-A15, R/W } \end{aligned}$ | $\mathrm{V}_{\mathrm{OL}}$ | - | - | $\mathrm{V}_{\mathrm{SS}}+0.4$ | Vdc |
| Power Dissipation | ${ }^{\text {D }}$ | - | 0.25 | 0.70 | w |
| Capacitance $\begin{aligned} \left(\mathrm{V}_{\text {in }}=0, \mathrm{~T}_{\mathrm{A}}\right. & \left.=25^{\circ} \mathrm{C}, \mathrm{f}=1 \mathrm{MHz}\right) \\ & \text { Logic } \\ & \text { Data } \\ & \text { AO-A15, R/W, SYNC } \\ & \phi_{\text {o(in) }} \\ & \phi_{1} \\ & \phi_{2} \end{aligned}$ | C <br> $c_{\text {in }}$ <br> $C_{\text {out }}$ <br> ${ }^{C}{ }_{\phi_{\text {o(in) }}}$ <br> C ${ }^{\phi}{ }_{1}$ <br> ${ }^{C} \phi_{2}$ | - | - - - 30 50 | $\begin{aligned} & 10 \\ & 15 \\ & 12 \\ & 15 \\ & 50 \\ & 80 \end{aligned}$ | pF |

Note: $\overline{\mathrm{RO}}$ and $\overline{\mathrm{NMI}}$ require 3 K pull-up resistors.

INSTRUCTION SET - OP CODES, Execution Time, Memory Requirements

| inatmuctiona |  | Patbiati |  |  | AEEBLUTE |  |  | Efmepast |  |  | actuat |  |  | - Mite |  |  | (im0, $)^{\text {a }}$ |  |  | (ineli. T |  |  | E,726t, |  |  | Amax |  | Ant 7 |  |  | atcative |  |  | Leamet |  | 2past, ${ }^{\text {a }}$ |  |  | coworimom coots |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| metuoner | chastioe | op | $\cdots 1$ | 1 |  | In | $\pm$ | -0 | N | - | Op | $N$ | $\cdots$ | or | N | \% | $\infty$ | $N$ | 1 | OP | $\cdots$ | $\square$ | op $n$ | $\cdots$ | \% | Oer ${ }^{\text {m }}$ | 4 | - 1 | $N$ | \% | OP N | $\cdots$ | - | Op $n$ | $\cdots$ | O | $\cdots$ | $\cdots$ | N ? | $c$ | 10 | 0.8 |
| $A D C$ <br> AN D <br> ASL <br> BCC <br> BC |  | 69 |  |  | $\begin{aligned} & 80 \\ & 20 \\ & 06 \end{aligned}$ |  | $\begin{aligned} & 2 \\ & 2 \\ & 3 \end{aligned}$ | $\begin{array}{\|l\|} \hline 5 \\ 25 \\ 25 \\ \hline \end{array}$ | $\begin{aligned} & 2 \\ & 2 \\ & 5 \end{aligned}$ | 2 | *a | 2 | 1 |  |  |  | $\begin{array}{\|c\|} \hline 81 \\ 21 \end{array}$ | $\begin{aligned} & 6 \\ & 6 \end{aligned}$ | $2$ | $\begin{aligned} & 71 \\ & 31 \end{aligned}$ | $\begin{array}{\|l\|l} \hline 5 & 2 \\ 5 & 2 \end{array}$ | $2$ | $\begin{array}{c\|} \hline 35 \\ 35 \\ 16 \end{array}$ |  | $\begin{aligned} & 2 \\ & 2 \\ & 2 \end{aligned}$ |  |  | 188 | 4 | $\begin{aligned} & 3 \\ & 3 \end{aligned}$ |  |  |  |  |  |  |  |  |  |  |  |  |
| BEO BIT <br> BMI <br> BNE <br> BPL |  |  |  |  | $x$ | C 4 | 1 | 24 | 3 | 7 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 50 2 <br> 30 2 <br> 00 2 <br> 10 2 | $\begin{array}{l\|l} \hline 2 & 2 \\ 2 & 2 \\ 2 & 2 \\ 2 & 2 \end{array}$ |  |  |  |  |  |  |  |  |  |  |
| BRK <br> BVC <br> evs <br> CLC <br> CLD | вААNCM ON $V+9$ แ! BAANCNON VOI vi $\begin{aligned} & 0-c \\ & 0 \sim 0 \end{aligned}$ |  |  |  |  |  |  |  |  |  |  |  |  | $\begin{array}{\|c\|} \hline 00 \\ 18 \\ 18 \\ \hline 08 \end{array}$ | $17$ <br> 7 $2$ |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 50 | $2$ | $\begin{aligned} & 2 \\ & 2 \end{aligned}$ |  |  |  |  |  |  |  |  |  |
| CLI clv CM <br>  <br>  | $\begin{aligned} & 0 \rightarrow 1 \\ & 0 * v \\ & A-u \\ & x \rightarrow u \\ & v \in u \end{aligned}$ | $\begin{aligned} & \mathrm{ce} \\ & \mathrm{Ev} \\ & \mathrm{ce} \end{aligned}$ | $\begin{aligned} & 2 \\ & 2 \\ & 2 \end{aligned}$ | $\begin{aligned} & 2 \\ & 2 \\ & 2 \end{aligned}$ | $\begin{aligned} & \mathrm{co} \\ & \mathrm{cc} \\ & \mathrm{cc} \end{aligned}$ |  | $\begin{aligned} & 3 \\ & 3 \\ & 3 \end{aligned}$ | $\begin{aligned} & \mathrm{Cs} \\ & \mathrm{EA} \\ & \mathrm{CA} \end{aligned}$ | $\begin{aligned} & 3 \\ & 3 \\ & 3 \end{aligned}$ | 2 2 7 |  |  |  | $\left.\begin{aligned} & 50 \\ & 80 \end{aligned} \right\rvert\,$ | $2$ | $1 \begin{aligned} & 1 \\ & 1 \end{aligned}$ | c1 | 6 | 2 | 01 | 5 | 2 | DS | 4 | 2 | 00. 4 | 43 | D0 | 4 | 3 |  |  |  |  |  |  |  |  |  |  |  |  |
| OEC <br> DEX <br> DEY <br> EOR <br> INC |  | 49 | 2 | 7 | $\begin{array}{\|cc} \mathrm{CE} \\ \hline \mathrm{CE} \\ \hline 10 \end{array}$ |  | $3$ | $\begin{array}{\|cc} \hline \text { c6 } \\ \hline 15 \\ \hline 156 \\ \hline \end{array}$ | 5 <br> 3 <br> 5 | \% |  |  |  | $\left\|\begin{array}{l} \mathrm{CA} \\ \mathrm{se} \end{array}\right\|$ |  |  | 4) | 6 | 7 | 51 | 5 ${ }^{1}$ |  | D6 | $\begin{gathered} 5 \\ 5 \\ 6 \end{gathered}$ | $2$ |  | $\begin{array}{l\|l} \hline 7 & 3 \\ 4 & 3 \\ 7 & 3 \\ \hline \end{array}$ | 50 | 4 | 3 |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  | ${ }^{48}$ | 2 |  | $AC 2$ |  3 <br> 0 6 <br> 0 4 | 3 3 3 | as | 3 | 2 |  |  |  | $\begin{array}{l\|} \hline 68 \\ \mathrm{cs} \end{array}$ | $12$ | $\begin{aligned} & 1 \\ & 1 \end{aligned}$ | $A_{1}$ | 1 | 2 | 181 | 5 | 2 | es | +1 | 2 | 80. | 13 | 88 | 4 | 3 |  |  |  | c\| 1 | 12 |  |  |  |  |  |  |  |



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## ABSOLUTE MAXIMUM RATINGS*

Voltage on any pin relative to VSS (Except VPP).................................................... -0.3 V to +6 V

Operating Temperature TA (Ambient) ......................................................... $0^{\circ} \mathrm{C} \leqslant$ TA $\leqslant 70^{\circ} \mathrm{C}$
Storage Temperature (Ambient)................................................................ $55^{\circ} \mathrm{C} \leqslant$ TA $\leqslant+125^{\circ} \mathrm{C}$
Power Dissipation ....................................................................................... 1 Watt
Short Circuit Output Current . ............................................................................... 50mA
"Stresses greater than those listed under "Abeoluts Maumum Ratings" may cause permanemt damage to the device. This is a stress ratung only and functional ooeration of the device at these or amy other conditions above those indicated in the operating sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## READ OPERATION

RECOMMENDED D.C. OPERATING CONDITIONS AND CHARACTERISTICS ${ }^{1,2,4,8}$
$\left(0^{\circ} \mathrm{C} \leqslant T A \leqslant 70^{\circ} \mathrm{C}\right)(V C C=+5 \mathrm{~V} \pm 5 \%, V P P=V C C)^{3}$

| SYM | PARAMETER | MIN | TYP | MAX | UNITS | NOTES |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IH }}$ | Input High Voltage | 2.0 |  | $\mathrm{Vcc}+1$ | Voits |  |
| $\mathrm{V}_{\mathrm{t}}$ | Input Low Voltage | -0.1 |  | 0.8 | Voits |  |
| ${ }_{\text {l CcI }}$ | VCC Standby Power Supply Current $(\overline{O E}=\text { VIL: } \overline{\mathrm{CE}}=\mathrm{VIH})$ |  | 10 | 25 | mA | 2 |
| Icce | VCC Active Power Suppiy Current $(\overline{O E}=\overline{C E}=\text { VIL })$ |  | 57 | 100 | mA | 2 |
| Ippı | VPP Current (VPP $=5.25 \mathrm{~V}$ ) . |  |  | 6 | mA | - 2,3 |
| VOH | Output High Voltage $(1 O H=-400 \mu \mathrm{~A})$ | 2.4 |  |  | Voits |  |
| $\mathrm{V}_{\mathrm{oL}}$ | Output Low Voitage $(1 \mathrm{OL}=2.1 \mathrm{~mA})$ |  |  | . 45 | Volts |  |
| IIL | Input Leakage Current ( $\mathrm{VIN}=5.25 \mathrm{~V}$ ) |  |  | 10 | $\mu \mathrm{A}$ |  |
| lot | Output Leakage Current (VOUT $=5.25 \mathrm{~V}$ ) |  |  | 10 | $\mu \mathrm{A}$ |  |

## AC CHARACTERISTICS ${ }^{1},{ }^{2}, 5$

$\left(0^{\circ} \mathrm{C} \leqslant T A \leqslant 70^{\circ} \mathrm{C}\right)(\mathrm{VCC}=+5 \mathrm{~V} \pm 5 \%, V P P=V C C)^{3}$

|  |  | - 5 |  | -6 |  | -7 |  | -8 |  | UNITS | NOTES |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SYM | PARAMETER | MIN | MAX | MIN | MAX | MIN | MAX | MIN | MAX |  |  |
| $t+\mathrm{cc}$ | Address to Output Delay $(\overline{C E}=\overline{O E}=V I L)$ |  | 300 |  | 350 |  | 390 |  | 450 | ns |  |
| ${ }^{\text {tee }}$ | CE to Output Delay $(\overline{O E}=V I L)$ |  | 300 |  | 350 |  | 390 |  | 450 | ns | 6 |
| TOE | Output Enable to Output Delay ( $\overline{\mathrm{CE}}=\mathrm{VIL})$ |  | 120 |  | 120 |  | 120 |  | 120 | ns | 10 |
| tuf | Chip Deselect to Output Float ( $\overline{C E}=$ VIL $)$ | 0 | 100 | 0 | 100 | 0 | 100 | 0 | 100 | ns | 9 |
| - | andrace in numut Hold | $\bigcirc$ |  | 0 |  | 0 |  | 0 |  | ns |  |

## CAPACITANCE

$\left(\mathrm{TA}=25^{\circ} \mathrm{C}\right)^{a}$

| SYM | PARAMETER | TYP | MAX | UNITS | NOTES |
| :--- | :--- | :---: | :---: | :---: | :---: |
| CiN $_{\text {IN }}$ | Input Capacitance | 4 | 6 | pF | 7 |
| Output Capacitance | 8 | 12 | pF | 7 |  |

notes:

1. VCC must be applied on or before VPP and Removed atter or at the same tumes as VPP
2. VPP and VCC may be connected together exceat during programming, in which case the suppiy current is the sum of ICC and IPPI
3. The tolerance on VPP is to allow use of a driver circuit to switch VPP from VCC to $\mathbf{2 5 V}$ in the REAO and PROGRAM mode respectively.
4. All voltages with respect to VSS.
5. Load conditions $=$ TTL laad and 100 pF , ir $=\mathrm{if}=20 \mathrm{~ms}$, raferenca levels are 1 V or 2 V for inputs and 8 V and 2 V lor outputs
6. TOE is referenced to CE or the addressea, whichever occurs last.
7. Effectrve Capacitance calculated from the equation $C=\Delta \Delta V$ where $\Delta V=3 V$
8. Typreal numbers are for $T A=25^{\circ} \mathrm{C}$ and VCC $=5.0 \mathrm{~V}$
9. tDF is applicable to both $\overline{C E}$ and $\overline{O E}$. whichever oceurs first.
10. OE may follow up to LACC - IOE after the faling adge of CE without effecting LACC

TIMING DIAGRAMS
READ CYCLE ( $\overline{\mathrm{CE}}=\mathrm{V}_{\text {IL }}$ )


## STANDBY POWER

DOWN MODE
$\left(\overline{O E}=V_{I L}\right)$



MODE SELECTION

| MODE | PIN | $\frac{\overline{C E} / \mathrm{PGM}}{(18)}$ | $\frac{\overline{O E}}{\text { (20) }}$ | $\frac{\text { VPp }}{(27)}$ | OUTPUTS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| READ |  | VIL. | VIL. | -5 | - Valid Out |
| STANDBY |  | VIH | Don't Care | -5 | Open |
| PROGRAM |  | Puised VIL to VIH | VIH | -25 | Input |
| PROGRAM VERIFY |  | VIL. | VIL | - 25 | Vand Out |
| PROGRAM INHIBIT |  | VIL | VIH | -25 | Open |
| VCCl241 $=5 \mathrm{~V}$ all modes |  |  |  |  |  |

## DESCRIPTION CONTINUED

Programming can be done with a single TTL level pulse. and may be done on any individual location either sequencially or at random. The three-state output controlled by the $\overline{O E}$ input allows OR-tie capability for construction of large arrays. A single power supply requirement of -5 volts makes the MK2716 ideally suited for use with Mostek's new 5 voit only microprocessors such as the MK3880 (Z80). The MK2716 is packaged in the industry standard 24 -pin dual-in line package with a transparent hermetically sealed lid. This allows the user to expose the chip to uitraviolet light to erase the data pattern. A new pattern may then be written into the device by following :he

The MK2716 is specifically designed to fit those applications where fast turnaround time and pattern experimentatin are required. Since data may be aitered in the device (erase and reprogram) it allows for early debugging of the system program. Since single location programming is available the MK2716 can have its data content increased (assuming all 2048 bytes were no: programmed) at any time for easy updating of system capabilities in the field. Once the data/ program is fixed and the intention is to produce large numbers of systems. Mostek also supplies a pin compatible mask programmable ROM, the MK34000. To transfer the program data to ROM, the user need only send the PROM along with device information to MOSTEK, from which the ROM with the desired pattern can be generated. This means a reduction in the possibility of error when converting data to other forms (cards, tape. etc.) for this purpose. However, data may still be input by any of these traditional means such as paper tape. card deck, etc.

## read operation

The MK2716 has five basic modes of operation. Under normal operating conditions (non-programming) there are two modes including READ and STANDBY. A READ operation is accomplished by maıntaining pin $18(\overline{\mathrm{CE}})$ at VIL and pin 21 (VPP) at +5 voits. If $\overline{O E}$ (pin 20) is heid active low after addressing ( $A 0$ - A10) have stabilized then valid output data will appear on the output pins at access time tACC (address access). In this mode, access time mav ha raforownan on $\overline{C_{r} \text {. A- }}$

POWER DOWN operation is accomplished by taking pin 18 (CE) to a TTL high level (VIH). The power is reduced by $75 \%$ from 525 mW maximum to 132 mW . In power down VPP must be at +5 volts and the outputs will be opencircuit regardless of the condition of $\overline{\mathrm{OE}}$. Access time from a high to low transition of $\overline{\mathrm{CE}}(\mathrm{tCE})$ is the same as from addresses (tACC). (See STANDBY Timing Diagram).

## PROGRAMMING INSTRUCTIONS

The MK2716 as shipped from Mostek will be completely erased. In this initial state and after any subsequent erasure, all bits will be at a ' 1 ' level (output high). Information is introduced by selectively programming 'O's into the proper bit locations. Once a ' $O$ ' has been programmed into the chip it may be changed only by erasing the entire chip with UV light.

Word address selection is done by the same decode circuitry used in the READ mode. The MK2716 is put into the PROGRAM mode by maintaining VPP at +25 V . and $\overline{\mathrm{OE}}$ at VIH. In this mode the output pins serve as inputs ( 8 bits in parallel) for the required program data. Logic levels for other inputs and the VCC supply voltage are the same as in the READ mode.

The program a "byte" (8 bits) of data, a TTL active high level pulse is applied to the $\overline{C E} /$ PGM pin once addresses . and data are stabilized on the inputs. Each location must have a pulse applied with only one pulse per location required. Any individual location, a sequence of locations or locations at random may be programmed in this manor. The program pulse has a minimum width of 45 msec and a maximum of 55 msec . and must not be programmed with a high level D.C. signal applied to the $\overline{\mathrm{CE}} / \mathrm{PGM}$ pin.

PROGRAM INHIBIT is another useful mode of operation when programming muitiple parallel addressed MK2716's with different data. It is necessary only to maintain $\overline{\mathrm{OE}}$ at VIH, VPP at +25 , allow addresses and data to stabilize and pulse the $\overline{\mathrm{CE}} / \mathrm{PGM}$ pin of the device to be programmed. Data may then be changed and the next device pulsed. The devices with CE/PGM at VIL will not be programmed.

PROGRAM VERIFY allows the MK2716 program data to be verified without having to reduce VPP from +25 V to $+5 V$. VPP should only be used in the PROGRAM/PROGRAM INHIBIT and PROGRAM VERIFY modes and must be at +5 V in all other modes.

## MK2716 ERASING PROCEDURE

The MK2716 may be erased by exposure to high intensity ultraviolet light, illuminating the chip thru the transparent window. This exposure to ultraviolet light induces the flow of a photo current from the floating gate thereby discharging the gate to its initial state. An ultraviolet source of $2537 \AA$ yielding a total integrated dosage of $15 \mathrm{Watt}-\mathrm{seconds} / \mathrm{cm}^{2}$ is required. Note that all bits of the MK2716 will be erased. The erasure time is approximately 15 to 20 minutes utilizing a ultra-violet lamp with a $12000 \mu \mathrm{~W} / \mathrm{cm}^{2}$ power rating. The lamp should be used without short wave filters, and the MK2716 to be erased should be placed about one inch away from the lamp tubes. It should be noted that as the distance between the lamp and the chip is doubled, the exposure time required goes up by a factor of 4 . The UV content of sunlight is insufficient to provide a practical means of erasing the MK2716. However, it is not recommended that the MK2716 be operated or stored in direct sunlight, as the UV content of sunlight may cause erasure of some bits in a short period of time.

FEATURES
$=$ Address Activated ${ }^{T M}$ Interface combines benefits $0^{-}$
Edge Activated ${ }^{\top M}$ and full static
$=$ High performance

| Part Number | Access Time | Cycle Time |
| :--- | :--- | :--- |
| MK4118-1 | 120 nsec | 120 nsec |
| MK4118-2 | 150 nsec | 150 nsec |
| MK4118-3 | 200 nsec | 200 nsec |
| MK4118-4 | 250 nsec | 250 nsec |

Z Single +5 volt power supply

## DESCRIPTION

The MK4118 uses Mostek's Poly R N-Channel Silicon Gate process and advanced circuit design techniques to package 8192 bits of static RAM on a single chip. Mostek's Address Activated ${ }^{\text {TM }}$ circuit design technique 's utilized to achieve high performance, low power, and easy user implementation. The device has a $\mathrm{V}_{1 H}=2.2$, $\mathrm{V}_{\mathrm{II}}=0.8 \mathrm{~V}, \mathrm{~V}_{\mathrm{OH}}=2.4, \mathrm{~V}_{\mathrm{Ot}}=0.4 \mathrm{~V}$ making it totally compatible with all TTL family devices.

The MK4118 is designed for all wide word memory applications. The MK4118 provides the user with a

- TTL compatible I/O

Fanout:
2. Standard TTL

2 - Schottky TTL
12 . Low power Schortky TTL

- Low Power -400 mw Active
- $\overline{C S}, \overline{O E}$, and $\overline{\text { LATCH }}$ functions for flexible system operation
a Pin compatible with Mostek's wide word memory family
high-density, cost-effective $1 \mathrm{~K} \times 8$-bit Random Access Memory. Fast Output Enable ( $\overline{\mathrm{E}}$ ) and Chip Select (CS) controls are provided for easy interface in microprocessor or other bus-oriented systems. The MK4118 features a flexible Lateh ( $\overline{\mathrm{L}}$ ) function to permit latching of the address and CS status at the user's option. Common data and address bus operation may be performed at the system level by utilizing the $\bar{L}$ and $\overline{\mathrm{OE}}$ functions for the MK4118. The latch function may be bypassed by merely tying the latch pin to $\mathrm{V}_{\mathrm{cc}}$. providing fast ripple-through operation.



|  |  | MK4118.1 |  | MK4118-2 |  | MK4118-3 |  | MK4118-4 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SYM | PARAMETER | MIN | MAX | MIN | MAX | MIN | MAX | MIN | MAX | UNIT | NOTES |
| tsc | Read Cycle Time | 120 |  | 150 |  | 200 |  | 250 |  | ns |  |
| $\mathrm{t}_{\wedge \wedge}$ | Address Access Time |  | 120 |  | 150 |  | 200 |  | 250 | ns |  |
| 4 cas | Chip Select Access Time |  | 60 |  | 75 |  | 100 |  | 125 | ns |  |
| tusz | Chip Select Data Off Time | 0 | 60 | 0 | 75 | 0 | 100 | 0 | 125 | ns |  |
| Leta | Output Enable Access Time |  | 60 |  | 75 |  | 100 |  | 125 | ns |  |
| tutz | Output Enable Data Off Time | 0 | 60 | 0 | 75 | 0 | 100 | 0 | 125 | ns |  |
| taL | Address Data Off Time | 10 |  | 10 |  | 10 |  | 10 |  | ns |  |
| tust | Address to Latch Setup Time | 10 |  | 10 |  | 10 |  | 20 |  | ns |  |
| that. | Address From Latch Hold Time | 40 |  | 50 |  | 65 | $\cdots$ | 80 |  | ns |  |
| tcsi | $\overline{\mathrm{CS}}$ To Latch Setup Time | 0 |  | 0 |  | 0 |  | 0 |  | ns |  |
| $\mathrm{k}_{\mathrm{HL}}$ | $\overline{\mathrm{CS}}$ From Latch Hold Time | 40 |  | 50 |  | 65 |  | 80 |  | ns |  |
| tha | Latch Off Access Time |  | 155 |  | 200 |  | 260 |  | 320 |  | ns |
| twe | Write Cycle Time | 120 |  | 150 |  | 200 |  | 250 |  | ns |  |
| tusw | Address To Write Setup Time | 0 |  | 0 |  | 0 |  | 0 |  | ns |  |
| tahw $^{\text {che }}$ | Address From Write Hold Time | 40 |  | 50 |  | 65 |  | 80 |  | ns |  |
| tcsw | $\overline{\text { CS }}$ To Write Setup Time | 0 |  | 0 |  | 0 |  | 0 |  | ns |  |
| kew | $\overline{\overline{C S}}$ From Write Hold Time | 40 |  | 50 |  | 65 |  | 80 |  | ns |  |
| tosw | Data To Write Setup Time | 20 |  | 30 |  | 40 |  | 50 |  | ns |  |
| tıı" | Data From Write Hold Time | 20 |  | 30 | $\checkmark$ | 40 |  | 50 |  | ns |  |
| (wi) | Write Pulse Duration | 35 |  | 50 |  | 60 |  | 70 |  | ns |  |
|  | Latch Duration, High | 35 | DC | 50 | DC | 60 | DC | 70 | DC | ns |  |
| t.l.bl. | Latch Duration, Low |  | DC |  | DC |  | DC |  | DC | ns |  |
| tweze | Write Enable Data Off Time | 0 | 60 | 0 | 75 | 0 | 100 | 0 | 125 | ns |  |
| t. 2 | Latch Data Off Time | 10 |  | 10 |  | 10 |  | 10 |  | ns |  |
| twp. | Write Pulse Lead Time | 75 |  | 90 |  | 130 |  | 170 |  |  |  |

PROGRAM OPERATION:
D.C. ELECTRICAL CHARACTERISTICS AND OPERATING CONDITIONS ${ }^{122}$
$\left(T A=25^{\circ} \mathrm{C} \pm 5^{\circ} \mathrm{C}\right)(\mathrm{VCC}=5 \mathrm{~V} \pm 5 \%, \mathrm{VPP}=25 \mathrm{~V} \pm 1 \mathrm{~V})$

| SYMBOL | PARAMETER | MIN | MAX | UNITS | NOTES |
| :--- | :--- | :---: | :---: | :---: | :---: |
| IIL | Input Leakage Current |  | 10 | $\mu \mathrm{~A}$ | 3 |
| VIL | Input Low Level | -0.1 | 0.8 | Volts |  |
| VIH | Input High Level | 2.0 | VCC +1 | Volts |  |
| ICC | VCC Power Supply Current |  | 100 | mA |  |
| IPP1 | VPP Supply Current |  | 5 | mA | 4 |
| IPP2 | VPP Supply Current during <br> Programming Pulse | 30 | mA | 5 |  |

A.C. CHARACTERISTICS AND OPERATING CONDITIONS ${ }^{1} .{ }^{2} .{ }^{6}{ }^{6}$,
$\left(T A=25^{\circ} \mathrm{C} \pm 5^{\circ} \mathrm{C}\right)(\mathrm{VCC}=5 \mathrm{~V} \pm 5 \% . \mathrm{VPP}=25 \mathrm{~V} \pm 1 \mathrm{~V})$

| SYMBOL | PARAMETER | MIN | TYP | MAX | UNITS | NOTES |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ${ }^{\text {tas }}$ | Address Setup Time | 2 |  |  | $\mu \mathrm{S}$ |  |
| toes | $\overline{O E}$ Setup -ime | 2 |  |  | $\mu \mathrm{S}$ |  |
| ${ }^{\text {to }}$ S | Data Setuo Time | 2 |  |  | $\mu \mathrm{S}$ |  |
| ${ }^{\text {t }}$ H | Address Hold Time | 2 |  |  | $\mu \mathrm{s}$ |  |
| TOEH | $\overline{\text { OE Hold Time }}$ | 2 |  |  | $\mu s$ |  |
| ${ }^{\text {t }} \mathrm{DH}$ | Data Hold Time | 2 |  |  | $\mu \mathrm{s}$ |  |
| ${ }^{\text {t }} \mathrm{DF}$ | Output Enable to Output Float | 0 |  | 120 | ns | 4 |
| toe | Output Enable to Output Delay |  |  | 120 | ns | 4 |
| tpw | Program Pulse Width | 45 | 50 | 55 | ms |  |
| tPRT | Program Pulse Rise Time | 5 |  |  | ns |  |
| tPFT | Program Pulse Fall Time | 5 |  |  | ns |  |

NOTES:
1 VCC must oe apolied at the same ume or before Vpp and removed after or at the same time as Vpp To prevent damage to the device it must not be inserted into abod
with VPP at 25 V
2 Ware must de taken to orevent overshoot of the VPp supply when switching to -25 V
2 Care must de taxen to
30.45 V - VIN . 525 V
30.45 V - VIN . 525 V

1 CE.PGM VIL
5 CE, PGM
6 it $~ 2 ~ 20 n s e c ~$
ir 2 V 'or inouts and 8 V or 2 V tor outouts are used as timing raference levels
....... ว6.

STATIC READ CYCLE
$\bar{W} \bar{E}=\bar{L}=\mathrm{HIGH}$


## LATCHED READ CYCLE <br> $\overline{W E}=\mathrm{HIGH}$



## WRITE CYCLE <br> $\overline{\mathrm{OE}}=\mathrm{LOW}, \mathrm{L}=\mathrm{HIGH}$


 change. Also $\overline{\mathrm{OE}}$ may be used to maintain OO open.

## OPERATION

## READ MODE

The MK4118 is in the READ MODE whenever the Write Enable control input ( $\overline{W E}$ ) is in the high state. The state of the 8 data I/O signals is controlled by the ChipSelect ( $\overline{\mathrm{CS}}$ ) and Output Enable ( $\overline{\mathrm{OE}}$ ) control signals. The READ MODE memory cycle may be either STATIC (ripplethrough) or LATCHED. depending on user control of the Latch Input Signal (L).

## STATIC READ CYCLE

In the STATIC READ CYCLE mode of operation, the MK4118 provides a fast address ripple-through access of data from 8 of 8192 locations in the static storage array. Thus. the unique address specified by the 10 Address Inputs (An) define which 1 of 1024 bytes of data is to be accessed. The STATIC READ CYCLE is defined by $\overline{W E}=\bar{L}=$ High.

A transition on any of the 10 address inputs will disable the 8 Data Output Drivers after tuz. Valid Data will be available to the 8 Data Output Drivers witinin $t_{\wedge \wedge}$ after all address input signals are stable, and the data will be output under control of the Chip Select ( $\overline{\mathrm{CS}}$ ) and Output Ēnable ( $\overline{\mathrm{OE}}$ ) signals.

## Latched read cycle



Enable control input ( $\overline{W E}$ ) being in the nigh state, and it is synchronized by proper control of the Latch (L) input.

As the Latch control input ( $\bar{L}$ ) is taken low, Address (An) and Chip Select ( $\overline{\mathrm{CS}}$ ) inputs that are stable for the specified set-up and hold times are latched internally. Data out corresponding to the latched address will be supplied to the Data Output drivers. The output drivers will be enabled to drive the Output Data Bus under control of the Output Enable ( $\overline{\mathrm{OE} \text { ) and latched Chip }}$ Select ( $\overline{\mathrm{CS}}$ ) inputs.

Taking the latch input high begins another read cycle for the memory locations specified by the address then appearing on the Address Input (An). Returning the latch control to the low state latches the new Address and Chip Select inputs internally for the remainder of the LATCHED READ CYCLE.

NOTE: If the 'LATCH' function is not used pin 19 ( $\overline{\mathrm{L}})$ must be tied high ( $V_{I H}$ min).

## WRITE MODE

The MK4118 is in the WRITE MODE whenever the Write Enable ( $\overline{W E}$ ) and Chip Select ( $\overline{\mathrm{CS}}$ ) control inputs are in the low state. The status of the 8 output buffers during a write cycle is explained below.


RECOMMENDED DC OPERATING CONDITIONS ${ }^{3}$
$\left(0^{\circ} \mathrm{C} \leqslant \mathrm{T}_{+} \leqslant-70^{\circ} \mathrm{C}\right)$

| SYM | PARAMETER | MIN | TYP | MAX | UNITS | NOTES |
| :--- | :--- | :---: | :---: | :---: | :---: | :---: |
| $V_{C C}$ | Supply Voltage | 4.75 | 5.0 | 5.25 | Volts | 1 |
| $V_{S S}$ | Supply Voltage | 0 | 0 | 0 | Volts | 1 |
| $V_{I H}$ | Logic "1" Voitage All Inputs | 2.2 |  | 7.0 | Volts | 1 |
| $V_{I L}$ | Logic "0" Voltage All Inputs | -0.3 |  | 0.8 | Voits | 1 |

DC ELECTRICAL CHARACTERISTICS ${ }^{1 / 3}$
$\left(0^{\circ} \mathrm{C} \leqslant T_{A} \leqslant+70^{\circ} \mathrm{C}\right)\left(\mathrm{V}_{\text {cc }}=5.0\right.$ volts $\left.\pm 5 \%\right)$

| SYM | PARAMETER | MIN | MAX | UNITS | NOTES |
| :---: | :---: | :---: | :---: | :---: | :---: |
| lat | Average $\mathrm{V}_{\text {cc }}$ Power Supply Current (Active) |  | 80 | mA |  |
| ke: | Average V ${ }_{\text {c }}$ Power Supply Current (Standby) |  | 60 | mA | 5 |
| IL | Input Leakage Current (Any Input) | -10 | 10 | $\mu \mathrm{A}$ | 2 |
| lot | Output Leakage Current | -10 | 10 | $\mu \mathrm{A}$ | 2 |
| $\mathrm{V}_{\text {OHI }}$ | Output Logic " 1 " Voltage locr $=-1 \mathrm{~mA}$ | 2.4 |  | Voits | . |
| $V_{\text {OH }}$ | Output Logic " 0 " Voltage $\mathrm{l}_{0,1}=4 \mathrm{~mA}$ |  | 0.4 | Voits |  |

AC ELECTRICAL CHARACTERISTICS'3
$\left.10^{\circ} \mathrm{C} \leqslant T_{1} \leqslant-70^{\circ} \mathrm{C}\right)\left(\mathrm{V}_{\mathrm{Cl}}=-5.0\right.$ voits $\left.=5 \%\right)$

| SYM | PARAMETER | TYP | MAX | NOTES |
| :--- | :--- | :---: | :---: | :---: |
| $C_{1}$ | Capacitance on all pins except $1 / 0$ | 4 pF |  | 4 |
| $C_{1, \prime}$ | Capacitance on $\mathrm{I} / \mathrm{O}$ pins | 10 pF |  | 4 |

## NOTES:

All vortages relerenced to V .
2. Measured with $0 \leqslant V_{1} \leqslant 5 V$ and outputs deselected (V., . SV)
3. A minimum of 100 usec time delay is required after abolication of $\left.\mathrm{V}_{1}, 1-5 \mathrm{~V}\right)$ before oroper device operation can be acnieved.

4 Effective capacitance calculated from the equation $C-1 \quad \frac{i t}{V}$ with $\Delta V=3 V$ ana $V_{1 \text { I }}$ nominai
5. Standby mode is defined as condition with addresses, laten and WE remain unenanged.
6. AC timing measurements made with 2 TL loads olus 100 oF

Static ROM (4096 x 8)

|  | 2332 | 2332 A | 2332 B |
| :--- | :---: | :---: | :---: |
| Maximum Access Time | 450 ns | 350 ns | 300 ns |

- Access Time Less Than 350ns
- Totally Static Operation
- Fully TIL Compatible
- Three-State Outputs for Wire-OR Exparsion
- Two Programmable Chip Selects


## DESCRIPTION

The MOS Technology 2332 is a 32,768 -bit Static Read-Only Memory organized as $409 \times 8$ bits. It features fast access time ( 450 ns maximum with the $2332,350 \mathrm{~ns}$ maximum with the 2332 A). The 2332 is designed to be compatible with all microprocessor and similar appiications where high performance, large bit storage capacity and simple interfacing requirements are important design considerations.

The 2332 operates totallv asynchronously; no dock input is required. With two programmable chip select inputs, four 32K ROMs can be OR-tied with no need for external decoding logic. Designed to replace two 2716 16K EPROMs, the 2332 can eliminate the need to redesign printed circuit boarcs for volume mask-programmed ROMs after prototyping is completed with EPROMs.

PIN CONFGURATION


BLOCK DUACRAM


DC CHARACTERISTICS $T_{A}=0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V} \pm 5 \%$ (uniess otherwise specified)

| Symbol | Parameter | Min | Max | Units | Test Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: |
| kCCl | Power Supply Current |  | 125 | ma | $V_{\text {PN }}=V_{C C}, V_{O}=$ Open, $T_{A}=0^{\circ} \mathrm{C}$ |
| $\mathrm{lcCl}_{2}$ | Power Supply Current |  | 120 | mA | $\mathrm{V}_{\mathrm{W}}=\mathrm{V}_{\mathrm{CC}}, \mathrm{V}_{\mathrm{O}}=$ Open, $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |
| 10 | Output Leakage Current |  | 10 | $\mu \mathrm{A}$ | Chip Deselected, $\mathrm{V}_{\mathrm{O}}=\mathrm{O}$ to $\mathrm{V}_{\mathrm{CC}}$ |
| 4 | Input Load Current |  | 10 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Max} . \mathrm{V}_{\mathrm{W}}=0$ to $\mathrm{V}_{\mathrm{CC}}$ |
| $\mathrm{v}_{\alpha}$ | Output Low Voltage |  | 0.4 | Volts | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Min} . \mathrm{l}_{\text {a }}=2.1 \mathrm{~mA}$ |
| $\mathrm{V}_{\text {OH }}$ | Output High Voitage | 2.4 |  | Volts | $\mathrm{V}_{\text {CC }}=\mathrm{Min}$. $\mathrm{COH}=-400 \mu \mathrm{~A}$ |
| $V_{1}$ | Input Low Voltage | -0.5 | 0.8 | Voits | See note 1 |
| $\mathrm{V}_{\mathrm{H}}$ | Input High Voitage | 2.0 | $\mathrm{V}_{\mathrm{CC}}+1$ | Voits | . |

AC CHARACTERISTICS $T_{A}=0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}, \mathrm{V}_{C C}=5.0 \mathrm{~V} \pm 5 \%$ (unless otherwise specified)

| Symbol | Parameter | 2332 |  | 2332A |  | Units | Test Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min | Max | Min | Max |  |  |
| $t_{\text {acc }}$ | Address Access Time |  | 450 |  | 350 | ns |  |
| ${ }^{\text {coo }}$ | Chip Select Delay |  | 200 |  | 200 | ns | See Note 2 |
| $t_{\text {DF }}$ | Chip Deselect Delay |  | 175 |  | 175 | ns |  |
| $\mathrm{COH}^{\text {O}}$ | Previous Data Valid After Address Change Display | 40 |  | 40 |  | ns |  |

CAPACTIANCE $T_{A}=25^{\circ} \mathrm{C}, \mathrm{f}=1.0 \mathrm{MHz}$, See Note 3

| Symbol | Parameter | Min | Max | Units | Test Conditions |
| :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{C}_{\mathrm{N}}$ | Input Capacitance |  | 8 | pF | All Pins except Pin under Test Tied to <br> AC Ground |
| $\mathrm{C}_{\text {OUT }}$ | Output Capacitance |  | 10 | pF |  |

## Notes

1 Input leveis that swing more negative than -0.5 V will be clamped and may cause damage to the device.
2 Loading 1 TII $+p$ f. input transition trne: 20 ms .
Timing measurement levels: input 1.5 V , output 0.8 V and 2.0 V $c_{L}=100 \mathrm{of}$.
3 This parameter is penodically sampled and is not $100^{\circ}$ tested.

## ABSOLUTE MAXIMUM RATINGS

| Ambient Operating Temperature | $0^{\circ}$ to $+70^{\circ} \mathrm{C}$ |
| :--- | ---: |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Supply Voitage to Ground Potential | -0.5 V to +7.0 V |
| Applied Output Voltage | -0.5 V to +7.0 V |
| Applied Input Voltage | -0.5 V to +7.0 V |
| Power Dissipation | 1.0 W |

## COMMENT

Stresses above those listed under "Absolute Maximum Ratings" may cause permament damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

TIMING DIAGRAM


TYPICAL CHARACTERISTICS


SSI GATES . . . LOGIC AND PIN ASSIGNMENTS,(TOP VIEWS)

| 00 <br> QUADRUPLE 2-INPUT POSITIVE-NAND GATES <br> positive logic: $Y=\overline{A B}$ <br> See pege 86 | SNE400/SN7400(J, N) SNB4HOO/SN74HOO(J, N) SN54LOO/SN74LOO(J, N) SNEALSOO/SN74LSOO(J, N, WI SN54S00/SN74800(J, N, W) |
| :---: | :---: |
| QUADRUPLE 2-INPUT POSITIVE NOR GATES <br> poeitive lonic: $Y=\overline{A+B}$ <br> See pege 92 | SN5402/SN7402(J, N) SN64L02/SN74L02(J, N) SN6ALS02/SN74LSO2(J, N, W) SN54S02/SN74SO2(d, N, W) |




$\mathrm{H}=$ high level (atesedy stata), $\mathrm{L}=$ Iow (evel lataedy atata), $\mathrm{X}=$ Irreievant
$\Omega$ = high-level puise; dats inguts ahould be hald conrtant while clock is high; data ie tranaferred to output on the failing edge of the puise, $\dagger=$ trencition from low to high level, $\downarrow=$ traneitton from high to low level
$a_{0}=$ the level of a before the Indicated Input conditions were estabilahed.
TOGQLE: Each output changes to the complement of ita previous level on eech ective transition (pulsa) of the ciosk.
-This configuration is nonstabie; that is, It will not perist when greset and elear inputs return to their Inactive (high) (evel.


TYPES SN54100, SN74100 8-BIT BISTABLE LATCHES GULLETIN NO. OL-S 7211830, DECEMEER 1972

## ICTION TABLE

(Esech Lateh)

| INPUTS | OUTPUTS |
| :--- | :--- |


| $D$ | $G$ | $\mathbf{Q}$ | $\overline{\mathbf{Q}}$ |
| :--- | :--- | :--- | :--- |
| $L$ | $H$ | $L$ | $H$ |
| $H$ | $H$ | $H$ | $L$ |
| $X$ | $L$ | $Q_{0}$ | $\bar{Q}_{O}$ |

$H=$ high lavel, $x=$ Irrolevan $O_{0}=$ the ievel of a befare the high-to-low transition of $a$

functiongl block disgram (each latch)

schematic (each latch)


