

- NOTES
1. "T" INDICATES TWISTED PAIR
 2. * INDICATES NO. POWER WIRING
 3. 1A50 AND 1A55 ARE 22 CONTACT AMPHENOL FEMALE CONNECTORS, LOCATED OVER 1A20 AND 1A25 RESPECTIVELY, HAVING THEIR MATING SIDES FORWARD, AND MOUNTING ON 1/8" STANDOFFS
 4. ADDITIONAL WIRING AS FOLLOWS:

FROM	TO	COLOR	FROM	TO	COLOR
1A22Y	1A50V	WHITE	1A21S	1A50P	WHITE
Z	J		1A19S	" K	
U	T		" T	" W	
V	L		1A23N	1A55T	
P	R		" S	" U	
R	N		1A24T	" M	
S	S		" S		

WIRING LAYOUT TYPES 1935FOMX

REV. LTR.	NO.	DATE	ENG.
A	23	5-20-64	(S)
B	47	6-5-64	(S)
C	89	6-10-64	(S)
D	110	6-14-64	(S)
E	116	6-15-64	(S)
F	136	6-15-64	(S)

REVISIONS	DATE	ENG.
DRAWN	5-11-64	
CHECKED		
ENG.		
PROJ. ENG.		
PROD.		

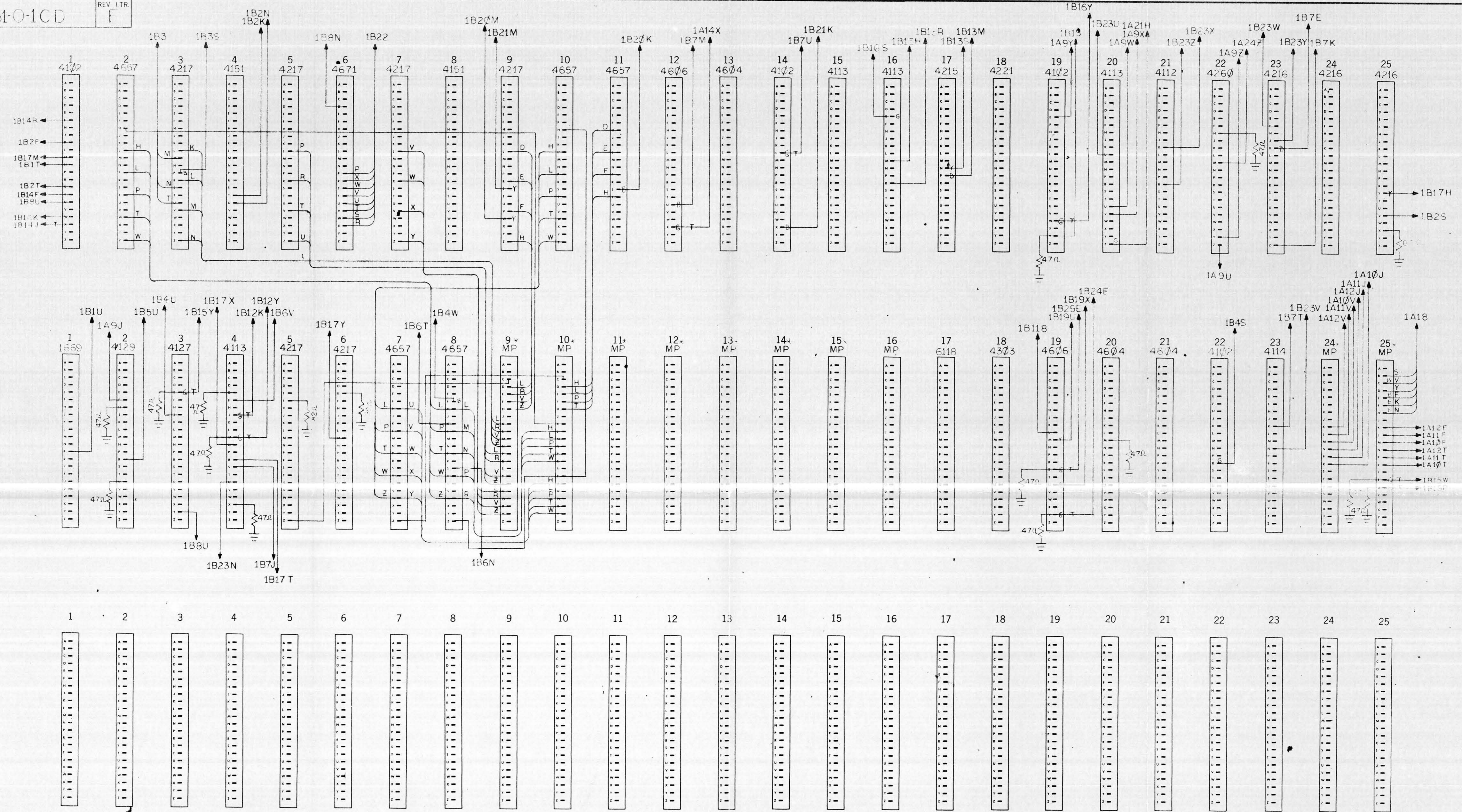
digital
EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE: J1T WIRING, PANELS 1A, 1B
FOR: J1T 551 PDF-1
CODE: WD DRWG. NO.: D-551-O-1AB REV. LTR.: F
SCALE: SHEET OF: DIST.

ISSUED MAY 13 1965

1C

1D



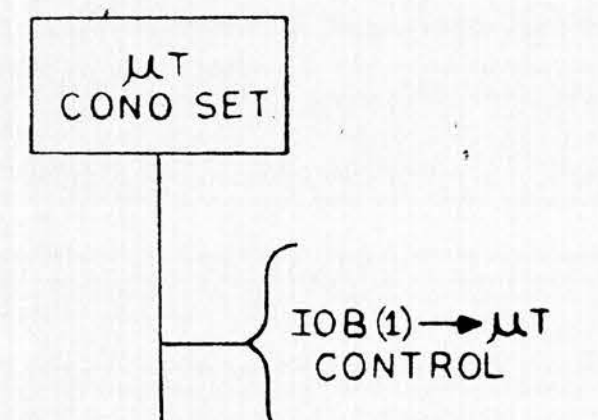
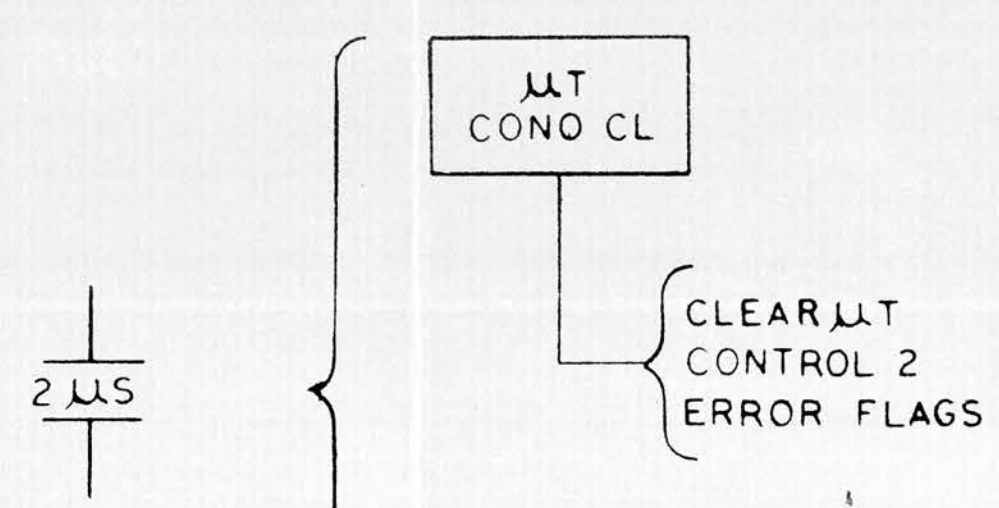
NOTES
1. SEE SHEET 1

ISSUED
MAY 13 1965

WIRING LAYOUT
TYPES 935FOMX

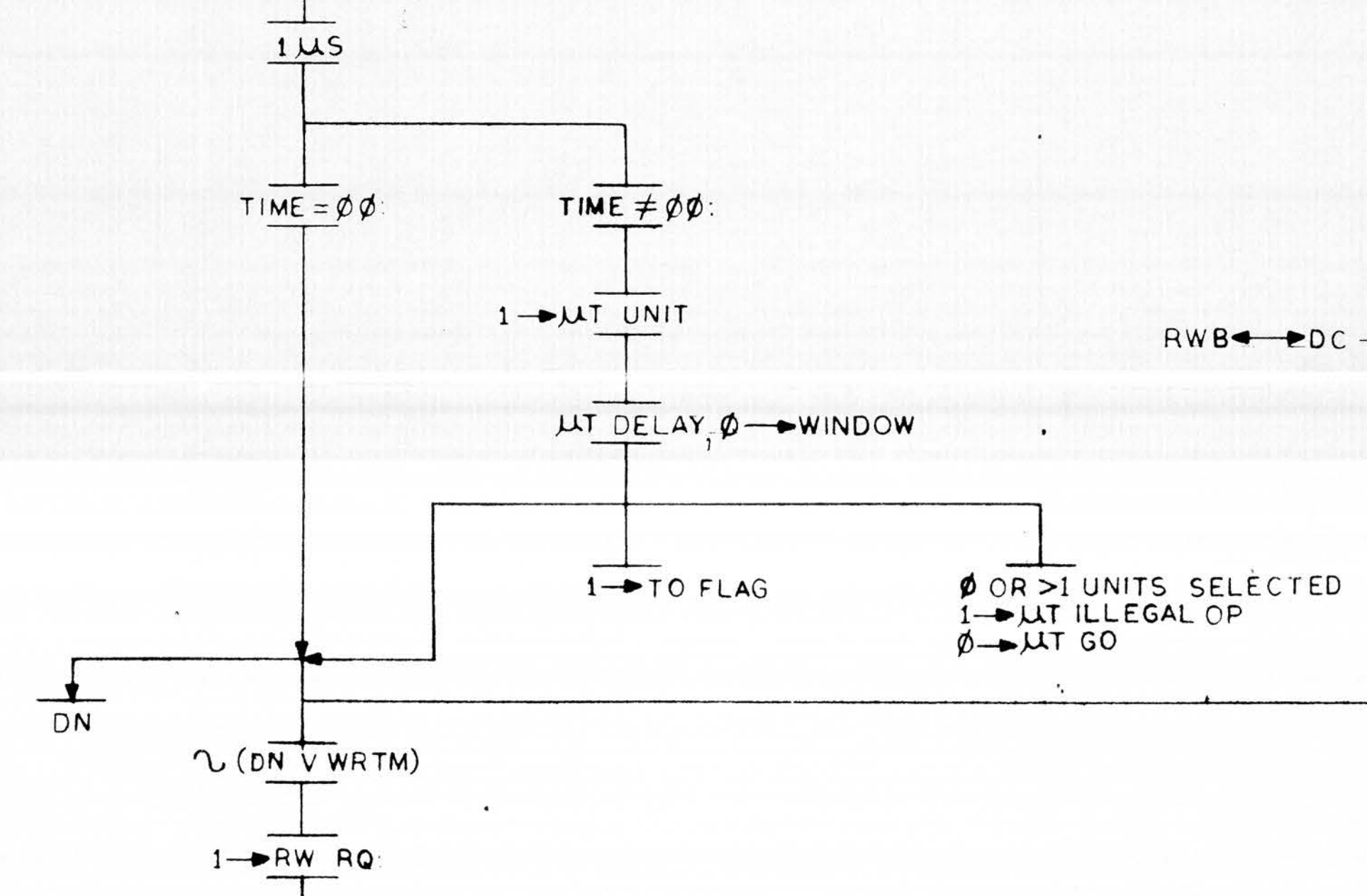
REV. LTR.	EQ. NO.	DATE	ENG.
A	23	5-29-64	
B	47	6-24-64	
C	89	12-21-64	
D	110	3-11-65	
E	116	4-5-65	
F	136	5-25-65	

DRAWN	DATE		TITLE
CHECKED	DATE		WT WIRING, PANELS 1C, 1D
ENG.	DATE	MAYNARD, MASSACHUSETTS	FOR
PROJ. ENG.	DATE		WT 551 PDP-6
PROD.	DATE	ASSY. NO.	CODE
			DRWG. NO.
			WD D-551-0-1CD
			REV. LTR.
			F

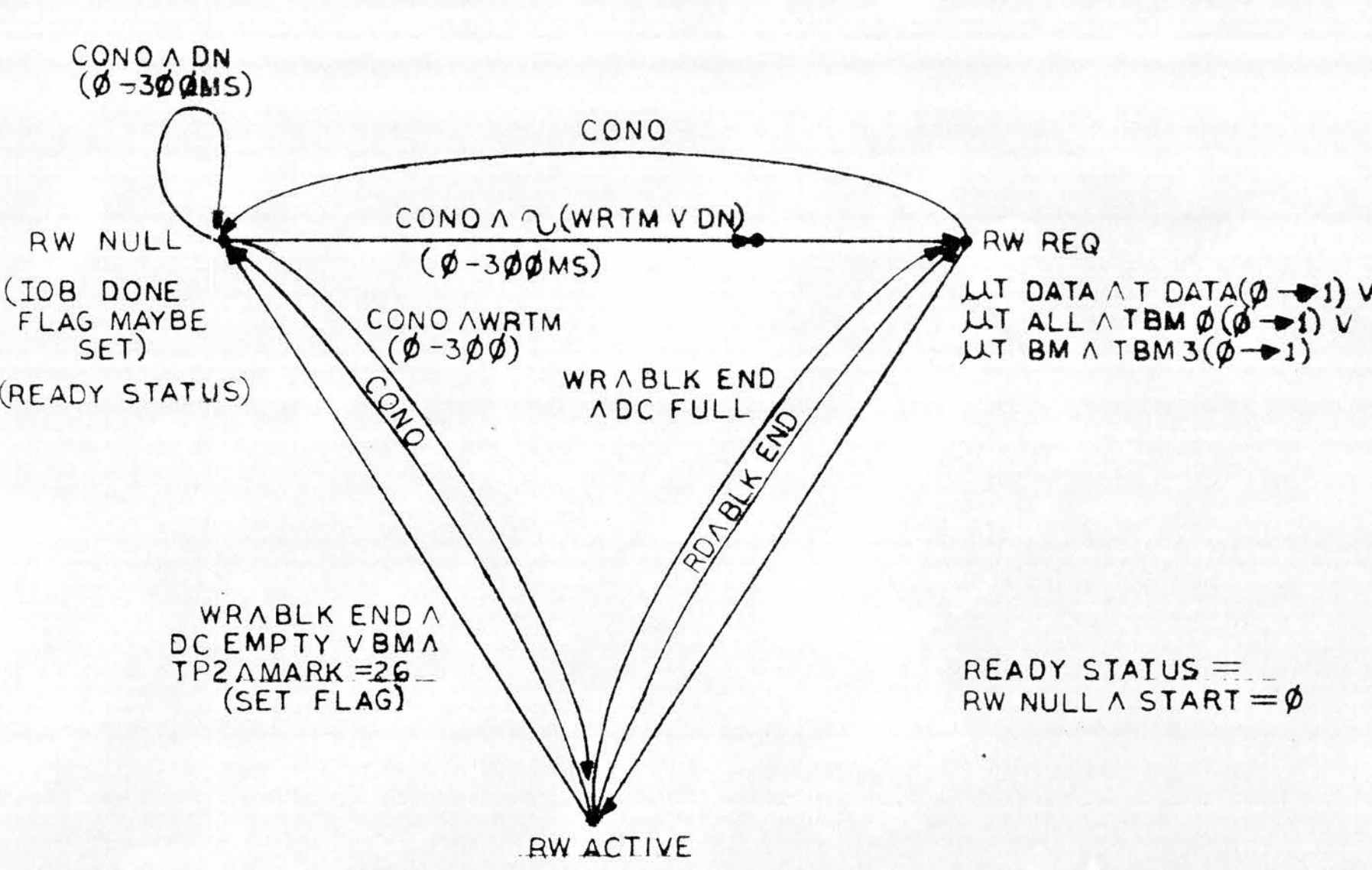
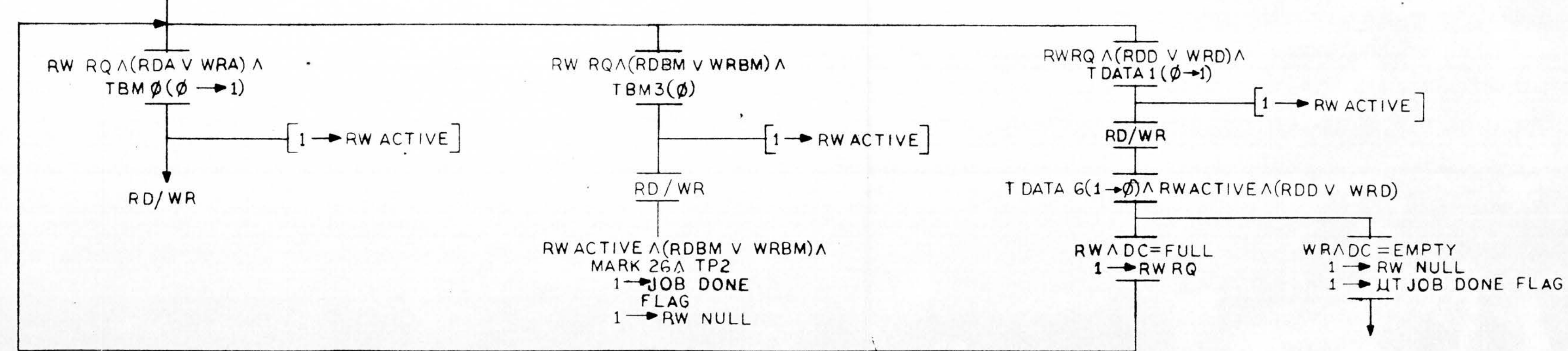


- DN 000 DO NOTHING
- RDA 001 READ ALL BITS AFTER 45
- RDBM 010 READ BLOCK MARK
- RDD 011 READ DATA
- WRTM 100 WRITE TIME & MARK
- WRA 101 WRITE ALL BITS AFTER 45
- WRBM 110 WRITE BLOCK MARK
- WRD 111 WRITE DATA

	COMMON EVENTS	READ A JOB IN PROG		WRITE A JOB IN PROG	
		(CT = 0) ODD	(CT = 1) EVEN	CT = 0 ODD	CT = 1 EVEN
TP3 TP1+2	MK END: 1 → MK END FLAG	RWB CLR		RWB CLR	
TP4 TP1+3	JOB IN PROG: 0 → WREN			(WRITE A(T DATA 1 V 6) V WRTM): DC → RWB	WRITE A(T DATA 1 V 6) V WRTM: LB → RWB
TP0	JOB IN PROG A WRITE: 1 → WREN			RWB(J) → WB	RWB(J) → WB
TP1	WRITE COMP:	RAMP(1) → RWB READ STROBE	RAMP(1) → RWB		
TP2 TP1+1	T DATA 7 0 → 1 A LB ≠ 0: 1 → μT INFO ERROR		RWB V → LB RWB ↔ DC		RWB V → LB



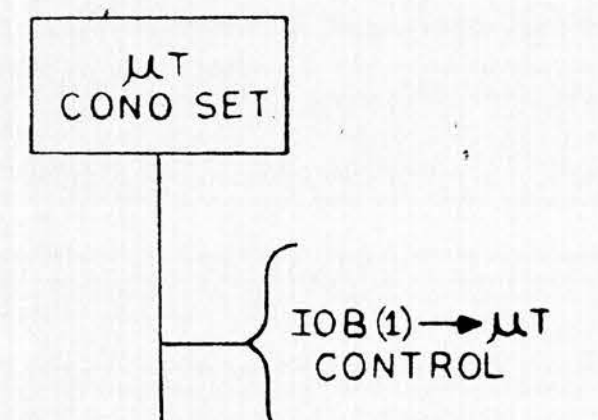
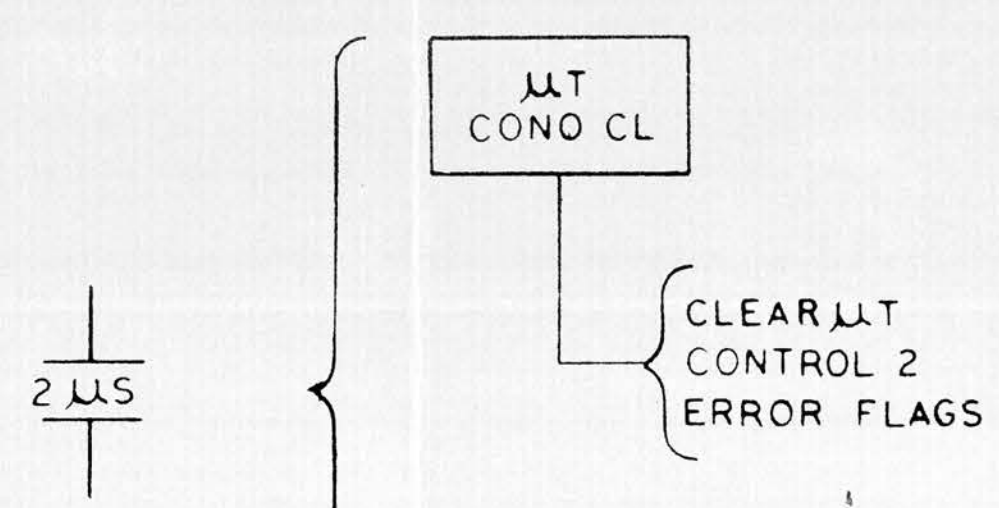
- RWB CLEAR = TP3 A RW ACTIVE A T CT (0)
- RWB ← RAMP (1) = TP1 A READ A RW ACTIVE
- LB ← RWB V = TP2 A RW ACTIVE A T CT (1)
- DC ← RWB (J) = TP2 A READ A RW ACTIVE A T CT (1) A (T DATA 1 V 6)
- LB CLEAR = T DATA 1 (0 → 1)
- RWB ← DC (1) = TP4 A RW ACTIVE A T CT (0) A (WRTM V (WRITE A(T DATA 1 V 6)))
- RWB (J) → WB = TP0 A WRITE A RW ACTIVE
- RWB ← LB (1) = TP4 A RW ACTIVE A T CT (1) A WRITE A T DATA 6 (1 → 0) A WRTM



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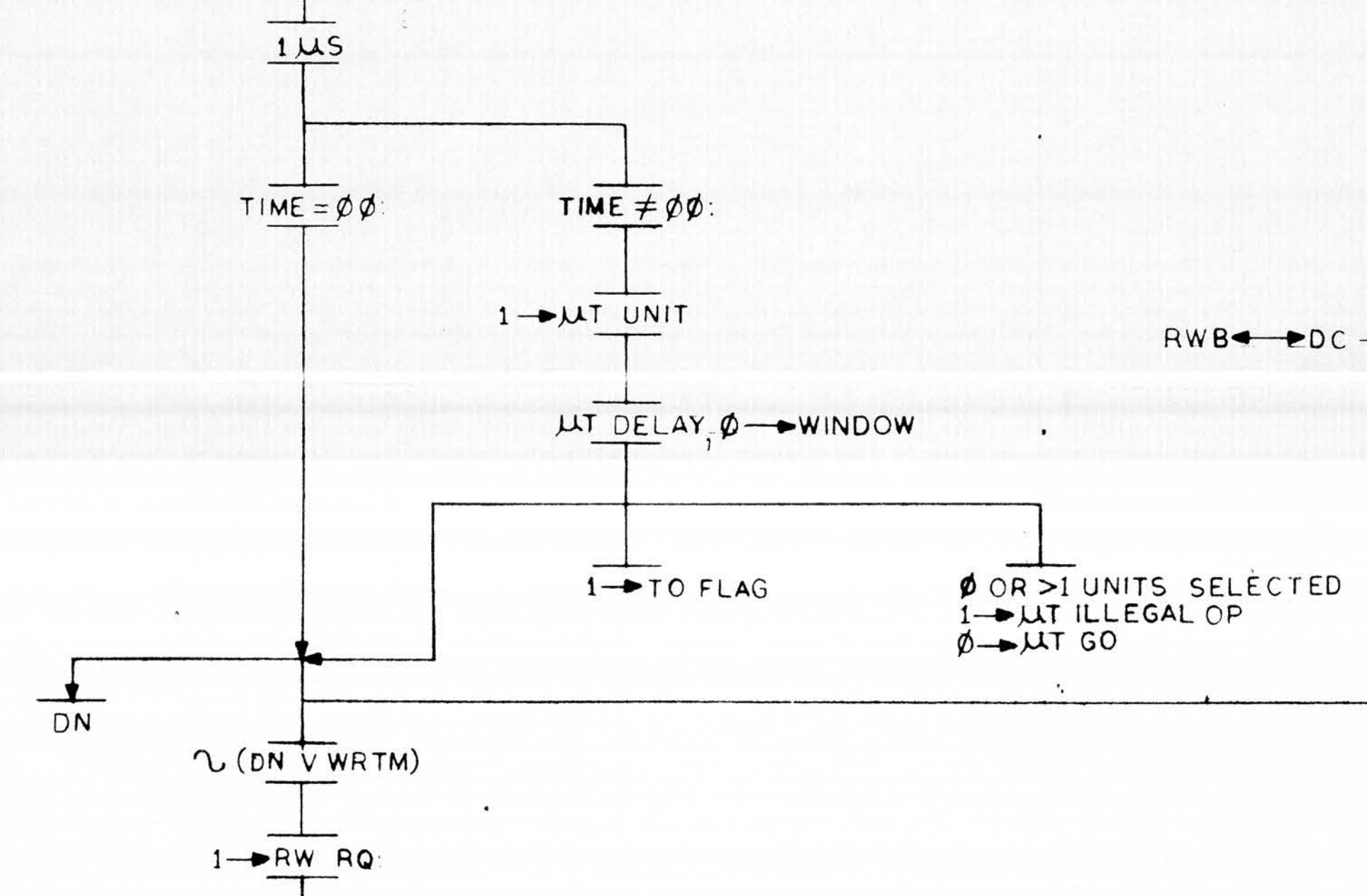
OCT 6 1964

CHANGE		REVISIONS	DRAWN G.A. Bourbeau 5-19-64	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE
DATE	ENG	DATE	CHECKED R. Reed 6/2/64	DATE		FLOW DIAGRAM
DATE	ENG	DATE	ENG R. Reed 6/2/64	DATE	FOR μT 551 PDP-6	CODE
DATE	ENG	DATE	PROJ ENG R. Reed 6/2/64	DATE		FD
DATE	ENG	DATE	PROD. R. Reed 6/3/64	DATE	DRWG NO	REV LTR
			SHEET		D-551-O-FD1	

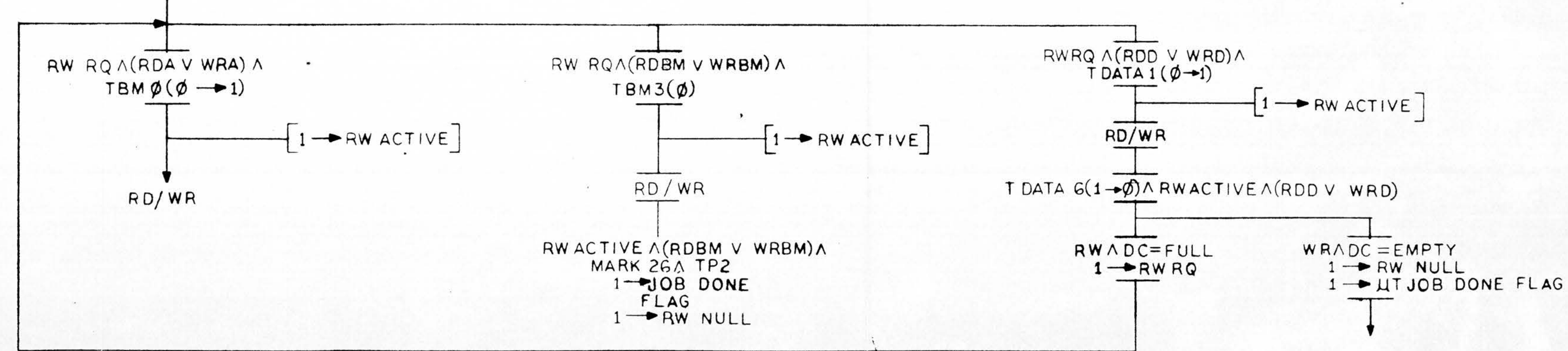
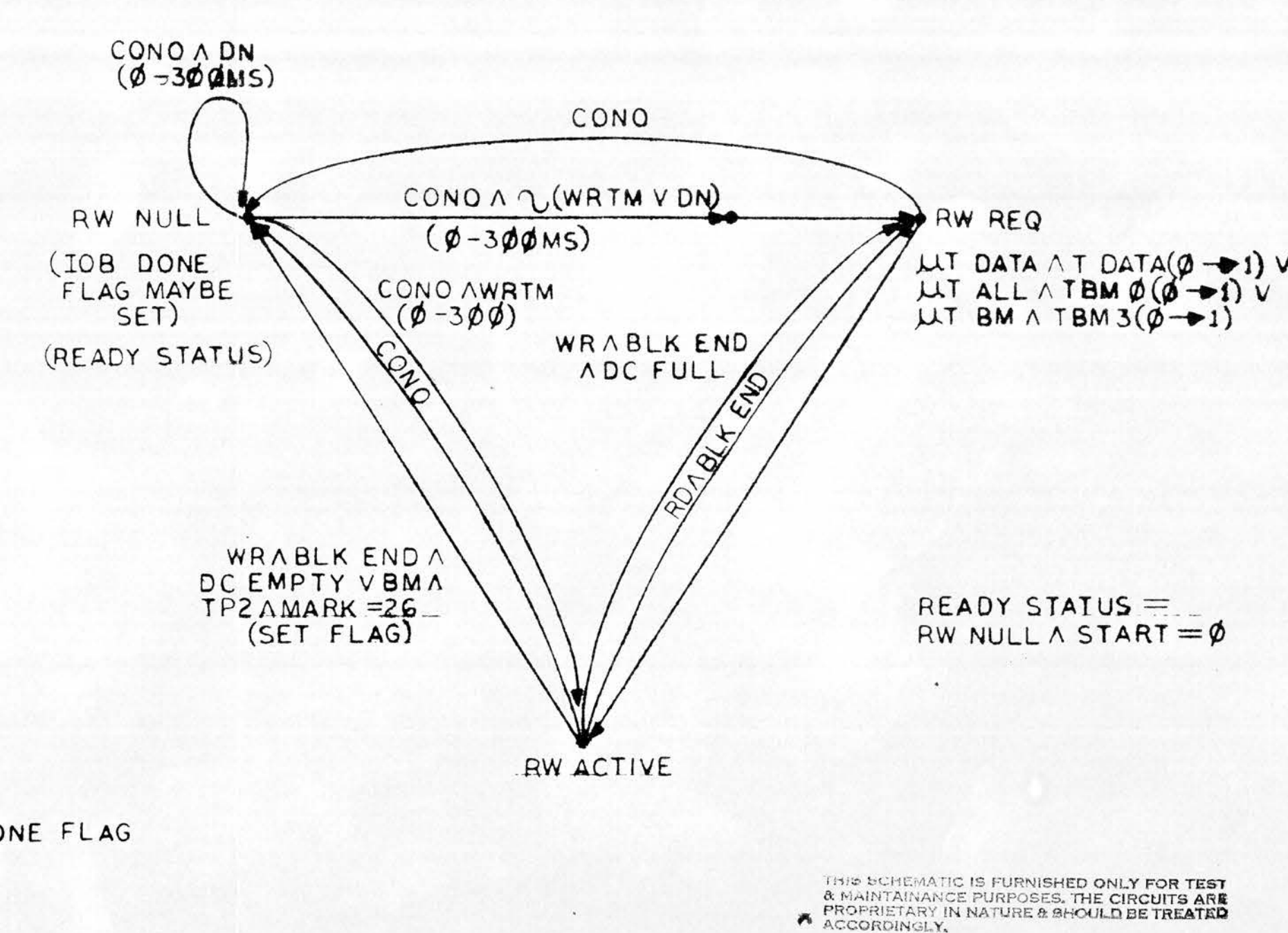


- DN 000 DO NOTHING
- RDA 001 READ ALL BITS AFTER 45
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- RDD 011 READ DATA
- WRTM 100 WRITE TIME & MARK
- WRA 101 WRITE ALL BITS AFTER 45
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	COMMON EVENTS	READ A JOB IN PROG		WRITE A JOB IN PROG	
		(CT=0) ODD	(CT=1) EVEN	CT=0 ODD	CT=1 EVEN
TP3 TP1+2	MK END: 1 → MK END FLAG	RWB CLR		RWB CLR	
TP4 TP1+3	JOB IN PROG: 0 → WREN			(WRITE A(T DATA 1 V6) V WRTM): DC → RWB	WRITE A(T DATA 1 V6) V WRTM: LB → RWB
TP0	JOB IN PROG A WRITE: 1 → WREN			RWB(J) → WB	RWB(J) → WB
TP1	WRITE COMP:	RAMP(1) → RWB READ STROBE	RAMP(1) → RWB		
TP2 TP1+1	T DATA 7 0 → 1 A LB ≠ 0: 1 → μT INFO ERROR		RWB V → LB RWB ↔ DC		RWB V → LB



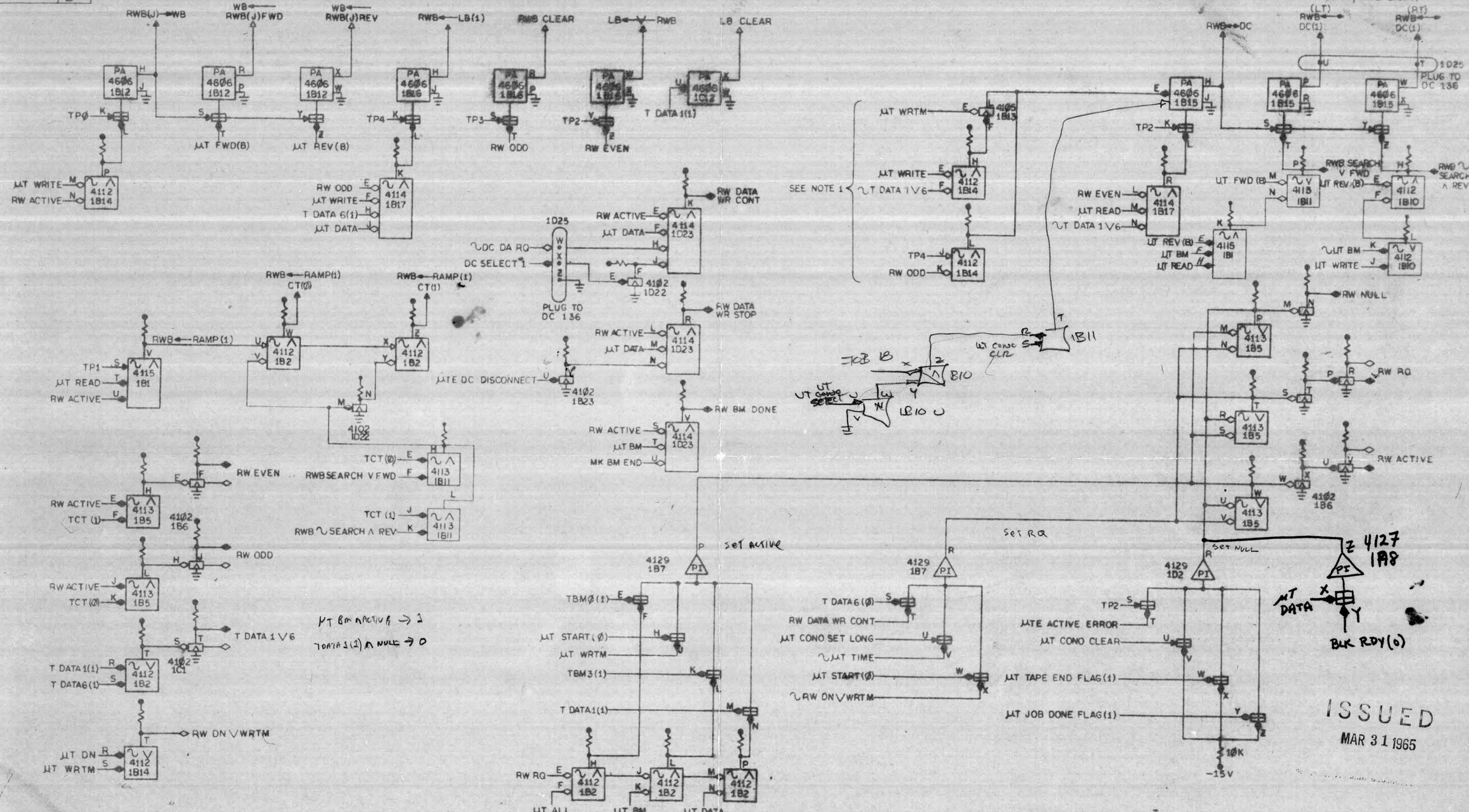
- RWB CLEAR = TP3 A RW ACTIVE A T CT(0)
- RWB ← RAMP(1) = TP1 A READ A RW ACTIVE
- LB ← RWB V = TP2 A RW ACTIVE A T CT(1)
- DC ← RWB(J) = TP2 A READ A RW ACTIVE A T CT(1) A (T DATA 1 V6)
- LB CLEAR = T DATA 1(0 → 1)
- RWB ← DC(1) = TP4 A RW ACTIVE A T CT(0) A (WRTM V WRITE A T DATA 1 V6)
- RWB(J) → WB = TP0 A WRITE A RW ACTIVE
- RWB ← LB(1) = TP4 A RW ACTIVE A T CT(1) A WRITE A T DATA 6(1) A WRTM



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OCT 6 1964

CHANGE		REVISIONS	DRAWN G.A. Bourbeau 5-19-64	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE
DATE	ENG	DATE	CHECKED R. Reed 6/2/64	DATE		FLOW DIAGRAM
DATE	ENG	DATE	ENG R. Reed 6/2/64	DATE	FOR μT 551 PDP-6	FOR μT 551 PDP-6
DATE	ENG	DATE	PROJ ENG R. Reed 6/2/64	DATE		DRWG NO
DATE	ENG	DATE	PROD. R. Reed 6/3/64	DATE	CODE FD	D-551-O-FD1
SHEET		OF		REV LTR		



NOTES
 1 (TP4 RW ODD (UT WRTM V (UT WRITE V T DATA 1 V 6)))

DRAWN <i>[Signature]</i>	DATE	4-29-64	TITLE RWB CONTROL
	CHECKED	DATE	
ENG	DATE	6/2/64	ASSY NO
PROJ ENG	DATE	6/2/64	CODE BS
REV	DATE	6/2/64	DRWG NO D-551-ORWBC
REV	DATE	6/2/64	REV LTR B

CHANGE	DATE	BY	REASON
A-23	6-29-64	BS	
B-89	6-29-64	BS	

SHEET	1	OF	1
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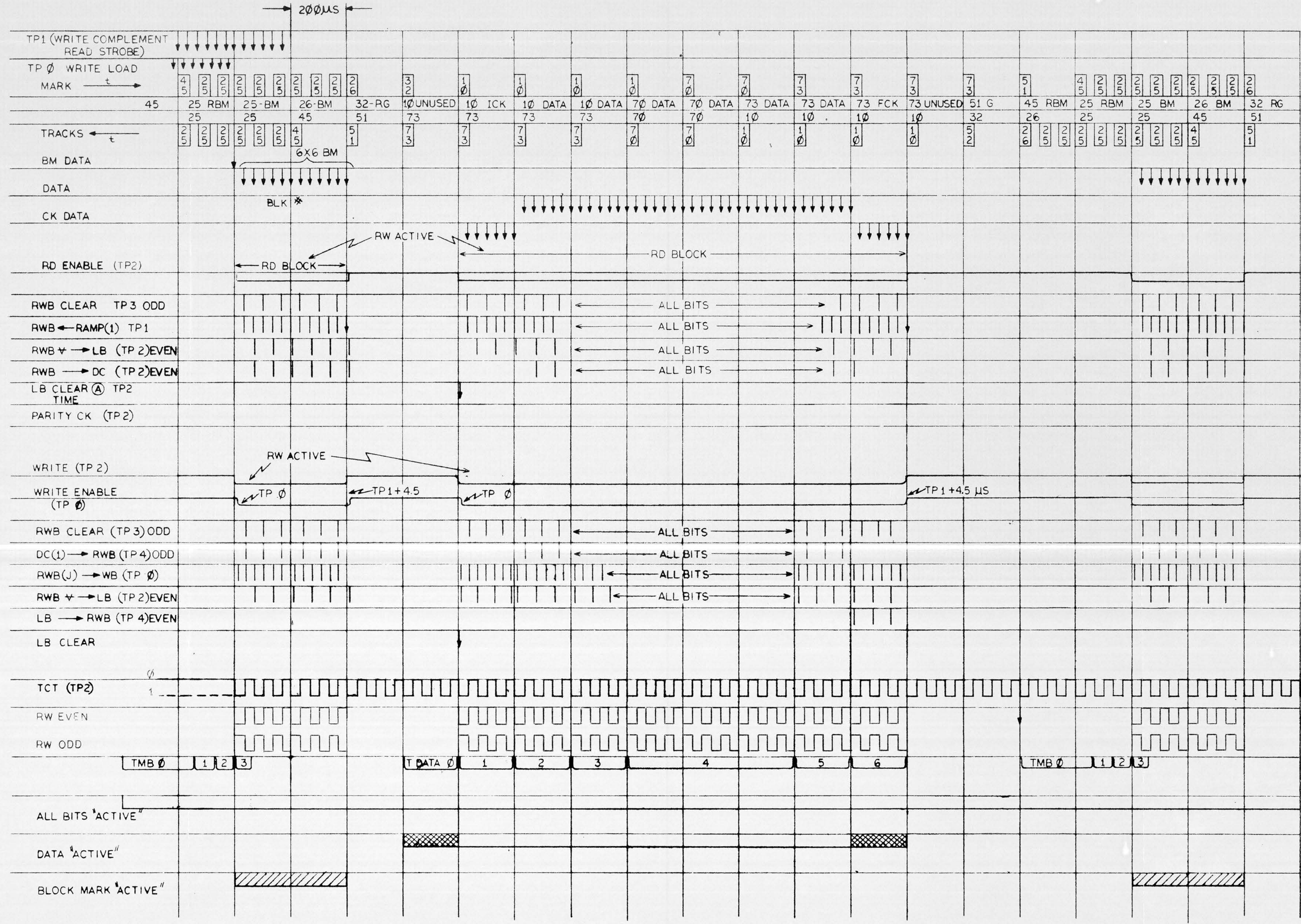
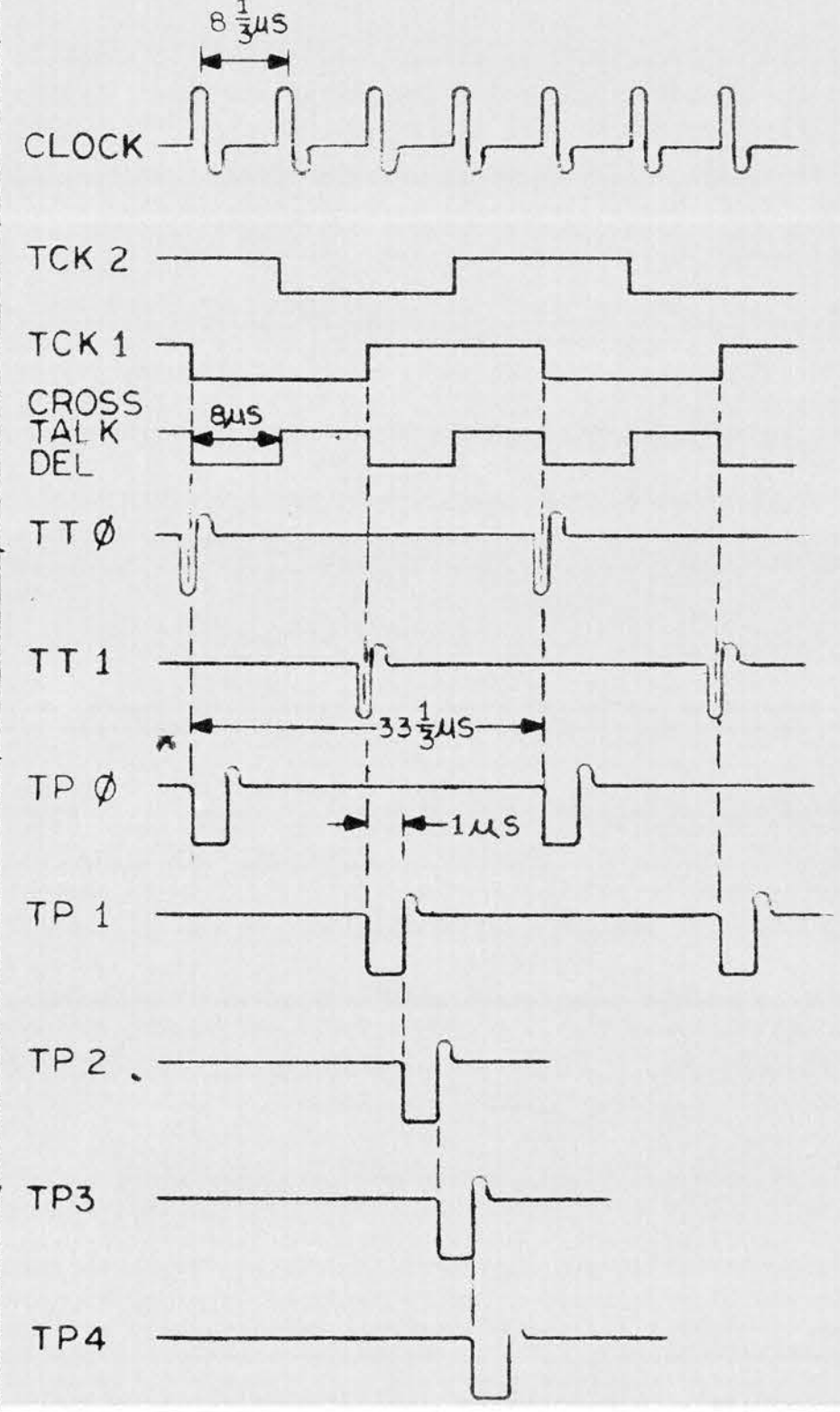
A

B

C

D

WRTM TIMING



TIMING SHOWN FOR FORWARD DIRECTION ONLY

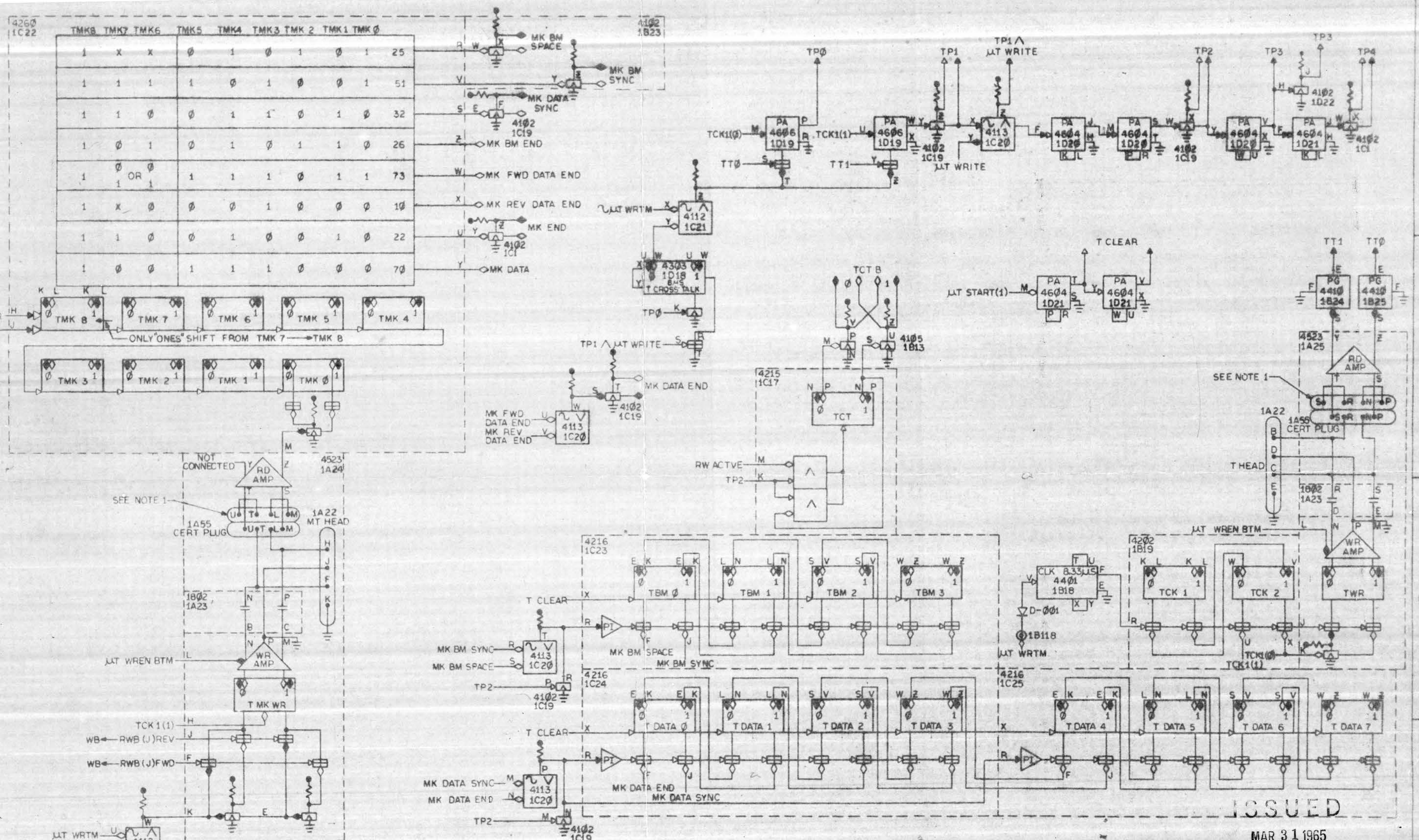
TIMING SHOWN FOR FORWARD DIRECTION ONLY

THIS SCHEMATIC IS FURNISHED ONLY FOR TEST & MAINTENANCE PURPOSES, THE CIRCUITS ARE PROPRIETARY IN NATURE & SHOULD BE TREATED ACCORDINGLY. OCT 6 1964

Table with columns: CHANGE, DATE, ENG, REVISIONS. Includes handwritten entries for DRAWN, CHECKED, ENG, PROJ ENG, PROD and their respective dates.

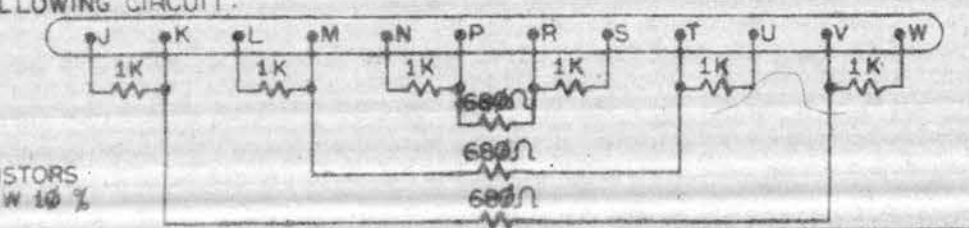
digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS ASSY NO CODE TD SHEET OF

Table with columns: TITLE, FOR, DRWG NO, REV LTR. Includes title 'TIMING DIAGRAM', part number 'UT 551 PDP-6', and drawing number 'D-551-O-TD1'.



TMK8	TMK7	TMK6	TMK5	TMK4	TMK3	TMK2	TMK1	TMK0	
1	X	X	0	1	0	1	0	1	25
1	1	1	1	0	1	0	0	1	51
1	1	0	0	1	1	0	1	0	32
1	0	1	0	1	0	1	1	0	26
1	0	OR 1	1	1	1	0	1	1	73
1	X	0	0	0	1	0	0	0	10
1	1	0	0	1	0	0	1	0	22
1	0	0	1	1	1	0	0	0	70

NOTES
 1. 22 PIN MALE AMPHENOL CONNECTOR JUMPERED AS SHOWN. FOR ATTENUATION, JUMPERS ARE REPLACED BY THE FOLLOWING CIRCUIT:



ALL RESISTORS ARE 1/2W 10%

NO.	DATE	BY	REVISIONS
C-89	8-25-64	MS	1
B-47	6-25-64	MS	2
A-23	6-25-64	MS	3

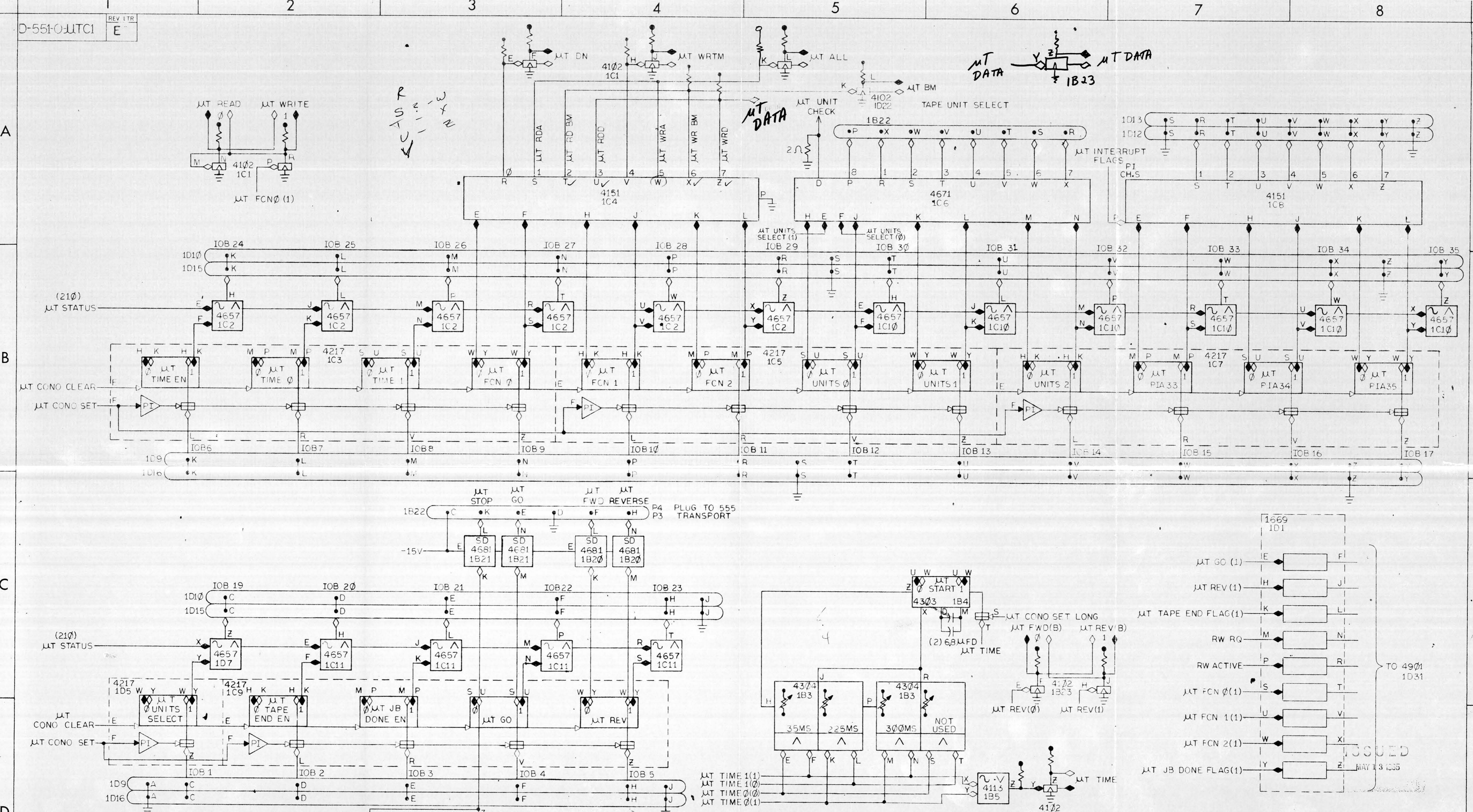
digital EQUIPMENT CORPORATION
 MAYNARD, MASSACHUSETTS

DATE: 4-27-64
 CHECKED: [Signature] 6/2/64
 END: [Signature] 6/2/64
 PROJ. ENG: [Signature] 6/2/64
 DATE: 6/2/64

ISSUED
 MAR 31 1965

T, MT CONTROL
 FOR JLT 551 PDP-6

ASSY NO: [] CODE: BS
 DRWG NO: D-551-O-TMT
 SHEET: [] OF: [] REV LTR: C



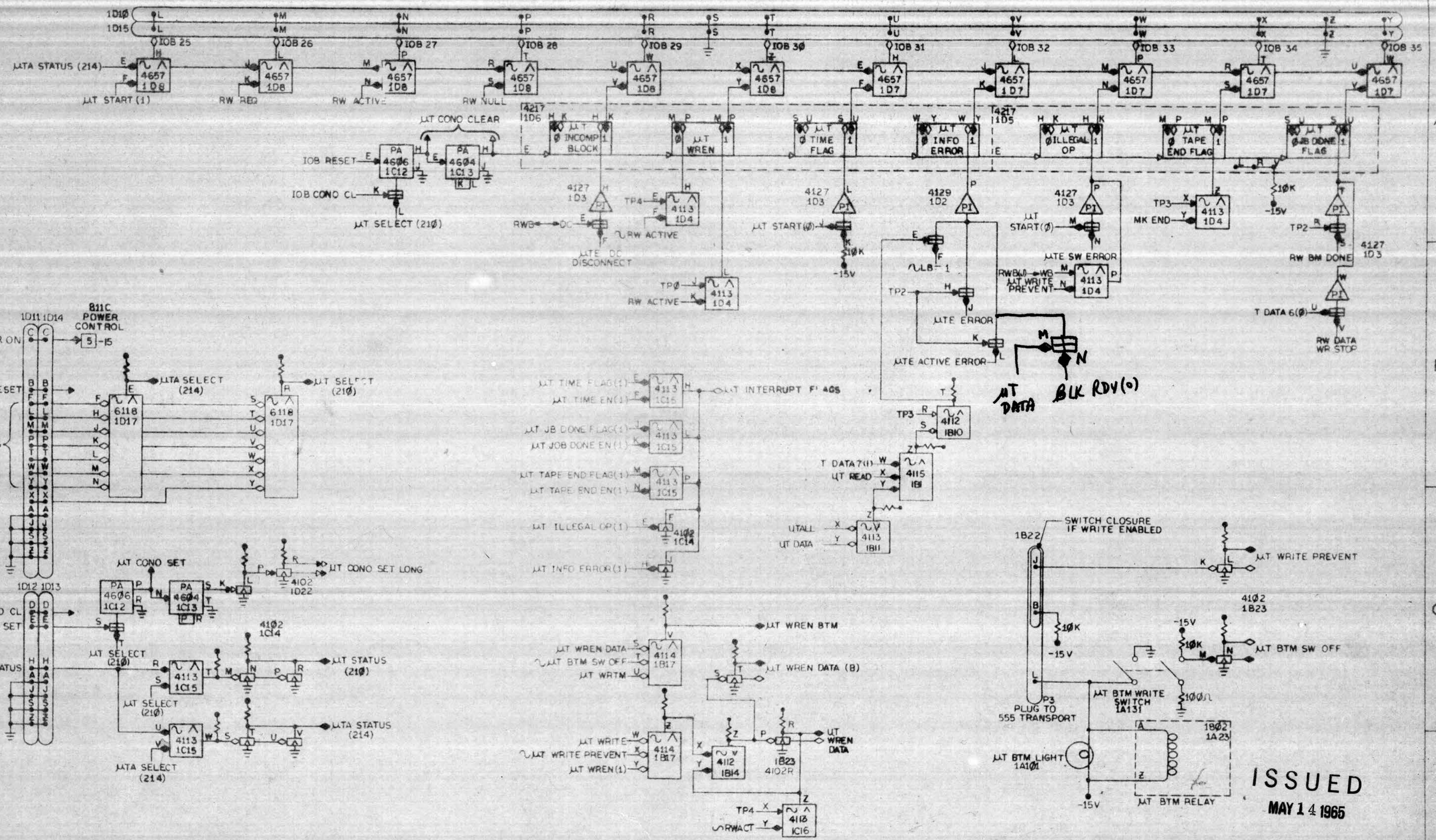
REVISIONS	CHANGE	DATE	BY	ENG
	A 23	6-20-64	W	W
	B 47	6-20-64	W	W
	C 215	7-1-64	W	W
	D 89	8-1-64	W	W
	E 136	8-1-64	W	W

DRAWN	DATE	TITLE	
3-1-64	4-24-64	WTC-DECTAPE CONTROL	
CHECKED	DATE	FOR W551 PDP-6	
ENG	DATE	ASSY NO	CODE
PROJ ENG	DATE	BS	DRWG NO
PROD	DATE		D-551-0-WTC1
SHEET		OF	REV LTR
			E

digital
EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

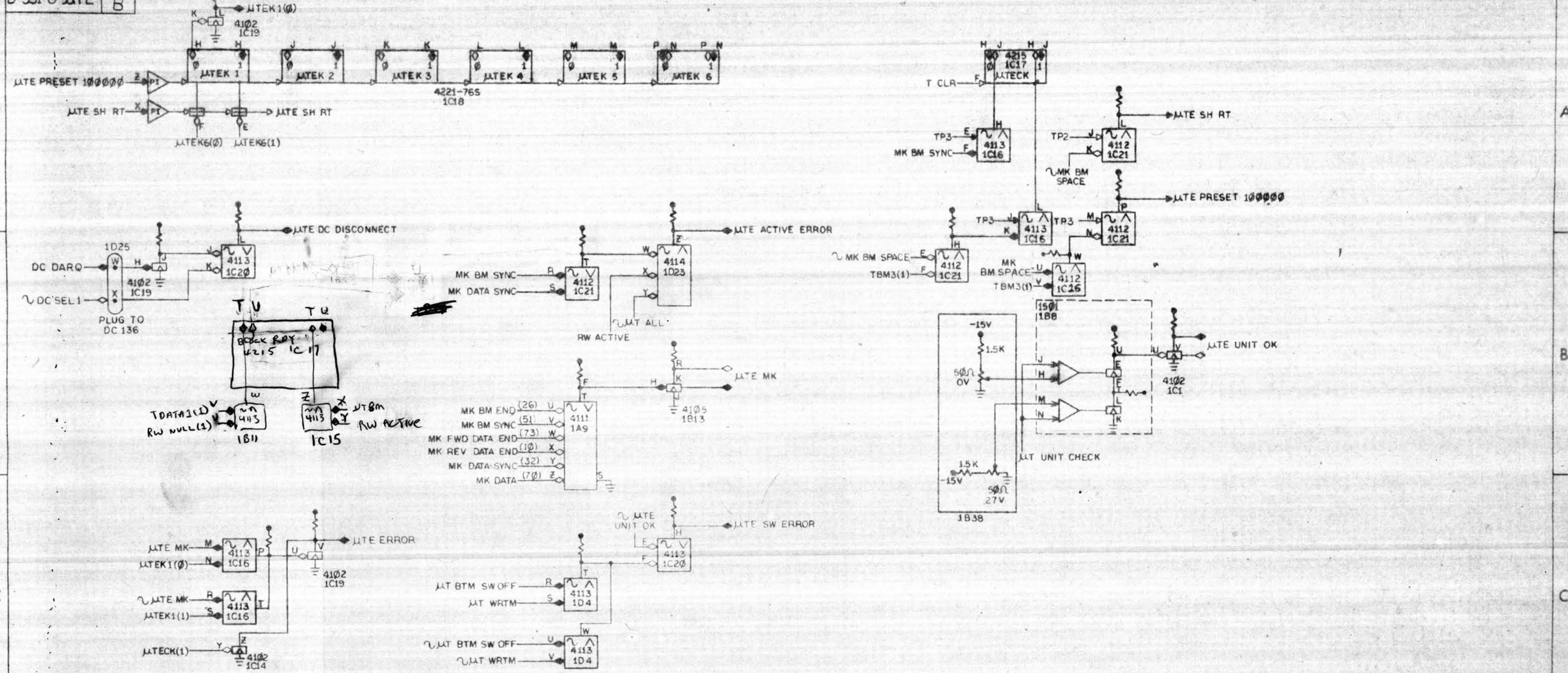
ISSUED MAY 13 1965

TO 4901 1D31



ISSUED
MAY 14 1965

DRAWN: <i>[Signature]</i> DATE: 4-28-64		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE: UJTC-DECTAPE CONTROL	
CHECKED: <i>[Signature]</i> DATE: 6/2/64	DATE: 6/2/64		FOR: UJ551 PDP-6	
ENG: <i>[Signature]</i> DATE: 6/2/64	DATE: 6/2/64	ASSY NO:	CODE: BS	DRWG NO: D-551-0-UJTC2
PROJ ENG: <i>[Signature]</i> DATE: 6/2/64	DATE: 6/2/64	SHEET: 7	OF: 7	REV LTR: E
PROD: <i>[Signature]</i> DATE: 6/2/64	DATE: 6/2/64			



ISSUED
MAY 3 1965

DRAWN: <i>[Signature]</i> DATE: 4-23-64		<p>digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS</p>	TITLE
CHECKED: <i>[Signature]</i> DATE: 4/20/64	JTE-JT ERROR		
ENG: <i>[Signature]</i> DATE: 4/20/64	FOR JT551 PDP-6		
PREP. ENG: <i>[Signature]</i> DATE: 4/20/64	ASSY NO	CODE	DRWG NO
DATE: 4/20/64		BS	D-551-O-JTE
REVISIONS		SHEET	OF
CHANGE	DATE	BY	REV LTR
A-23	6-20-65		
B-47	8-18-64		

A

1A

B

1B

C

D

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
								4111	4129	4129	4129	4127-R	4215-N	4215-N	4215-N	4113-70	4113-70	4523	4523	4523		1802 (?)	4523	4523
												LB0	LB1	RWB 2	RWB 0	RWB0	RWB CH0					TMK 0		4523
								LB0 → LBS	RWB 2	RWB 1	RWB 0	LB3	LB4	RWB 5	RWB 3	RWB1	RWB CH1					TT1, TT0		
												LB1	LB2	LB0	RWB 1	RWB0	RWB CH0	RAMP 1	RAMP 2	RAMP 3		PLUG TO 555		TT1, TT0
								ITE MK	RWB 5	RWB 4	RWB 3	LB4	LB5	LB3	RWB 4	RWB1	RWB CH1							
4115-R	4112-R	4304	4303	4113-R	4102-R	4129	1501		4112-R	4113-37	4606	4105	4112-R	4606	4606	4114-R	4401	4202	SOL DRIVER 4631	SOL DRIVER 4681		4102-R	4410	4410
LT RWB DC (1)	RW ACTIVE	deny		RW EVEN	RW EVEN				UT BM	CTO CTI	RWB (J)	RWB → DC	RWB	RWB	RWB	LB(1)						ITE FWD		
	RW ACTIVE	ITE START		RW ODD	RW ODD	RW ACTIVE	ITE UNIT OK		RWB SEARCH	CTO CTI	→ RB	ITE MK	→ DC	→ DC	LB(1)		TCK 1, JCK 2	TCK 1			ITE STOP	ITE REV		
	RW ACTIVE	ITE START	ITE START	RW NULL	RW NULL				DATA OP + BLACK NOT RDY	RWB SEARCH	→ RB	TCT B	RWB (J)	TK/GV CHAR LT RWB		→ DC					PLUG TO 555	ITE BTX SW OFF	ITE WREN DATA (B)	
RWB → RAMP (1)	T DATA 1V6			RW REQ	REQ					RWB FWD	→ RB	ITE DNV WRDM	→ RB	FWD	CLEAR					REV	ITE GO		RW DATA WR STOP	
→ INFO ERROR	RWB RAMP (1) CT(0)			RW ACTIVE	RW	RW ACTIVE				Block RDYR	→ RB	(CLAMP) ITE MK	→ RB	TK/GV CHAR RT RWB	→ RB	→ DC(1)							ITE WREN DATA	
	RWB RAMP 1 CT(1)			ACTIVE	ACTIVE					→ RB	REV	(CLAMP) TCT B	(CLAMP) TCT B	→ DC(1)	REV	RWB V							ITE WREN DATA	
				ITE TIME	ITE TIME																			ITE DATA

ISSUED
MAY 13 1965

I= INSTALL JUMPER BETWEEN CENTER LUG & LUG NEAREST PLUG
N= INSTALL JUMPER BETWEEN CENTER LUG & LUG FARTEST FROM PLUG

REVISIONS		DRAWN	DATE	TITLE	
CHANGE	DATE	M. MARIANO	6/29/64	UT UTILIZATION MODULE LIST	
A-23	6-29-64	CHECKED	7/1/64	FOR UT 551 PDP-6	
B-89	8-10-64	ENG		ASSY NO	
C-110	8-10-64	PROJ ENG		CODE	
D-136	8-10-64	PROD	7/6/64	DRWG NO	
				REV LTR	
				D	

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

SHEET 1 OF 2

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25		
	4122-R	4657	4217	4151-370	4217	4671	4217	4151	4217	4657	4657(2)	4606	4504	4102-150	4113-07	4113-06	4215 I	4221-77	4102-717	4113-R	4112-R	4260	4216	4216	4216		
A	UT DR UT WRTM UT ALL UT READ UT WRITE T DATA 1V6 UTE UNIT OK TP4 MK END	UT TIME EN UT TIME UT TIME UT TIME UT FCN 0 UT FCN 1 UT FCN 2	UT TIME EN UT TIME UT TIME UT TIME UT FCN 1 UT FCN 0	UT FCN 1 UT FCN 2 ROA, ROBM, RD D, WR A, WR BM, WRD	UT FCN 1 UT FCN 2 TAP UNIT SELECT	UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35	UT TAPE END EN UT TAPE END EN UT TAPE END EN UT UNITS 2 UT UNITS 33 UT UNITS 34 UT UNITS 35

B	1669	4129	4127	4113-04	4217	4217	4657	4657	22 PIN METHODE MALE PLUGS								6118-R	4303	4606	4604	4604	4604	4102-R	4114-R	22 PIN METHODE MALE PLUGS	
	UT GO UT REV UT END FLAG RW RQ RW ACTIVE UT FCN0 UT FCN1 UT FCN2 UT JOB DONE FLAG	UT INCOMP BLOCK UT TIME FLAG UT INFO ERROR UT ILLEGAL OP RW UT JB DONE UT DONE UT GO	UT INCOMP BLOCK UT TIME FLAG UT INFO ERROR UT ILLEGAL OP UT TAPE END FLAG UT SW ERROR UT TAPE END FLAG UT GO	UT ILLEGAL OP UT TAPE END FLAG UT SW ERROR UT UNITS SELECT	UT INCOMP BLOCK UT WREN UT WREN UT TIME FLAG UT INFO ERROR UT UNITS SELECT	UT TIME FLAG UT INFO ERROR UT ILLEGAL OP UT TAPE END FLAG UT JB DONE UT JB DONE UT UNITS SELECT	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	UT START RW REQ RW ACTIVE RW NULL UT INCOMP BLOCK UT WREN	

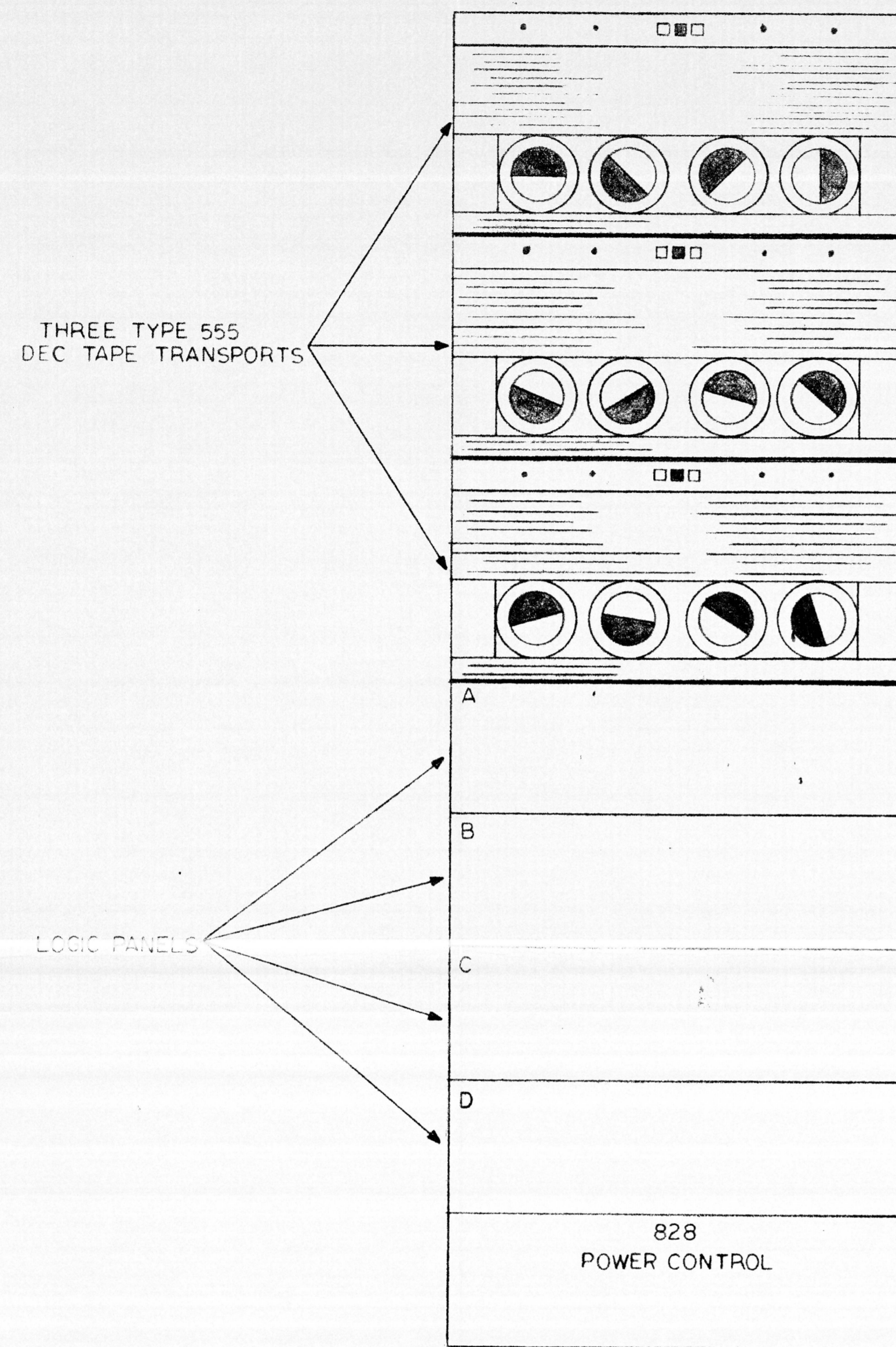
ISSUED
MAY 13 1965

I= INSTALL JUMPER BETWEEN CENTER LUG & LUG NEAREST PLUG
N= INSTALL JUMPER BETWEEN CENTER LUG & LUG FARTHEST FROM PLUG

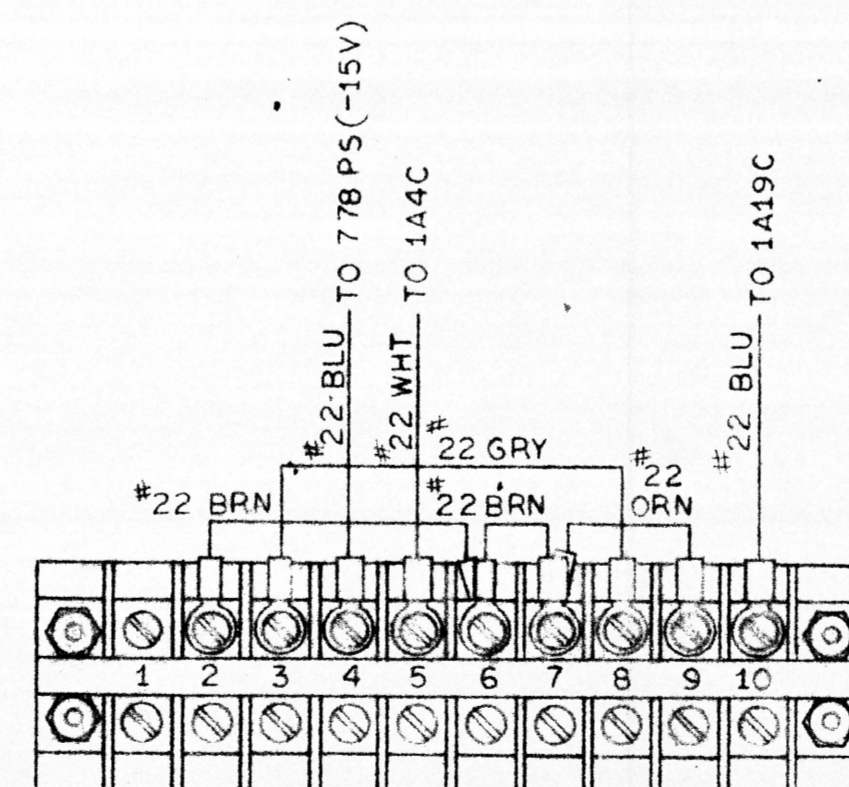
CHANGE	A-23	DATE	6/29/64	ENGINEER	RS
REVISIONS	B-89	DATE	7/1/64	ENGINEER	RS
	C-110	DATE	7/1/64	ENGINEER	RS
	D-13C	DATE	7/1/64	ENGINEER	RS

DRAWN	M. MARIANO	DATE	6/29/64
CHECKED	B. REED	DATE	7/1/64
ENG	B. REED	DATE	7/1/64
PROJ ENG	B. REED	DATE	7/1/64
PROD	R. SMALL	DATE	7/1/64

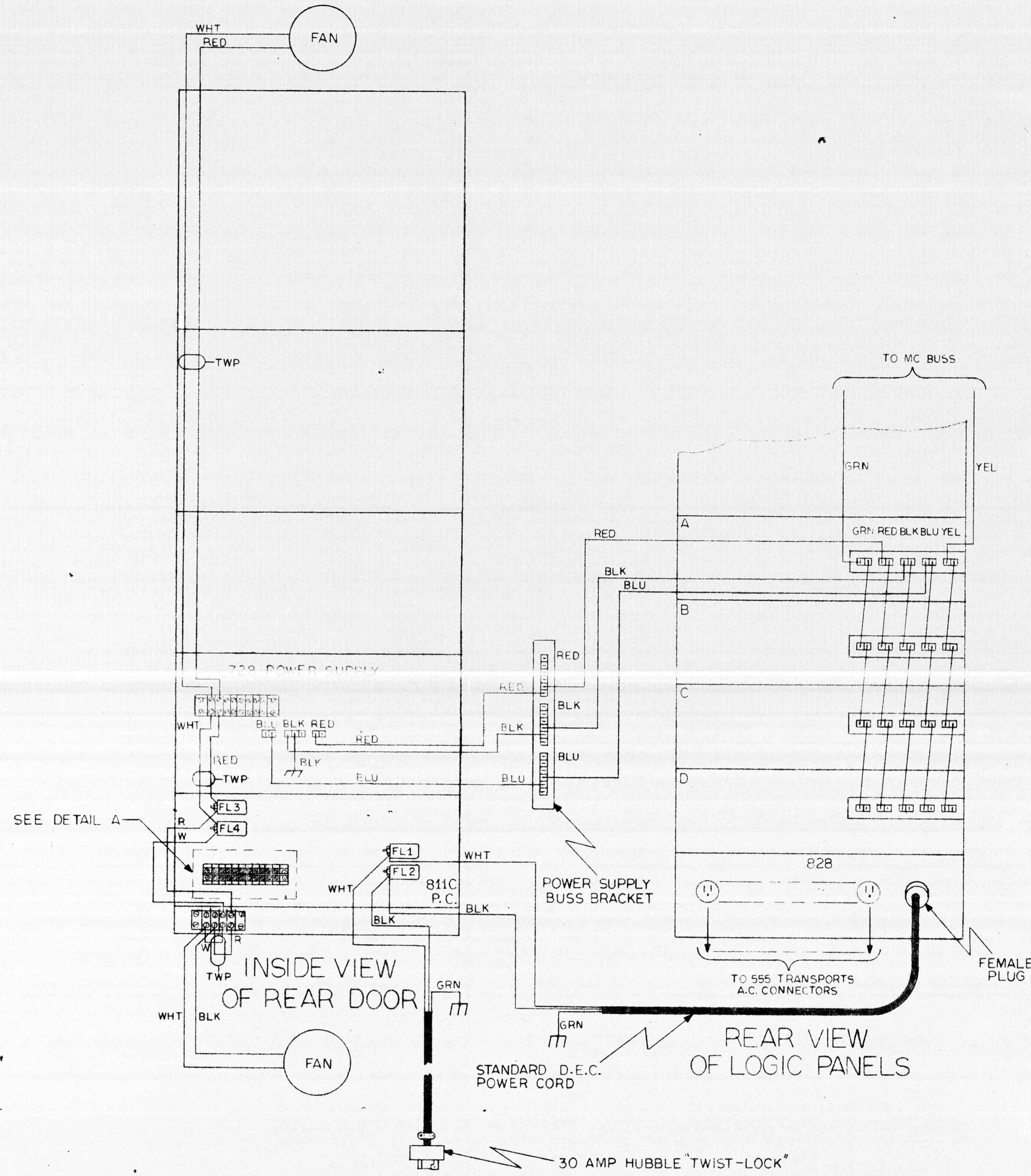
digital		EQUIPMENT CORPORATION		MAYNARD, MASSACHUSETTS	
ASSY NO	CODE	DRWG NO	REV LTR	TITLE	
	UML	D-551-O-UTML	D	UT UTILIZATION MODULE LIST	
SHEET 2 OF 2		FOR UT 551 PDP-6			



FRONT



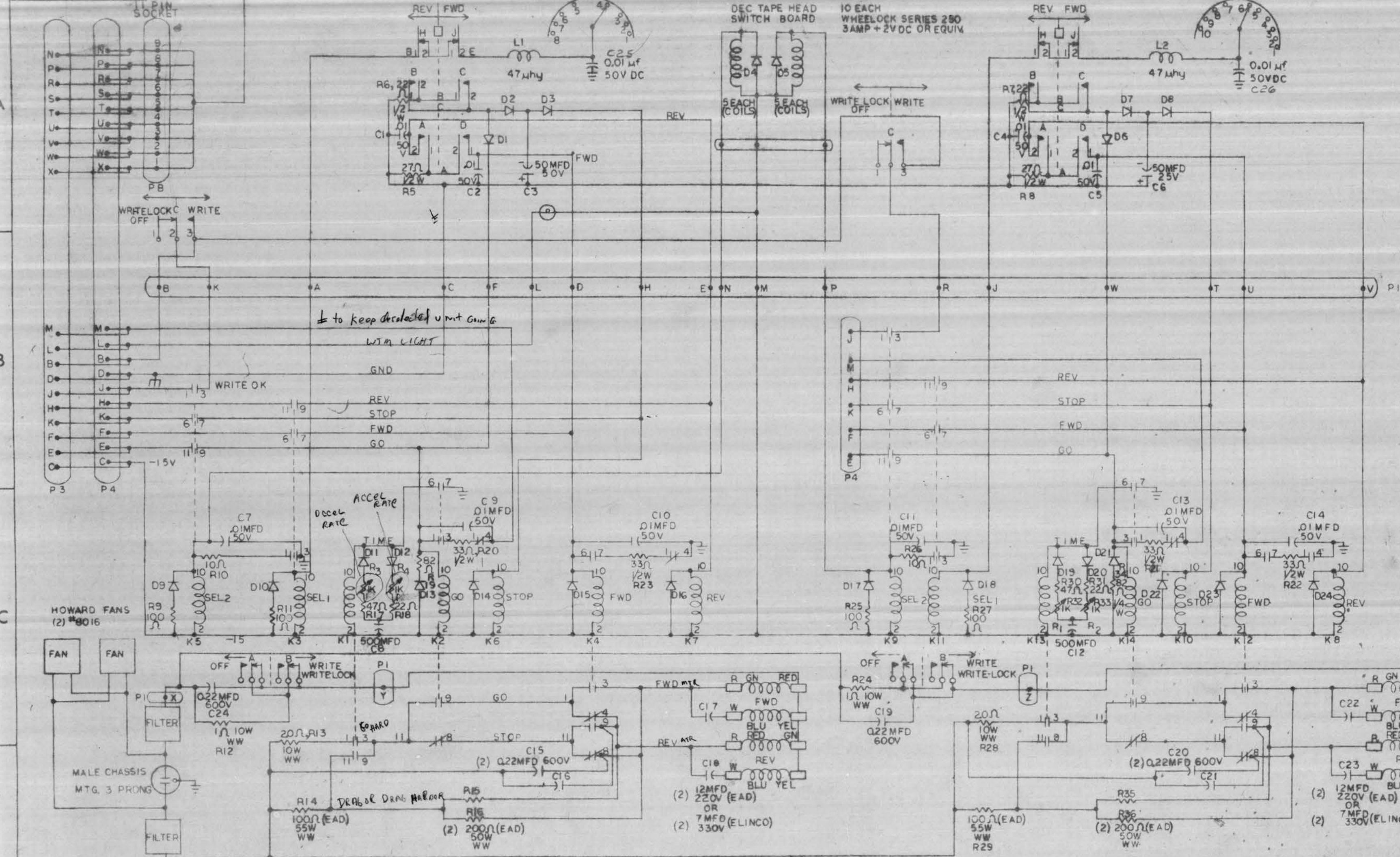
DETAIL A



NOTE 1. UNLESS OTHERWISE SPECIFIED ALL WIRE SHALL BE #14 AWG

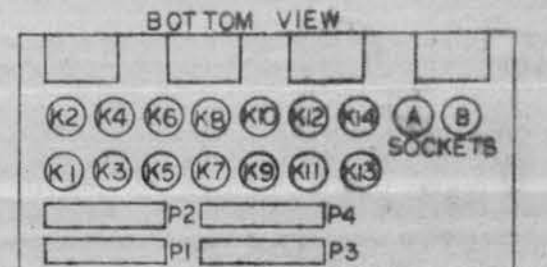
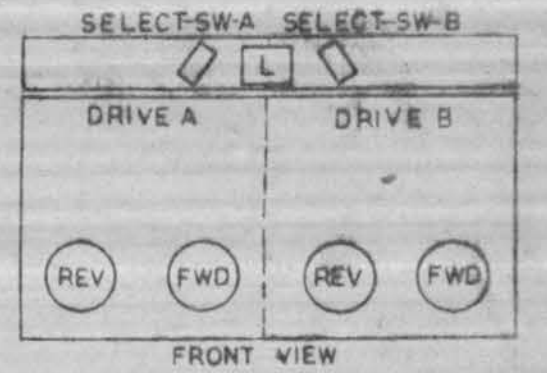
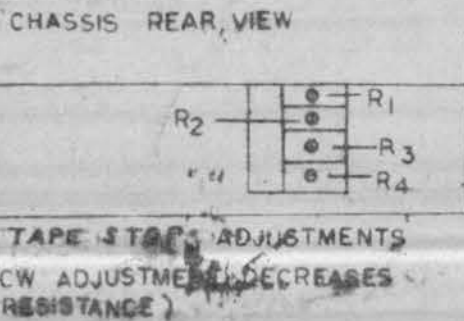
OCT 6 1964
THIS SCHEMATIC IS FURNISHED ONLY FOR TEST & MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE & SHOULD BE TREATED ACCORDINGLY.

DRAWN <i>[Signature]</i>		DATE 5-26-64	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE UT POWER WIRING
CHECKED <i>[Signature]</i>		DATE 6/2/64		FOR UT551 PDP-6
ENG <i>[Signature]</i>		DATE 6/1/64	ASSY NO	DRWG NO D-551-0-UTPW
PROJ ENG <i>[Signature]</i>		DATE 6/1/64	CODE PW	REV LTR B
PROD <i>[Signature]</i>		DATE 6/1/64	SHEET	OF
CHANGE A-47	DATE 8-18-64	ENG <i>[Signature]</i>		
REVISIONS B-2133	DATE 9/5/64	ENG <i>[Signature]</i>		

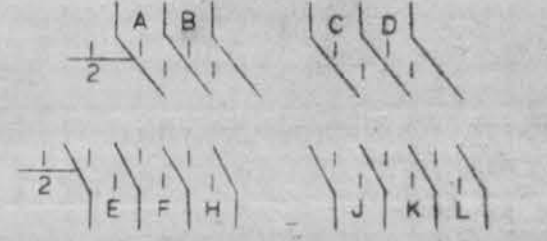


NOTE: ALL DIODES ARE IN1220

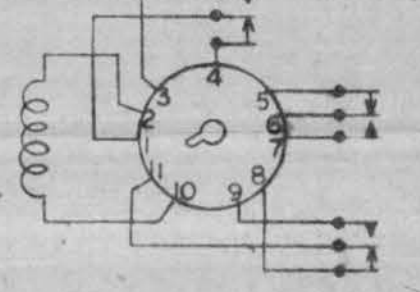
NOTE:
 R1. SIDE B LONG GO TIME STOP ADJ.
 R2. SIDE B SHORT GO TIME STOP ADJ.
 R3. SIDE A LONG GO TIME STOP ADJ.
 R4. SIDE A SHORT GO TIME STOP ADJ.



FWD-REV SWITCH CONNECTIONS (WIREMAN'S VIEW)



RELAY WIRING - P/B TYPE KR14DG 12V 3POLE



ISSUED
 MAY 3 1965

DRAWN	RAY HATT	DATE	12/30/63
CHECKED		DATE	1/16/64
ENG		DATE	1/21/64
PROV ENG		DATE	7/2/64
APP		DATE	1/21/64

digital
 EQUIPMENT CORPORATION
 MAYNARD, MASSACHUSETTS

TITLE	CHASSIS WIRING
FOR	DECTAPE 555
ASSY NO	
CODE	CS
DRWG NO	D-25326
SHEET	7 OF 8