Marho-7

Installed User Program

APL Decision Table Processor (DTABL) Program Description/Operations Manual

Program Number: 5796-PJB

This manual provides instructions for the use of a Decision Table Translator and auxiliary routines that have been implemented to run under several APL systems. Decision tables can be entered, displayed, edited, saved, and compiled with DTABL. The results of compilation can be displayed in several formats - · "abstracts", COBOL, PL/I, ALGOL, and APL. APL programs can be generated in executable form. Programs in other languages can be written on a data set and be used as input to other language processors. The decision tables and object code produced from them can be saved within the APL system for future reference or manipulation in separate terminal sessions.



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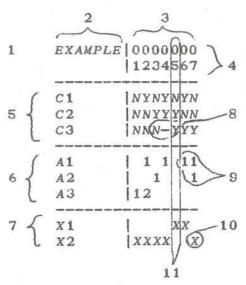
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INTRODUCTION AND DECISION TABLE CONCEPTS

This manual provides instructions for the use of a Decision Table Compiler and auxiliary routines that have been implemented to run under several APL systems (APL.SV, APL/CMS, and VS APL under CMS, VSPC or TSO). Decision tables can be entered, displayed, edited, saved, and compiled with DTABL. The results of compilation can be displayed in several formats — "abstract", COBOL, PL/I, ALGOL, and APL. APL programs can be generated in executable form. Programs in other languages can be written on a data set and be used as input to other language processors. The decision tables and object code produced from them can be saved within the APL system for future reference or manipulation in separate terminal sessions.

Figure 1 is an example of a decision table with an indication of significant elements and terminology used in this manual.



- Table header
- 2. Stub section
- 3. Entry section
- 4. Header section
- 5. Condition section
- 6. Action section
- 7. Exit section
- 8. Condition values (Y=yes, N=no, -=don't care)
- 9. Action sequence numbers
- 10. Exit indicators
- 11. Rule column number 5

Decision Table Anatomy Figure 1.

The decision table allows specification of program <u>logic</u>, and the compiler converts this specification into an efficient <u>procedure</u>. The condition section of the table speci-

fies one or more conditions to be tested (but does not imply the order of testing). The results of the condition tests are recorded in the condition entries. This compiler handles only "Limited Entry Decision Tables" (LEDTs) -i.e., those which restrict the values of the condition tests to yes (Y) or no (N). Therefore, a condition test is any expression (in some particular language) that yields only a single yes (true) or no (false) answer. Examples of condition tests are "A<B" (PL/I), ")/A=B" (APL), and "A is less than B" (English). Each column of the entry section is considered to be a rule and contains some (unique) combination of test values. These combinations must exhaust all possible combinations of test values or the table is incompletely specified. For example, if there are "n" binary (two-valued) tests, then there will be 2ⁿ combinations in a valid table.

The action section of the table contains actions which will be performed (or not) depending upon the results of testing the conditions in the condition section. An action is any imperative expression in the language selected. Examples are, "A=B+C" (PL/I) and "Give the customer a discount" (English). Note that actions may not specify or imply flow control. (Branching or GO TO statements make no sense in a decision table context.) When a combination of test values matches those specified in a rule column, the actions in that column are performed in the order specified by the numbers indicated in that column. A blank opposite an action indicates that it is not to be performed. example, in Figure 1 if all three conditions test to "no" (NNN) then rule number 1 is selected, causing the third action (A3) to be executed. If actions carry the same number, no implied order is required for their execution with respect to each other. There is also to be no order required between actions and tests or among tests.

The exit section of the table is analogous to the action section. However, exits specify the method of exiting the table. An exit can consist of the word RETURN, indicating the program generated from the table is a subroutine which will return to its caller. An exit can also indicate the name of some procedural code which can be reached by a branch from the decision table. (APL decision tables must always generate subroutines. However, this requirement does not extend to the other languages.) In the above example, the second exit (X2) is taken after execution of A3 in rule 1. There must be exactly one exit selected for each rule column, and exits will always follow tests and actions when executed.

Central to the effectiveness of DTABL is the convention that no test or action in a given table will change the results of any test in the same table. This gives the compiler complete freedom to determine the order of tests in optimizing the object code. The ordering of actions with respect

to tests is therefore immaterial. The user can, however, specify the order of actions with respect to each other (as indicated earlier).

For convenience, rule columns may be combined by the user. The conditions allowing two columns to be combined are:

- actions and exits in the two columns are identical
- 2) condition values are identical except one
- 3) the values in the differing condition are "Y" and "N".

To consolidate two columns into one, replace the value of the differing condition with "don't care" in one column, and discard the other column. This function can be performed by a program (MERGE) that is available with DTABL and described later in this manual. These two rules were applied to form column 4 of the decision table in Figure 1. Figure 2a shows column 4 and the two rule columns (4a and 4b) that it stands for.

	4	4a	4b	107-0794	Z	Za	Zb
C1	Y	Y	Y	C1	N ·	N	N
C2	Y	Y	Y	C2	Y	Y	Y
C3_	-	Y	Ņ	С3		-	-
	1	1	1	C4	-	Y	N
A1 A2				A 1	1	1	1
A3			teat Services	A2			
X 1				A3_	1	1	1
X2	X	X	X	X1	X	Х	Х
		(a)			(b)	

Rule Column Consolidation Figure 2.

Figure 2b shows two columns (Za and Zb) from some other decision table and how they are consolidated into a single column (Z). Note that columns Za and Zb each stand for two columns, so column Z stands for four columns. (Note also that the order or actions A1 and A3 in the code produced from column Z will be immaterial because they both have the same selection number (1) in the entry positions.)

SIGN-ON PROCEDURE

There are a variety of terminals that can be attached to an APL system. The terminal discipline and sign-on procedures for each are described in [4-8] and are beyond the scope of this manual. You should be familiar with these procedures before attempting to use the APL system. For your convenience the APL keyboard is shown in Figure 3.



Keyboard Arrangement for APL Figure 3.

Once APL has acknowledged your sign-on procedure you obtain your initial copy of DTABL from an APL public library by typing

LOAD libno DTABL

followed by a carriage return or enter key. Note that you will always signal that your input is complete and ready to be acted upon by pressing the return (or enter) key. (In the above line "libno" stands for the library number -- such as 27 -- which is assigned to your installation's copy of the DTABL processor by the APL administrator.)

To sign off at the end of a session, type

OFF

When the APL system responds with your usage statistics, you then turn off the power on the terminal. If you are using an acoustic coupler turn its power off and hang up the phone.

INITIAL TABLE ENTRY

Your first act in using the DTABL programs will be to enter a decision table. The initial entry of a decision table is aided by a program which you invoke by typing "INPUT" (followed by a carriage return or enter key). You will be prompted for each input needed to fill out each section of the table. (Note: If you are using an IBM 3270 terminal, enter "IS3270-1" before you enter INPUT so that prompting messages will appear at the proper place on your screen. At the end of this manual is a sample dialogue used to prepare and manipulate the table shown in Figure 1. The following comments give the specific rules for entering information.

The following paragraphs are headed by the prompting messages issued by the table-entry program. The paragraphs explain the significance of prompting. Figure 4, following these descriptions, shows how the table of Figure 1 was entered. A copy of this table is included with DTABL. In Figure 4 and examples subsequent to it the right-pointing arrows (→) indicate lines entered by the user. Other lines are produced by the DTABL programs.

ENTER TABLE HEADER

This table header will be placed in the top of the table and will be associated with all related output for identification. In generated programs (except APL) this header will be prefixed to all generated labels. Those programs that require both table and object code (TESTGEN and PRINTHLL) use this header to ensure compatibility of their inputs. In APL programs the table header becomes the function header. (Note that the header may not be empty. If it is the input process will be cancelled.)

ENTER CONDITION STUBS

Following this message you enter the condition stubs one line at a time. You signal the end of the condition section by entering an "empty line" (carriage return only). You can place any text that is appropriate to the target language in the decision table stubs. The stub can also contain "space" information refered to in the section COMPILING DECISION TABLES. (Note that you must enter at least one condition stub or the input process will be cancelled.)

ENTER ACTION STUBS

You respond to this message as you did to the conditions message, this time entering action stubs. An action stub can be any text suitable to the target language. You enter an empty line (carriage return only) to signal the end of the action stub secton. (Note that you must enter at least one action stub or the input process will be cancelled.)

ENTER EXIT STUBS

Your response is the same as with conditions and actions, but you enter exit stubs. Exit stubs are to be labels that are suitable to the target language. The special keyword "RETURN" in an exit stub will be recognized as a program exit and will generate a RETURN statement in the object code where the target language contains such a statement. APL programs will treat all exits as the same (as a return). As before, you enter a carriage return (empty line) to signal the end of this table section. (Note that you must enter at least one exit stub or the input

process will be cancelled.)

HOW MANY RULES ARE THERE?

You respond by entering the number of rule columns that you will require in the entry section of the table. All subsequent entry input must contain this number of columns. If you respond to the rule-count request with a zero (0) or an empty line the input program assumes that you want the maximum number of columns. It will then automatically generate all of the condition entries for you in a standard way. You will also be told how many rule columns there are. The standard condition entry sequence is as follows: The first condition row will have the first half of its columns filled with "Y" and the second half filled with "N". In any subsequent condition row the first half of the columns under a "Y" sequence in the previous row will be filled with "Y" and the second half with "N". This is also true for the columns under each "N" sequence in the previous row. This rule is followed for all rows so that the last condition row contains "YNYNYN ". For example, if there are four conditions there will be sixteen columns filled as follows:

Following your entry of the rule column count you will be prompted to supply the entry data. The prompting will consist of the low-order digit of each column number and the stub information (that you have previously entered). Figure 4 shows this. You type the entries just as you want to see them in the final table. If the "standard" conditions were requested, prompting will start with the actions, otherwise prompting will start with conditions.

There are certain restrictions on the characters that are acceptable in each entry section of the table. The condition section accepts only the characters "Y", "N", and "-", for "yes", "no", and "don't care" values respectively. The action section can receive numbers, letters, underscored letters, or blanks. These characters allow an ordering of the actions in a given rule column. The numbers are lowest, letters next, and underscored letters highest. A \underline{Z} (underscored Z) has a value equivalent to 61. A blank indicates that an action does not participate in a rule. The exit section accepts an X to indicate that an exit participates in a particular rule, and is blank otherwise.

BAD INPUT

If any of the above restrictions is violated, your input is reprinted with the illegal character(s) replaced by an "x". You can then correct the line by typing the correct character beneath the "x". You can also revise any other characters by typing (non-blank) characters beneath them. If you want to replace a character with a blank, type a slash (/) beneath it. You will not be allowed to proceed to the next line until you have eliminated all illegal characters from the current line.

OK (NG)

After the last exit entry is accepted, the entire decision table is scrutinized for global errors. The following checks are performed:

- Completeness: All possible combinations of condition values are accounted for.
- Consistency: No combination of condition values is accounted for in more than one rule.
- Action/Exit use: Each action and exit should be used in at least one rule. (warning only)
- Exit consistency: Exactly one exit must be used in each rule column.

If the above scrutiny finds the table to be correct, the letters "OK" are typed. Otherwise, detailed diagnostics and the letters "NG" are printed. These diagnostics are self-explanatory except for the following abbreviations:

CDN -- Condition number

YCT -- Yes count (number of yes's in a row)

NCT -- No count

DIF -- Difference between YCT and NCT

The yes- and no-counts are computed taking into account the "don't cares" in each column. A column with one "don't care" stands for two columns, so each yes or no in that column is counted twice. These counts can be useful in locating typing errors in the condition section of a table. For example, if the table in Figure 1 had a Y instead of an N in the second column of condition 2, then you will be told that for condition 2 the yes-count is 5 and the no-count is 3. You are also told that columns 2 and 4 overlap. So the trouble is narrowed to condition 2, columns 2 and 4. You are also told that the rule "YNN" is missing. You can then conclude that if column 2 were changed to "YNN" that the table would be satisfactory.

)LOAD DTABL ALOAD DTABL WORKSPACE SAVED 14:56:37 08/17/76 INPUT ENTER TABLE HEADER AARROW (→) INDICATES LINES → EXAMPLE ATYPED BY THE USER. ALL OTHER ENTER CONDITION STUBS ALINES ARE OUTFUT FROM THE ASYSTEM. (THE ARROWS ARE NOT C1 C2 ANORMALLY PART OF THE LISTING.) C3ENTER ACTION STUBS → A1 → A2 A3 ADOUBLE ARROWS (---) INDICATE A ACARPIAGE RETURN PERFORMED BY ENTER EXIT STUBS ATHE USER. → X1 → X2 HOW MANY RULES ARE THERE? 7 0000000 1234567 C1 → NYNYNYN 1234567 C2 → NN YYNN BAD INPUT ABLANK IS NOT A LEGAL CONDITION. NN×YYNN C2 ACORRECT ONLY THE BAD COLUMN. Y 1234567 C3 → NNN-YYY 1234567 A1 1 1 11 1234567 A2 1 1 1234567 A3 12 1234567 X1 XX1234567 X2 → XXXX X ATHE TABLE IS CONSISTENT. OK

Example of Initial Table Entry Figure 4.

ATABLE SAVED AND NAMED.

SAMPLE-SAVE

The input process is completed with the typing of OK or NG. If there were errors you can correct them as described below under EDITING A DECISION TABLE. If you should try to compile a table with errors, the results will be unpredictable.

You can cancel the input process by entering an empty line when prompted for the table header, or condition, action, or

exit stubs. (The implication is that a table must have a header and at least one condition, action and exit.) You can also cancel input if you enter an "x" (an APL multiply sign) in any entry.

Table verification can occasionally produce many error messages. To terminate these prematurely, press the attention button and enter

→STOPCHECK

You can then edit the resulting table to remove gross errors.

CORRECTION OF TYPING ERRORS

If you discover an error in typing a line <u>before</u> you have given the carriage return, you can correct it by backspacing to the point of error and pressing the attention button. This will erase all of the line at, and to the right of the point of error. A linefeed will acknowledge this along with a caret. You continue the line just below the caret. This facility is available on all input. (On an IBM 3270 you just backspace and type over the incorrect input.) If your error is discovered after a carriage return, it is better corrected via EDIT.

STORING AND DISPLAYING DECISION TABLES

After you have finished the input process described above, the decision table exists in a special internal format that allows efficient processing. We say the decision table is "loaded" when it is in this condition. To display a loaded decision table, simply type

SAVE

To place the decision table into a compact form and give it a name, type

tname-SAVE

An example of this is given as the last line of Figure 4. In future operatons of editing, compiling, etc., you can process this table by referring to its name. (e.g., EDIT tname, COMPILE tname). You can both save and display a decision table at the same time by typing

tname-□-SAVE

You are cautioned that none of these processes save the

table across terminal sessions. In naming your decision table you should avoid the names of data structures being used by the processor. Most of these start with an underscored letter. Those that don't are listed in the section TABLE OF RESERVED NAMES near the end of this manual.

A decision table can be reloaded by typing:

LOAD tname

It is seldom necessary to do this, however, because this operation is incorporated into all of the functions that manipulate decision tables.

SPREAD tname

also displays the table in compact form. In this display vertical bars are placed between the entry columns to improve readability.

SAVING TABLES ACROSS TERMINAL SESSIONS

In the APL system there is a library facility. It is implemented through the concept of "workspaces." When you sign on you have immediately available a "clear workspace" and a private library of (previously saved) workspaces. Each workspace that is to be separately saved must be uniquely named. This is done by typing

)WSID myws

where "myws" stands for any name (up to 8 letters long) you choose to give your workspace. (WSID means "workspace identifier".)

To save a workspace in your private library you type

) SAVE myws

If you intend to use the contents of a workspace for the next session, you should save your workspace before signing off of the current session. When you save a workspace the contents of any previously saved copy of this workspace are overwritten. To retrieve a workspace from your private library type:

) LOAD myws

Note that your initial workspace name (given in the sign-on instructions) was "libno DTABL". You must rename this workspace before you save it because the APL system will not save it under the original name.

Brief explanations of all messages are listed in the section MESSAGE SUMMARY. If you do not understand a message in your use of DTABL, you should save your workspace before signing off. Perhaps a person in your area who is familiar with APL can help you. If not, you can send your listings to the maintenance address indicated when you execute the function DESCRIBE in your DTABL workspace.

EDITING A DECISION TABLE

You will undoubtably have occasion to change a decision table after it has been entered -- either to correct an error, or to change the desired logic. The editing process that provides a ready means to make changes is initiated by typing:

EDIT tname

This will cause the named table to be loaded and start the editing process. If you want to edit a table that is already loaded, type:

EDIT 0

The editor allows you to insert rows or columns, delete rows or columns, and to change all or part of rows or columns, and to display all or part of the table being edited. You will be prompted with a quad-colon ([:) and you respond with a request as indicated below.

-	EDIT SAMPLE	AINITIATE EDIT OF SAMPLE
	□:	
-	SHOW ALL	ADISPLAY TABLE BEING EDITED
	^^^^^	
	1234567	AWITH ROW AND COLUMN
	vvvvvv	ANUMBERS.
	<1>NYNYNYN C1	
	<2>NNYYYNN C2	
	<3>NNN-YYY C3	
	<1> 1 1 11 A1	
	<2> 1 1 A2	
	<3>12 A3	
	<1> XX X1	
	<2>XXXX X X2	

Initiate Table Editing and Display Table Figure 5.

Requests are made up of a verb which is usually followed by row/column specifications. In Figure 5 the table SAMPLE is loaded for editing. In response to a prompt from the editor a request (SHOW) is made for a display of all rows and columns. Notice that each row and column is given a number

that is printed between angle brackets. Notice also that the stubs are printed to the right of the entries (to speed up printing). These row and column numbers are used in row/column specifications to select the section of the table to be displayed or changed. The row numbers start from 1 in each of the three lateral sections of the table (conditions, actions and entries). Figure 6 shows examples of display requests for sections of the table being edited.

```
[:
                                   C:
      SHOW COL 4 5
                                          SHOW CND 1, ACT 2 3
   ^^
                                       ΛΛΛΛΛΛ
   45
                                       1234567
   vv
                                       VVVVVVV
<1>YN | C1
                                  <1>NYNYNYN C1
<2>YY | C2
                                   <2> 1 1 | A2
<3>-Y | C3
                                   <3>12
                                              |A3
<1>1 | A1
<2>
      A2
<3>
      IA3
\langle 1 \rangle X | X 1
<2>X | X2
```

Example of Selective Display Requests Figure 6.

(Note: Subsequent examples will constitute continued changes on the table EXAMPLE. The examples are taken from a sample terminal session that is listed in its entirety at the end of this manual.)

Alteration verbs are:

```
CHG (change existing table elements)
CHS (change existing stubs)
ADD (add new rows or columns to the table)
DEL (delete existing rows or columns from the table)
REHEAD (change the table header)
```

A row/column specification consists of a prefix followed by one or more numbers. The prefixes are:

```
COL (column numbers follow)
CND (condition row numbers follow)
ACT (action row numbers follow)
EXT (exit row numbers follow)
```

The following are special specifications and need no number.

```
ALLCOL (all columns including the stub column)
ALL (all rows and all columns)
```

CHG ACT 2 3, COL 5 6

asks to change action rows 2 and 3 in columns 5 and 6. You will be prompted with the contents of these elements, one row at a time. The change is made like correcting initial input -- i.e., nonblanks replace, and a slash blanks out those characters under which they are typed. If you want to change a stub, you can refer to it as column zero (0) -plus any appropriate row specification. You replace the contents of a row stub by merely typing new text beneath it. In this case, everything you type to the right of the vertical divider will replace the stub. (See CHS for a way to partially modify a stub.) Note that the row/column specifications must be separated by a comma, and that the numbers must be separated by at least one blank. specification is omitted, it implies "all rows". If a column specification is omitted, it implies "all entry columns". If you want to refer to all columns (in a change -- CHG) including the stub column, use the specification "ALLCOL".

```
SHOW CND 2
   AAAAAA
   1234567
   vvvvvv
<2>NNYYYNN C2
      CHG CND 2, COL 4 7
YN C2
-
_ .
      SHOWCND
                          ATHE ATTN BUTTON WAS PRESSED
                           ATO CORRECT A TYPING ERROR.
             CND 2
   \Lambda\Lambda\Lambda\Lambda\Lambda\Lambda\Lambda
   1234567
   VVVVVV
                      AELEMENTS IN COLS 4,7 REPLACED.
<2>NNY-YN- | C2
```

Example of a Row Change (CHG) Figure 7.

The request

CHS CND 1, ACT 2 3

asks to perform modification of the stubs of condition 1 and actions 2 and 3. You will be prompted with the image of each stub and a right arrow. To the right of the arrow you enter a modification request of the form "/string1/string2". Every instance of "string1" will be replaced by "string2" in the stub. (In the foregoing "/" stands for any character not contained in the arbitrary character sequences "string1"

and "string2".) Multiple pairs of strings can be entered at one time. For example "/A/BB/XX/Y" changes all A to BB and all XX to Y. After each change you are again prompted by the arrow to enter additional changes to the same stub. When the right arrow prompts you, you can also display the results of the previous change by typing one space (blank) followed by a carriage return. A single slash (/) followed by a carriage return will cancel all changes made to the current stub. To move to the next stub you enter an empty line. When all stubs requested have been changed you return to the normal mode and are prompted with a \(\preceil:\).

If your modification request starts with a letter or number it is assumed that you want to entirely replace the stub with the text you enter. If your modification request starts with an asterisk (*) then it is assumed to be of the form "*n/string1/string2". In this case you are asking to replace the nth instance of string1 with string2. "+n" means replace the first n instances of string1, and "-n" means replace the last n instances of string1.

CHS CND 2

ACHANGE STUB ONLY.

|C2

ARROW B (→B) INDICATES SINGLE

|COND2

ABLANK TYPED TO DISPLAY NEW STUB.

ACANCEL CHANGE, AND

|C2

ASHOW RESTORED VALUE.

Example of Stub Modification Figure 8.

The request

ADD COL 3.5 3.6

asks that two columns be added to the table. You will be prompted to fill these columns. The new columns are to be numbered 3.5 and 3.6 respectively indicating their ordering among the old columns. (They will be between column 3 and 4.) When the new columns are added, they do not affect the numbering of any prior columns. A row or column therefore retains a unique number throughout the editing process. When the editing process begins, rows and columns are given consecutive integers starting from 1 in each section of the table. You can insert columns between 3.5 and 3.6 by adding a column numbered with any number that falls between them -say 3.55 or 3.596. Fractional numbers with up to 15 digits to the right of the decimal point can be used in this fashion. However, when you display the table (described below) a maximum of three digits will be printed on each side of the decimal point.

→ ADD CND 2.5 ANEW CONDITION ROW TO BE FILLED.

××××××|×

YYYNNNN C2.5

□:

SHOW CND 1 2 2.5 3 ^^^^^ 1234567

<1.0>NYNYNYN | C1

<2.0>NNYNYNY | C2

<2.5>YYYNNNN C2.5 ANOTE NEW ROW.

<3.0>NNN-YYY | C3

Example of Row Addition Figure 9.

The request

DEL ACT 6

asks for the deletion of action row number 6. If row 6 does not exist, no action will be taken and you will be so informed. If both row and column specifications are included in a request for addition or deletion, the entire row(s) and column(s) will be added or deleted. If both row and column are specified in a change request, only the elements at the intersection(s) of the row(s) and column(s) will be affected.

When you request a change or addition (implying that new material is to be placed in the table) you can specify that the added data be obtained from another part of the table. This is accomplished by using a FROM specification. For example,

ADD COL 4.1, FROM COL 4

requests that a new column (number 4.1) be added to the table, and that it is to be filled with the contents of column 4.

CHG CND 2 3, FROM CND 3 2

requests that condition rows 2 and 3 be changed, and filled with the contents of condition rows 3 and 2. Because such changes take place "simultaneously", the effect is to reverse the order of the entry protion of rows 2 and 3. To specify the reversal of the stubs of these rows, type

CHG CND 2 3, COL 0, FROM CND 3 2, COL 0

and to reverse the entire contents of the rows, type

CHG ALLCOL, CND 2 3, FROM ALLCOL, CND 3 2

Note from the above example, that the order of the specifications is immaterial (i.e., row-column or column-row) but that the ordering of the numbers within a specification is significant. In all cases, the FROM specification should be last.

CHG CND 2, COL 4 7, FROM CND 2 , COL 2 3

L:

SHOW CND 2

1234567

<2>NNYNYNY C2

AELEMENTS COPIED.

CHG request with FROM specification Figure 10.

In the event that you wish to cancel a partially entered request, you can abandon it by typing KILL (preceded by a comma) at the end of the line. For example

CND ACT 2 3, COL 4 5, FROM CND 2, KILL

To rehead a table or otherwise modify the table header, enter the request

REHEAD

You will be prompted with the current header and a right arrow. You can modify the header using the conventions used in CHS (including display or cancellation of the modification). However, if you merely enter a new header it will replace the old one.

To terminate an edit session type

END

This will cause the table to be verified as it is when initial input is completed. You will receive the OK/NG signal as appropriate, and you can save the resulting table via the SAVE order. (The table is in the "loaded" state at conclusion of editing.) Before the table is verified, a check is made to see if there are any x's in the table as a result of editing. If there are, you are not allowed to terminate and will be prompted by the \(\subseteq:\). You can use SHOW to see where the errors are.

Verification of the table takes place at the conclusion of editing and input processing. If the table has any serious errors it will be marked so that the compiler will know to warn you.

The editing process can be aborted at any time you are prompted with the [: by typing

ABORT

If you do this, the results of the edit are lost and there will be no resultant table. You will not lose any table you have retained by using the SAVE function.

Self-explanatory diagnostics will be issued if you attempt to change a non-existent row or column, or if you omitted a prefix (COL, CND, ACT, EXT), or if there is a mismatch between the number of rows and columns supplied in a FROM specification and the space it is to fill. In some cases you will be prompted to supply a correction. If you cannot properly correct the error, you can KILL the request (by entering "KILL") and write a new one. Any time you are prompted with a \(\int_{\text{:}}\); you can KILL a request by typing "KILL" at the end of the line.

COLUMN MERGING

As indicated in the introduction, columns can be merged by the introduction of "don't care" values. You can do this by hand before entering the table, or you can have the DTABL system do it for you by typing

MERGE tname

This will cause the named table to be loaded and the appropriate columns to be merged. If the table is already loaded, type

MERGE 0

To save the table resulting from MERGE, use SAVE. The merging process will not affect any "don't cares" that you have already introduced. There is an example of the MERGE process in the sample terminal session at the end of this manual.

ADDING AN "ELSE" COLUMN

During the verification process, if the table conditions are found to be incomplete, you will be asked if you want to add an ELSE column. You may answer YES or NO. If you answer YES, the condition section of the table is completed automatically by the processor. You will then be asked to supply the action and exit column contents. If you must add an action stub to the table for inclusion in the ELSE column, you should answer NO and use the editor to add these rows first. The editor will then ask you again whether you want the ELSE column added and you can say YES. You will be

asked to enter an exit name for the ELSE column. You can then enter an existing or new exit stub. (Note that the ELSE column actually may be more than one column. This happens when it requires more than one column to represent all of the omitted condition combinations. The same actions and exit will be selected in all of the added columns. There is nothing special about these columns once they are added.)

You can initiate the addition of an ELSE column yourself by typing

ADDELSE tname or ADDELSE 0

depending on whether the named table is already loaded.

COMPILING DECISION TABLES

To compile a decision table, simply type

COMPILE tname

This will cause the table to be loaded and then compiled. If the table is already loaded, use zero (0) instead of the table name.

The compiler, using only entry information, produces a procedural code structure in which the units are condition tests (that always branch on true), actions, and exits. The code is initially in a tree structure with the exits as terminal elements. Because such a tree often contains many redundant code sequences, certain optimization processes are employed to reduce the code to a more efficient form. These optimizations (detailed in [1] and [2]) remove redundant code, reorder condition tests, and relocate actions in order to reduce the size of the resultant code. Unconditional branches may be introduced at this time, but the logic of the original form of the program will remain unchanged.

Optimizations OPT2 and OPT3 operate under the assumptions regarding sequence independence stated earlier. (Optimization options are listed on page 20.) You are therefore cautioned to assure that actions in a table do not modify condition test results. Program loops should be avoided unless they encompass the entire decision table, or are entirely contained within a single action.

The code resulting from compilation will be optimized primarily with respect to space. There will be a one-to-one correspondence between the number of rule columns in the source table and the number of flow paths through the object code. No path will contain more tests than necessary to isolate the particular rule that it represents. Each path

will contain the required actions in the specified order, and will terminate in the specified exit. Speed optimization is limited to the avoidance of redundant testing in a given flow path. Because the compiler does not analyze the meaning of the stub information, no attempt is made to perform classical optimizations such as common subexpression elimination or folding. Such a task is beyond the intent of this compiler.

In the absence of other information, the optimizer assumes that all actions and conditon tests take the same amount of space (8 bytes). You can override this assumption by including a space estimate in the stub section of the table. The size in bytes should follow the action and condition information (entry section) and be enclosed in "C D" brackets.

If the compiler detects an unverified table it will warn you but give you the option to proceed with a compilation. The results of such a compilation will not be reliable. In some cases the compiler will abort the compilation because of overlapping or missing rule columns.

Several types of output listings can be produced by the compiler — "abstract", COBOL, PL/I, ALGOL, and APL. The non-abstract outputs are collectively called "HLL" (for Higher Level Language). Examples of these can be found in the terminal session example at the end of this manual. (If you are able to program in APL you can, with the aid of [2], readily develop your own HLL listing generator for the language of your choice.)

The abstract format is analogous to an assembly listing with a label column, code column, and operand column. The op-codes are references to the conditions, actions, and exits of the original table. "Cnn" refers to condition number "nn". "Ann" and "Xnn" similarly refer to actions and exits respectively. All labels are two-digit numbers, and appear in column 1 followed by a colon. Condition tests (which are conditional branches) and unconditional branches (three dashes) have a right arrow followed by a label which is the label of the branch-target.

If the stub section of the decision table is properly formatted, you can obtain a listing of a valid HLL procedure. The condition section should contain relational expressions; the action section should contain assignment statements; and exits should be valid HLL program labels. The HLL lister merely places the stub information (excluding space estimates) verbatim into the appropriate HLL template. The decision table header should also be a valid HLL label, both of itself, and when suffixed by a two-digit number. (Note that stubs can be in English or other natural language suitable to the user. This is usually the case when the questionnaire processor will be used. The language selected

does not affect compilation.)

There are a number of compiler options. Prior to compilation they sould be set as follows:

OPTIONS-option-list

The options in the list can be placed in any order and are separated by commas. The options selected will remain in force until they are explicitly set again. When a workspace is saved the options in force at the time it is saved are retained in the saved workspace. They will be in force again when you reload the workspace. Options can be set at any time between compilations. The list of options is below.

List object code in ALGOL format
List object code in APL format
Generate APL program
List object code in COBOL format
List object code in PL/I format
List object code in abstract format
List preoptimized code (abstract format)
Place output on a file
Perform optimization 1 (duplicate sequence removal)
Perform optimization 2 (duplicate path removal)
Perform optimization 3 (hoisting)
Perform all optimizations
Report details of compilation progress

The option list in the distributed version of DTABL is

OPTM

If an option is omitted from the list, the corresponding action will not be performed.

If listings are requested the processor will print a row of periods, pause, and unlock the keyboard. You can then position the paper and signal when you are ready for printing by giving a carriage return.

The FILE option can be specified in conjunction with the ALGOL, COBOL and PL1 listing options. When this is done, the listing is placed in a file instead of appearing on the terminal. The file has the same name as the table header. The various APL systems use different qualification conventions to separate user files. There are also varying protocols for establishing a connection between the APL and operating system environments. While these protocols are simple, you must consult the APL User's Guide [4_8] relevant to your system for details.

An example of the compilation of the table in Figure 1 is shown in the terminal session example at the end of this manual.

DISPLAYING AND SAVING COMPILATION RESULTS

The results of compilation are held in an encoded matrix called MAT. This can be saved and loaded in a manner analogous to the saving and loading of decision tables. By typing

mname - SAVEMAT

you will cause the object matrix to be compressed, saved, and named. To reload it, type

LOADMAT mname

Note that saving a matrix with SAVEMAT does not save it across terminal sessions. You use) SAVE myws to do that.

To display object code in HLL format, set the options list for the desired language and type:

tname PRINTHLL mname

To print object code in abstract format, type

ABSTRACT mname

Note that these requirements load the code matrix, and PRINTHLL also loads the decision table. To request printing of a loaded matrix or table you can use zero (0) instead of the name. The code matrix is loaded and unnamed just following compilation.

After these listing routines are invoked, they pause to allow you to position the paper. You signal that you are ready by giving a carriage return. The listing routines print approximately 50 lines per page.

SORTING A DECISION TABLE

It has been discovered that by sorting a decision table according to certain "rules of dominance", one can gain a better understanding of the relationships among the conditions. DTABL therefore provides a utility sort function. One condition row is said to "dominate" a second row if for all of the "yes columns" of the first row, the second row has all "don't care" values; or for all of the "no columns"

of the first row the same is true.

The sort routine first sorts rows in order of increasing number of "don't cares". Then it sorts the rows so that they are in disjoint "groups". (Two rows are in the same group if they both have a non-don't care (i.e., "Y" or "N") in the same column.) Finally, the rows are sorted so that if one row dominates another, it lies above the other in the final arrangement. After the rows are sorted, the columns are sorted as though each condition entry was a digit in a base-3 number system ("Y"=0, "N"=1, and "-"=2, and as though each column was a number. The numbers so devised are sorted in ascending sequence. Figure 11 below shows a decision table before and after sorting.

To load a decision table and sort it, type:

DSORT tname

As usual, a zero (0) instead of the table name will cause sorting of the table that is already loaded. DSORT leaves the sorted table in loaded form.

SORT	000000000111 123456789012	SORT	000000000111 123456789012
C01	YYNNNNNNNNN	C01	YYNNNNNNNNN
C02	YYYYYYYYNN	C06	YNYYYYYNNNNN
C03	YYYYYYNN	C02	YYYYNYYYYN
C04	YYNNNN	C03	YYYN-YYYN-
C05	YYNN	C04	YNNYNN
C06	YNYNYNYNYN	C05	YNYN
A01	1111	A01	11 11
A02	1111	A02	1 11 11
A03	1111	A03	11 11
A04	11	A04	1 1
A05	11111111111	A05	1111111111111
x01	x x x x x x	X01	x xxxxx
X02	x x x x x x	X02	X XXXXX
	before		after

Example of Decision Table Sorting Figure 11.

TEST CASE GENERATION

In the object code generated from a decision table there is one path for each rule column in the source table. If one test case is prepared for each rule column then every instruction in the object code will be executed, and every conditional branch instruction will be executed for both the true (yes) and false (no) values of the corresponding condition. Because of the action of the optimizers it is often the case that both of the above test criteria can be met with fewer test cases than there are rule columns. This is because the optimizers overlap paths through the object code. The extent to which testing can be reduced depends upon the extent to which the paths overlap. To determine the minimum set of test cases enter:

tname TESTGEN mname

and you will be supplied with a list of rule column numbers which constitute the minimum coverage set. (As usual you can use zero (0) for tname and mname if the corresponding table or code matrix is already loaded.)

QUESTIONNAIRE PROCESSING

It is possible to use a decision table as the basis for an on-line questionnaire. Each condition stub in the table constitutes a question to be asked which can be answered "yes" or "no" by a user who is sitting at the terminal. Each answer determines which question should be asked next, or whether no further questions should be asked. When a sufficient number of answers are given to enable the isolation of a single rule column, the actions and exits selected by that column constitute a list of responses to the user. DTABL contains a questionnaire processor that operates in this fashion when the user enters:

Q tname

where tname can be zero (0) for a table that is already loaded. The questionnaire processor appends question marks to the condition stubs before presenting them to the user. The answers entered in response to the questions can be any word containing at least one "Y" (meaning "yes") or one "N" (meaning "no") but not both. For example OKAY, YES, YEP, Y and YEAH would be interpreted as "yes". NEIN, NOPE, N, NAY, or NO would mean "no". JA, which has neither a "Y" or "N", and NYET which has both would be rejected as ambiguous.

PROCESSOR CAPACITY

There are few programmed capacity restrictions in DTABL. You are limited to 255 conditions and a total of 255 actions and exits. Furthermore, this restriction applies only if you want to use SAVEMAT (described above).

There are, however, restrictions imposed by the APL environ-

ment under which DTABL is implemented. These restrictions are in input and output line widths, and in storage capacity. Experience to date shows that in practice these limitations are seldom encountered.

The maximum stub width is 130 characters. If your table width (including entries and stubs) exceeds this, table displays will deteriorate -- i.e., a single row will be placed on two or more lines. In any event neither half can exceed the terminal width because an end-of-line cuts off input.

The HLL and abstract printing routines will print only the two low-order digits of a numeric label, although the restriction on label values in MAT is over 16 million.

The size of the workspace can affect the size of a table that can be compiled. DTABL has handled tables with as many as ten conditions and 35 rule columns without spilling storage in a 50K workspace.

You can reduce the chances of a storage spill by erasing those tables or matrices which you have saved but no longer need. This can be done by typing:

) ERASE name1 name2 name3 etc.

The list of names (separated by blanks) are those named items you wish to discard.

You can LOAD a table and) ERASE the compact (unloaded) version prior to operating on it.

You can establish a workspace solely for storing tables (or object codes) and using the) COPY facility of APL to transfer your tables (codes) to it. After saving your compiler workspace under the name of your choice, perform the following sequence:

) CLEAR) WSID mydata) COPY myws name1 name2 ...

) SAVE mydata

Repeat the \()COPY line as many times as needed to save the desired tables (codes). You can then reload your compiler workspace and erase the copied tables (codes) from it. (Don't forget that your library copy of the compiler work-space still contains the old version of tables (codes) until you \()SAVE the updated version.\)

There are three kinds of overflow messages you can get. They are WS FULL, SYMBOL TABLE FULL and STACK FULL. APL maintains a symbol table of all the names you create (as well as those that are used in DTABL). Unfortunately, when

you) ERASE a named item, its name is not removed from the symbol table. Your recourse will be to perform the sequence below if you received the SYMBOL TABLE FULL diagnostic.

) ERASE unneeded tables and object codes
) SAVE myws
) CLEAR
) SYMBOL 500 (= bigger than the current setting)
) COPY myws
) WSID myws
) SAVE myws

You can then restart the interrupted activity.

If you receive a WS FULL message try erasing unneeded decision tables then perform the above sequence without the ")SYMBOL" line. Then retry the operation that was interrupted.

If you receive a STACK FULL message try the above sequence with STACK in place of SYMBOL. You can find out the current size limitations by typing)SYMBOLor)STACK without a number following. The amount of free workspace is obtained by typing [WA. (Note that space given to the stack or symbol table is taken from the free workspace.)

Another way to obtain more space is to separate certain DTABL functions into several different workspaces. To assist you in doing this the DTABL workspace has been divided into six groups as follows:

COMPILER	all functions used by COMPILE (includes
	listing functions)
EDITOR	all functions used by INPUT, EDIT and
	ADDELSE
MISC	all functions used by MERGE, DSORT,
	TESTGEN, and Q
NCOMPILER	functions not used by COMPILE
NEDITOR	functions not used by INPUT, EDIT and
	ADDELSE
NMISC	functions not used by MERGE, DSORT,
	TESTGEN, and O

Each of the first three groups can be used to copy relevant functions from DTABL into a clear workspace. The last three can be used to delete unneeded code from the workspace you are working with. For example, if you get a WS FULL during compilation, you can erase group NCOMPILER and continue.

If the WS FULL condition persists your needs have exceeded the capacity of the APL system you are running under. The remedies available are (1) to increase your virtual machine or region size, or (2) to move your workspaces to an APL system that accomodates larger workspace sizes. See your APL system administrator for assistance.

KEYWORD SUMMARY

Page	Keyword	Reminder
3		Sign on
4)LOAD libno DTABL	Initialize workspace
4	OFF	Sign off
4	INPUT	Initial table entry
4	IS3270←(0/1)	Terminal mode
9	→STOPCHECK	Interrupt checking
9	tname+SAVE	Save decision table
10	LOAD tname	Load decision table
10	SPREAD tname	Display table with verticals
1000000)WSID myws	Give workspace a name (myws)
) SAVE myws	Save workspace in library
1000000)LOAD myws	Load workspace from library
	EDIT tname (0)	Edit decision table
	SHOW spec	Show partially edited table
	CND rrrr	Specify condition rows
	ACT rrrr	Specify action rows
12	EXT r r r r	Specify exit rows
12	COL c c c c	Specify columns
12	ALLCOL	Specify all columns & stubs
12	ALL	Spec. all rows, cols & stubs
13	CHG spec	Request change as specified
13	CHS spec	Change stub
14	ADD spec	Request row/column addition
15	DEL spec	Request row/column deletion
15	FROM spec	Specify data source
	KILL	Cancel edit command
	REHEAD	Request table header change
4500	END	Terminate editing
1000	ABORT	Cancel editing
	MERGE tname (0)	Merge table columns
	ADDELSE tname (0)	Add an ELSE column to table
18	COMPILE tname (0)	Compile a table
20	OPTIONS-	Set compiler options
	ALGOL Algol format	FILE Place output in a file
	APL APL format	OPT1 Redundant Sequence
	APLG APL (executable	
	COBOL COBOL format	OPT3 Hoisting
		OPTM OPT1, OPT2, OPT3
		DETAIL Report progress
24	ILIST Preoptimization	
21	mname+SAVEMAT	Save results of compilation
21 21	LOADMAT mname (0)	Load object code matrix ne(0) Print in High Lev. Lang.
21		" - 1000년에 1000년에 1000년에 100년에 1
22		Print code, abstract format Sort decision table
23	the control of the co	e(0) Generate test columns
23	Q tname (0)	Ouestionnaire processor
20	2	Agogarametre bracegon.

MESSAGE SUMMARY

Error Messages

STUB TRUNCATED TO 130 CHARACTERS

Table stub exceeded 130 characters,

and was truncated to 130.

EMPTY STUB Stub contains only space data.

NO ACTION You tried to delete nonexistant

rows or columns, or to add rows or columns that were already in the table. Or you tried to add an ELSE

column to a complete table.

BAD INPUT Incorrect input, "x" replaces the

bad data shown with this message.

NOT EDITING You tried to use the edit commands

without entering the EDIT function.

IN EDIT. NO ACTION You attempted to perform a non-edit

function while editing.

VALUE ERROR n n is a list of invalid row/column

numbers.

MISSING PREFIX COL, CND, ACT, OR EXT

One or more of the listed prefixes

was omited.

STUB MISMATCH The FROM specification does not

match the CHG specification with

respect to column 0.

ROW LENGTH ERROR The FROM specification does not

match the CHG specification with

respect to the number or rows.

COLUMN LENGTH ERROR The FROM specification does not

match the CHG specification with

respect to the number of columns.

ERRORS There is an empty condition, action

or exit section in the table being

edited.

YES-NO IMBALANCE

CDN, YCT, NCT, DIF There are not an equal number of

Y's and N's in some condition rows. The counts and differences are

given for the imbalanced rows.

MISSING RULE: ... The indicated rule column is not accounted for in the table.

RULE OVERLAP: n "n" is a list of columns which have overlapping condition values.

NO RULE USES ACTION(S) n "n" is a list of actions not used by any rule.

NO RULE USES EXIT (S) n "n" is a list of the numbers of exits not used by any rule.

MISSING OR MULTIPLE EXITS IN RULE(S) n

"n" is a list of rule columns without exactly 1 exit.

NG The table being entered or edited is in error.

tablename IS IN ERROR, DO YOU WISH TO PROCEED?

The compiler has detected an invalid table. You respond YES or NC.

PLEASE ENTER YES OR NO You have given an ambiguous response to a YES/NO questions. Your response must contain either a Y or an N, at least one, not both.

RULE n OVERLAP. QUESTIONING TERMINATED RULE n OVERLAP. COMPILATION ABANDONED

"n" is a list of the rule columns that have been found by the compiler or questionaire processor to overlap. Occurs with an invalid table.

RULE MISSING. QUESTIONING TERMINATED RULE MISSING. COMPILIATION ABANDONED

Some rule column was discovered missing by the compiler or the questionnaire processor. Occurs with an invalid table.

ILLEGAL TABLE You attempted to LOAD (possibly indirectly) an object that was not a decision table.

ILLEGAL CBJECT CODE You attempted to LOADMAT (possibly indirectly) an object that was not a code matrix.

EMPTY TABLE There is no table loaded (or saved).

EMPTY OBJECT CODE

There is no code matrix loaded (or saved).

INCOMPATIBLE TABLE/CODE You have tried to use PRINTHLL or TESTGEN with an object code that does not match the decision table.

DEFINITION ERROR:

[n] line image

You used the APLG option to produce an APL object program, but it could not be defined because of an error in the line image displayed. the line image is line 0, the error may be due to the existence in your workspace of an APL variable or group with the same name as your program. You will also get this message if the function you are trying to define is "pendant" in your workspace.

SYNTAX ERROR

You have entered a command improperly. Look for an omitted comma or blank or a mispelled keyword.

VALUE ERROR

You have mispelled something, or attempted to save a table that doesn't exist.

During an attempt to file output you may encounter one of the error messages listed below. The error number (n) accompanying the message is particular to the operating system you are using. Your APL administrator can direct you in seeking help if you cannot discover the cause of this error.

OPEN FAILURE: n

Probable cause is that your table header is not a legal file name. It is also possible that the APL shared variable interface is (temporarily) unavailable (output will be cancelled)

WRITE FAILURE: n

Probable cause is a line that is too long for the file being used.

INTERFACE QUOTA EXHAUSTED

You don't have enough interface capacity for your file. See your APL administrator to increase your quota.

NO SHARES. SVP INACTIVE You didn't include APL110 or APL121 when you accessed VSAPL.

SV SPACE QUOTA EXCEEDED Too little space for Shared Varia-

ble processor. Get more when you activate APL by entering VSAPL 8K

APL110.

WS FULL Not enough free workspace to

continue calculations. See the discussion under PROCESSOR CAPACI-

TY.

SYMEOL TABLE FULL Not enough symbol table capacity.

See discussion under PROCESSOR

CAPACITY.

STACK FULL Too many recursions for the execu-

tion stack to handle. See the discussion under PROCESSOR CAPACI-

TY.

xxxx[n] You have stopped in function "xxxx"

at line "n". This was caused by an interrupt (sometimes due to line noise). To resume execution enter

"-n".

Prompting and Other Messages - - - - - - - - -

ENTER TABLE HEADER Enter the header of the decision

table.

ENTER CONDITION STUBS Enter at least one condition stub.

ENTER ACTION STUBS Enter at least one action stub.

ENTER EXIT STUBS Enter at least one exit stub.

HOW MANY RULES ARE THERE?

Enter the number of rule columns

for this table.

STANDARD CONDITIONS (n RULES)

You have requested "standard" conditions. There are "n" rule

columns.

INPUT CANCELLED You have cancelled the input

process.

DO YOU WANT TO ADD AN ELSE COLUMN?

Your table has missing rules, do you want them added? Answer YES or

NO.

nnn:ACTIONS "nnn" is the action row numbers for

the ELSE column you are adding.

ENTER EXIT NAME Enter the exit stub for the ELSE

column you are adding.

HEADER IS header You have requested REHEAD. "head-

er" is the current header expres-

sion of the table you are editing.

OK The decision table has been veri-

fied following INPUT or EDIT.

COMPILING tablename Compilation has started.

n LINES COMPILED IN s CPU SECONDS

Compilation is completed.

· · · · · · · You are prompted to position the

paper on your terminal to receive a program listing. Enter a carriage

return to start printing.

FILING to tablename Signals start of filing output

END OF FILED OUTPUT Signals filed output complete

The following messages are printed during compilation when you have selected the DETAIL option.

n LINES GENERATED FROM tablename (prior to opts. 1 and 2)

LOCATE DUPLICATE SEQUENCES IN n LINES (opt 1)

n SEQUENCES TO BE EXAMINED

REMOVING REDUNDANT GOTOS FROM n LINES

REMOVING REDUNDANT GOIOS FROM n LINES

REMOVING REDUNDANT EXITS FROM n LINES

REMOVING DEAD CODE FROM n LINES

FURTHER SEQUENCE REDUCTION IS POSSIBLE

NO FURTHER SEQUENCE REDUCTION IS POSSIBLE

THERE ARE n LINES OF CODE LEFT (opt1)

ATTEMPTING DUPLICATE PATH REMOVAL (opt2)

TABLE OF RESERVED NAMES

ABORT	CHS	DETAIL	INPUT	NMISC	REHEAD
ABSTRACT	CND	DSORT	KILL	OPTIONS	SAMPLE
ACT	COBOL	EDIT	LOAD	OPTM	SAVE
ADD	COIBM	EDITOR	LOADMAT	OPT1	SAVEMAT
ADDELSE	COL	END	MAT	OPT2	SHOW
ALGOL	COMPILE	EXT	MERGE	OPT3	SLIST
APL	COMPILER	FILE	MISC	PL1	SPREAD
APLG	DEL	FROM	NCOMPILER	PRINTHL	STOPCHECK
CHG	DESCRIBE	ILIST	NEDITOR	Q	TESTGEN

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- 4. APL Shared Variables (APLSV) User's Guide. SH20-1460 or SH20-9087
- 5. APL/CMS User's Guide. SH20-1846
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- 7. VS APL for VSPC: Terminal User's Guide. SH20-9066
- VS APL for TSO: Yale University Terminal User's Guide. SH20-1872

EXAMPLE TERMINAL SESSION

```
)LOAD 27 DTABL ALOAD DTABL WORKSPACE
  SAVED 14:56:37 08/17/76
        INPUT
  ENTER TABLE HEADER
                        AARROW (→) INDICATES LINES
 EXAMPLE
                         ATYPED BY THE USER. ALL OTHER
  ENTER CONDITION STUBS ALINES ARE OUTPUT FROM THE
                        ASYSTEM. (THE ARROWS ARE NOT
  C1
                         ANORMALLY PART OF THE LISTING.)
  C2
  C3
  ENTER ACTION STUBS
 A1
  A2
  A3
                        ADOUBLE ARROWS (---) INDICATE A
                        ACARRIAGE RETURN PERFORMED BY
  ENTER EXIT STUBS
                         ATHE USER.
→ X1
→ X2
  HOW MANY RULES ARE THERE?
  7
  0000000
  1234567 C1
→ NYNYNYN
  1234567 C2
→ NN YYNN
                        ABLANK IS NOT A LEGAL CONDITION.
  BAD INPUT
   NN×YYNN C2
                        ACORRECT ONLY THE BAD COLUMN.
   Y
  1234567 C3
 NNN-YYY
  12345+7 A1
  1 1 11
   1234567 42
   1 1
   1234567 A3
  12
   1234567 X1
      XX
   1234567 X2
  XXXX
                         ATHE TABLE IS CONSISTENT.
   OK
```

SAMPLE-SAVE

ATABLE SAVED AND NAMED.

```
SAMPLE
                        ADISPLAY TABLE NAMED SAMPLE.
EXAMPLE | 0000000
      1234567
        NYNYNYN
C1
C2
       NNYYYNN
C3
       NNN-YYY
       1 1 1 11
A1
       1 1
A2
A3
       112
      l xx
X1
       XXXX X
X2
      EDIT SAMPLE
                         AINITIATE EDIT OF SAMPLE
[:
      SHOW ALL
                         ADISPLAY TABLE BEING EDITED
   ^^^^^
                         AWITH ROW AND COLUMN
   1234567
   VVVVVV
                         ANUMBERS.
<1>NYNYNYN|C1
<2>NNYYYNN C2
<3>NNN-YYY | C3
<1> 1 1 11 | A1
<2> 1 1 | A2 
<3>12 | A3
\langle 1 \rangle  \chi \chi | \chi 1
<2>XXXX X X X2
[:
      SHOW CND 1, ACT 2 3
   ^^^^^
   1234567
   VVVVVV
                        ASELECTIVE DISPLAYS
<1>NYNYNYN|C1
<2> 1 1 | A2
         A3
<3>12
C:
     SHOW COL 4 5
   ^^
   45
   VV
<1>YN | C1
<2>YY | C2
<3>-Y | C3
<1>1 | A1
<2> | A2
<3> | A3
\langle 1 \rangle X | X1
<2>X | X2
```

```
[:
        SHOW CND 2
     ^^^^^
      1234567
     VVVVVV
   <2>NNYYYNN C2
        CHG CND 2, COL 4 7
  YN C2
  C:
        SHOWCND
                          ATHE ATTN BUTTON WAS PRESSED
                          ATO CORRECT A TYPING ERROR.
              CND 2
     AAAAAA
      1234567
      VVVVVVV
  <2>NNY-YN-|C2
                         AELEMENTS IN COLS 4,7 REPLACED.
  \Box:
        CHG CND 2, COL 4 7, FROM CND 2 , COL 2 3
  [:
        SHOW CND 2
     AAAAAAA
      1234567
      VVVVVV
  <2>NNYNYNY | C2
                         AELEMENTS COPIED.
        CHS CND 2
                          ACHANGE STUB ONLY.
   C2
  →/C/COND/
⇒B →
                          AARROW B (→B) INDICATES SINGLE
   COND 2
                          ABLANK TYPED TO DISPLAY NEW STUB.
  -/
                          ACANCEL CHANGE, AND
   C2
                          ASHOW RESTORED VALUE.
→ →
  C:
                          ANEW CONDITION ROW TO BE FILLED.
        ADD CND 2.5
   xxxxxxx x
  YYYNNNN C2.5
  L:
         SHOW CND 1 2 2.5 3
        ^^^^^
        1234567
        VVVVVV
  <1.0>NYNYNYN|C1
   <2.0>NNYNYNY | C2
  <2.5>YYYNNNN | C2.5
                         ANOTE NEW ROW.
   <3.0>NNN-YYY | C3
  [:
        DEL CND 2.5
                         ADELETE ROW JUST ADDED.
  [:
        ADD COL 4.5 4.6, FROM COL 1 1
```

```
[:
      SHOW ALL
   ^^^^^^
   123444567
   . . . . . . . .
   000056000
                       ANOTE THAT CONDITION ROW 2.5
   VVVVVVVV
                       RHAS BEEN DELETED, AND COLUMNS
<1>NYNYNNNYN C1
                       A4.5 AND 4.6 HAVE BEEN ADDED.
<2>NNYNNNYNY | C2
<3>NNN-NNYYY C3
<1> 1 1 11 | A1
<2> 1
          1 | 42
<3>12 11 | A3
       xx \mid x1
<1>
<2>XXXXXX X | X2
      DEL COL 3.5
NO ACTION
                        ACOLUMN 3.5 DOESN'T EXIST
[:
      DEL COL 4.5 4.6
: :
      ADD COL 2
NO ACTION
                        ACOLUMN 2 ALREADY EXISTS.
[:
      CHG CND 1 2, ALLCOL, FROM CND 2 1, ALLCOL
[:
      SHOW ALL
  ^^^^^
   1234567
   VVVVVV
<1>NNYNYNY|C2
                       ANOTE THAT CONDITION ROWS 1
\langle 2 \rangle NYNYNYN | C1
                       AAND 2 ARE REVERSED.
<3>NNN-YYY|C3
<1> 1 1 11 A1
<2> 1 1 | A2
         |A3
<3>12
      XX \mid X1
<1>
<2>XXXX X X X 2
      CHG CND 1 2, COL 4 7
NY C2
YN
YN C1
YN
[:
      END
                       ATABLE IS CONSISTENT .
OK
```

ASAMPLE USE OF MERGE

	MER	JEXA	MI	PLI	E											
MERGE	0000	0000	00	01	11	11	11	1	11	22	22	22	22	222	2	333
	1234	456	789	90	12	34	56	7	89	0 1	12	34	56	578	9	012
C1	YYY	VVV	, v	vv	vv	v	v	7 N	אא	N A	IN	NI NI	NI 2	VAIA	N	אנגע
	YYY															
	YYY									27.5						
C4 C5	YYN															
			-					-			-				-	
	111		11	11	11	11	11	1	11	11	1.1	11	1:	111	1	111
	111															
A3	111															
A4	l	11:										11				
A5	111	111	11	11	11	11	111	11	11	1	11	11	1	111	. 1	111
	1	1		1		1		1		1	Ĺ		1		1	
A7	11	11		11		11	L	1	1	1	11		1	1	1	1
X1	1 x	x	XX		хx		XX	ĸ	x	X		xx		XX		XX
X2	XX	XX	-	XX		Xλ	(X	X	2	XX		X	X	X	X
	MER	ĠE I	ME.	RG	EX	AB	4PI	LE								
	SAV	E														
MERGE	1000	000	00	01	11	11	11									
	123					-										
			YY	y-		NN	NN									
	YYY															
C1 C2	YYY	YYY	NN.	NN	NN											
C1 C2 C3	YYY	YYY	NN YY	NN YN	NN NN		 YY									
C1 C2 C3 C4	YYY YYY YYY	YYY NNN YYN	NN YY YY	NN YN NY	NN NN YN	Y Y Y	YY YN									
C1 C2 C3	YYY	YYY NNN YYN	NN YY YY	NN YN NY	NN NN YN	Y Y Y	YY YN									
C1 C2 C3 C4 C5	YYY YYY YYY YYN YN-	YYYY NNN YYN YN-	YY YY YY	NN YN NY -Y	NN NN YN N-	YYY	YY YN V-									
C1 C2 C3 C4 C5	YYY YYY YYY YYN YN-	YYYY NNN YYN YN-	YY YY YY	NN YN NY -Y	NN NN YN N-	YYY	YY YN V-									
C1 C2 C3 C4 C5 A1 A2 A3	YYY YYY YYY YYN YN-	YYY NNN YYN YN- 111	NN YY YY YN	NN YN NY -Y -11	NN NN YN N-	YYY	YY YN V-									
C1 C2 C3 C4 C5 A1 A2 A3 A4	YYY- YYY YYY YYN- 111 111	YYYN NNN YYN YN- 111	NN YY YY YN 11	NN YN NY -Y 11	NN NN YN N- 11	Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	YY YN V- 11									
C1 C2 C3 C4 C5 	YYY YYY YYN YN- 111 111 111 111	YYYN NNN YYN 111 111	NN YY YY YN 11	NN YN NY -Y 11	NN NN YN N- 	YYYY Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	YY YN V- 11									
C1 C2 C3 C4 C5 	YYY YYY YYY YYN YN-	YYYN NNN YYN YN- 111 111	NN YY YY YN 11 11	NN YN NY -Y -11 11	NN NN YN N- 11	Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	YY YN V- 11									
C1 C2 C3 C4 C5 	YYY YYY YYN YN- 111 111 111 111	YYYN NNN YYN YN- 111 111	NN YY YY YN 11 11	NN YN NY -Y -11 11	NN NN YN N- 11	Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	YY YN V- 11									
C1 C2 C3 C4 C5 	YYY YYY YYN YN -	YYYN NNN YYN YN- 111 111	NN YY YY YN 11 11 11	NN YN NY -Y 11 1 11 1	NN NN YN N- 11	Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	YY YN V- 11									

ASAMPLE COMPILATION.

```
SAMPLE
EXAMPLE | 0000000
     1234567
C1
    NYNYNYN
C2
      NNYYYNN
      NNN-YYY
C3
-----
A1
      1 1 11
       1 1 1
A2
A3 | 12
      XX
X1
X2
      XXXX X
      OPTIONS-OPTM, COBOL, PL1, APL, SLIST
      COMPILE SAMPLE
COMPILING EXAMPLE
14 LINES COMPILED IN 2 CPU SECONDS
. . . . . . . . . .
EXAMPLE.
           IF C1 THEN GO TO EXAMPLE 03.
           IF C3 THEN GO TO EXAMPLE04.
           IF C2 THEN GO TO EXAMPLE05.
EXAMPLEG1. A3.
           GO TO X2.
EXAMPLE03. A1.
           IF C2 THEN GO TO X2.
           IF C3 THEN GO TO X1.
           GO TO EXAMPLEO1.
EXAMPLE04. IF C2 THEN GO TO X1.
           A1.
EXAMPLE05. A2.
EXAMPLE06. GO TO X2.
           COMMENT END OF EXAMPLE.
. . . . . . . . .
```

```
EXAMPLE: BEGIN;
            IF C1 THEN GO TO EXAMPLE 03;
            IF C3 THEN GO TO FXAMPLE04;
            IF C2 THEN GO TO EXAMPLE05;
 EXAMPLE01: A3;
            GO TO X2;
 EXAMPLE03: A1:
            IF C2 THEN GO TO X2;
            IF C3 THEN GO TO X1;
            GO TO EXAMPLEO1;
EXAMPLE04: IF C2 THEN GO TO X1:
            A1;
EXAMPLE05: A2;
EXAMPLE06: GO TO X2;
          END /* OF EXAMPLE */;
 . . . . . . . . . .
    EXAMPLE
[1] →(C1)/6
[2]
   →(C3)/10
[3]
    →(C2)/12
[4]
    A3
[5]
    →0
[6]
    A1
[7]
    →(C2)/0
[8] →(C3)/0
[9] →4
[10] →(C2)/0
[11] A1
[12] A2
 . . . . . . . . .
EXAMPLE
000| C01→03
 001
       C03→04
 002
      C02→05
 003 01:A03
 004| X02
 005 02:X01
 006 03:A01
 007| C02→06
 008
      C03→02
 009| ----01
 010|04:C02-02
 011 A01
 012 05:402
 013 | 06:X02
```



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APL Decision Table Processor (DTABL)
Program Description/Operations Manual
SH20-1924-1

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8 AMPLE. DET 748-65 from GLAWS

DTABL

DECISION TABLE PROCESSOR

IUP 5796-PJB

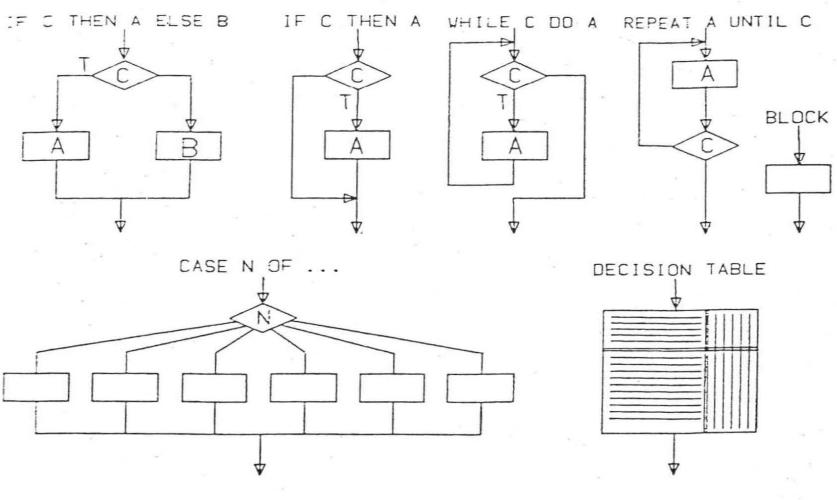
THE DECISION TABLE IS A TOOL FOR

- PROGRAMMING AND
- COMMUNICATION

- · LOGIC, NOT FLOW
- · SIMPLE TO UNDERSTAND
- OCCUMENTATION
- INDUCES CHECKS ON LOGIC CONSISTENCY COMPLETENESS

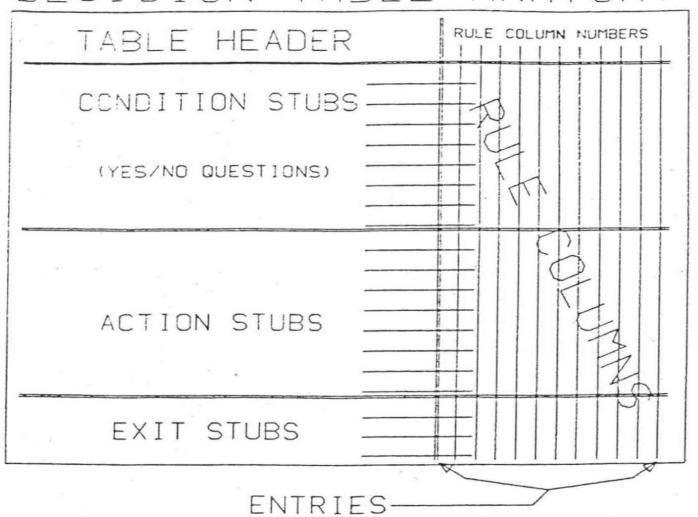
RESULTING PROGRAMS ARE

- · QUICKER TO WRITE
- · MORE RELIABLE
- · EASIER TO MAINTAIN
- · LESS COSTLY TO HAVE



BLOCK STRUCTURES

DECISION TABLE ANATOMY



D190F3	100000000011111
	Y Y Y Y Y Y Y Y Y Y Y N
BSP TWICE SET CCW1 TO BYPASS TM HDR BSF	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
D190G1 ABEND D190F4	XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX

* /

i j

. .

COLUMN MERGING

1. ALL ELEMENTS SAME EXCEPT 1 CONDITION 2. VALUES IN CONDITION ARE YES AND NO

TWO EXAMPLES

C1 I Y Y Y	C 1	1	N	N	N
C2 1, Y Y Y	C2	1	Y	Y	Y
C3 X N -	C3	1	-	_	-
	C4	1	Y	N	-
A 1					-
A2 1	A 1	1	1	1	1
A3 1	A2	1			
	A3		1	1	1
$\times 1$ I					-
X2 X X X	$\times 1$	1	X	X	X

LOGIC_VALIDATION

N CONDITIONS REQUIRE N 2 UNIQUE RULE COLUMNS

AMBIGUOUS	REDUNDANT	INCOMPLETE
C1 IYY. C2 INN. C3 IYY.	C2 INN.	C1 IYNN C2 I-YN C3 IY
A1 I1. A2 I 1.		A1 11 1 A2 2 1
X1 1XX.	X1 I. XX.	X1 IXXX

DIABL PROVIDES

- · A TABLE EDITOR
- · LOGIC VALIDATOR
- OPTIMIZING PRE-COMPILER (APL, COBOL, PL/I)
- · TEST CASE ADVISOR
- TABLE MANIPULATION COLUMN MERGING TABLE SORTING
- · QUESTIONNAIRE PROCESSOR

IABLE EDITING

INSERTION DELETION MODIFICATION

BY

ROWS COLUMNS ELEMENTS

```
OPTIONS-OPTM.PLI
     COMPILE D-D190F3
D190F3
                          1000000000011111
FRROR
TH ERROR DXCCV4
RDBCK PARAM
                          TYYYYYNNNNNNN - -
MOD JECBIND2
1/0 ERROR
                          - - NYMNNYNNNYN - -
RDBCK PARAM AGAIN
                          I - YNNN - YNNN - - - -
MOD JFCB[ND3
                          1 - - YNN - - YNN - - - -
I/O ERROR FOR BSF
                          1---YN---YN----
   TO BSF
SET
BSP TWICE
SET CCVI TO BYPASS TM HDRI 11 11
BSF
                            1111
                                 111111
                          11 11
SET UP ABEND 613
D190G1
                          I XX X XX X XX
ABEND
                          IX X X X X
D190F4
COMPILING D190F3
 20 LINES COMPILED IN 1 CPU SECOND
D190F3: BEGIN:
          IF ERROR THEN GO TO D190F301;
             TO D190F4;
             TM ERROR DXCCW4 THEN GO TO D190F302;
D190F301:
          [F
             10 D190G1;
          CO
             ROBCK PARAM THEN CO TO D190F304;
D190F302:
             MOD JECBIND2 THEN GO TO D190F304;
          SET TO BSF;
          BSF;
          IF I/O ERROR THEN CO TO D190F303;
          CO TO D190C1;
D190F303:
          SET UP ABEND 613;
          GO TO ABEND:
          BSP TVICE;
D190F304:
          IF
             I/O ERROR THEN GO TO D190F303;
          BSF:
          IF RDBCK PARAM AGAIN THEN GO TO D190C1;
          IF MOD JECBIND3 THEN GO TO D190G1;
          SET CCW1 TO BYPASS TM HDR;
             I/O ERROR FOR BSF THEN CO TO D190F303;
          IF
D19GF305: GO TO D19OG1;
        END / + OF D190F3 +/;
```

```
D190F3
      <C01>
                  011
       (XC3)
                  <002>
                             021
                             <C03>
                  (XO1)
D190F3I00000000011111
                             <C04>
                                        041
       112345678901234
                             [A01]
                                        [A02]
<001>
                                        <C05>
                             [A04]
<C02>
<C03>
                                        [A04]
                             <C05>-→
<C04>
       YNNNYNNNYN
<C05>
                                        <C06>-
                              (XO1)
<006>
<C07>
       1 - - YNN - - YNN -
                                        <C07>-→
<C08>
                                        [A03]
[A02]
                                        <C08>
[ EOA]
[A04]
                                        05+-
                             03+
                                         (XO1)
                             [A05]
        (X02)
           XX
                 XX
       IX
                             (XO2)
(XO3)
                      X
      5 9 11 12 13
```

QUESIIONNAIRE PROCESSING

Q DISCOUNT QUANTITY>100? YES QUANTITY>500? NO PREFERRED CUSTOMER? YES APPLY 10 PCT DISCOUNT	DISCOUNTS	1000000
APPLY 5 PCT DISCOUNT DO BILLING NEXT Q DISCOUNT QUANTITY>100? YES QUANTITY>500? YES	QUANTITY>100 QUANTITY>500 PREFERRED CUSTOMER APPLY 10 PCT DISCOUNT	
PREFERRED CUSTOMER? NO APPLY 20 PCT DISCOUNT DO BILLING NEXT	APPLY 20 PCT DISCOUNT APPLY 5 PCT DISCOUNT	11
Q DISCOUNT QUANTITY>1007 NO PREFERRED CUSTOMER? YES		

APPLY 5 PCT DISCOUNT DO BILLING NEXT Reprinted from

Vol. 16 No. 5 September 1972

Journal of research and development

H. J. Myers

Compiling Optimized Code from Decision Tables

Compiling Optimized Code from Decision Tables

Abstract: This paper reviews the structure of decision tables and methods for converting them into procedural code. It describes new optimization methods, which are applied before, during, and after code generation. Some results from an experimental decision table processor are provided.

Introduction

Decision tables have been in use for over ten years, principally in business applications, to state problems that contain a relatively high proportion of programmed tests. Numerous compilers have been built that convert the logic expressed in decision tables into algorithms that are executable by computer. Emphasis in decisiontable compilers has typically been placed on producing logically correct code, on checking the decision table for completeness and consistency, and on ordering condition tests for efficient execution. Most decision table compilers produce code that is in a higher-level language, leaving optimization of the produced object code up to the high-level language compiler. However, the structure of the code produced by typical decision-table compilers is of a type that is improved little by any of the optimizing algorithms used by commercial compilers today.

General-purpose support programs (systems programs) are also typified by a high proportion of programmed tests. It would therefore appear that effective use of decision tables could be made in describing systems programs. However, systems programs must also be comprised of highly optimal code. This paper describes some decision-table compiling algorithms that provide a program structure of high enough quality to satisfy most systems programming needs. The output can be used either by a post-processing compiler or by a programmer as a guide in hand coding.

For the project, we constructed a running compiler and support system into which were introduced numerous decision tables based on actual systems programs. From these decision tables, procedural code (in PL/I format) was produced. Some samples of the code structure produced are included in the Appendix to enable the reader to judge the effectiveness of the compiler. The system was produced in an interactive API, environment, which allows considerable flexibility in revising and augmenting algorithms.

In the paper, we first review the structure of decision tables and the procedures used to map them into code. However, the main emphasis is on the optimization methods we use before, during, and after code generation.

Decision tables

In order to make this paper reasonably self-contained, we review here the structure of decision tables and the general methods used to convert them into procedural language. We attempt to emphasize those aspects of the process that provide opportunities for optimization.

A sample decision table is shown in Fig. 1.

The *stub* portion (2) of the table gives descriptions of *conditions* (5.6) *actions* (7.8) and *exits* (9.10). The format of the stub contents is generally constrained by the target language into which the table is being translated. Our processor places no constraint on the contents of the stubs; if correct PL/1 code is to be produced, the condition stubs must contain one PL/1 relational expression each; the actions should contain a PL/1 assignment statement, call statement (or other nonbranching execut-

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	2	3
1	DPUT	0000000 4 1234567
5	MAME FIELD PRESENT OPERAND 1 IN REG. N FILE DEFINED	INYNYNYN
7	GENERATE NAME ERROR 1 ERROR 2	1 1 11 12 2 8
9	PUTTWO FINISH	XX 10

- 1. Decision table name
- Stub portion
- Entry portion Table header
- Condition stubs Condition cutries (Y yes, N no. --
 - Action entries (numbers indicate execution sequence)
- 9. Exit stubs
 10. Exit entries (X indicates exit taken)

Figure 1 Sample decision table.

able statement); and the exit stubs should contain only a valid PL/1 name. The table name should also be valid in PL/1.

Each column in the entry portion (3) of the table represents a rule. Rule numbers (two-digit numbers read vertically) and the table name appear in the table header (4). Rule 02 in Fig. 1, for example, indicates that if a NAME -EIELD IS PRESENT (Y means yes) and if OPERAND 1 IS NOT IN REGISTER NOTATION (N means no) and if FILE IS NOT DEFINED, then the actions taken are GENERATE NAME and ERROR 2 (in that order), followed by an exit to FINISH. In general, a rule specifies that for a unique combination of conditions, some selected actions are performed and a selected exit is taken.

The decision-table representation of logic does not impose any strict ordering on the sequencing of condition tests. Furthermore, actions in a given table are not allowed to modify factors that would cause a change in the outcome of a condition test in the same table. This gives the compiler more latitude in selecting an optimal ordering of condition tests with respect to each other and with respect to actions.

The ordering of actions with respect to each other can be loosely defined. In Fig. 1 the order required is specified by an integer opposite a selected action. If there is no integer, the action is not selected. Within a given rule, actions are executed in the order specified (e.g., in rule 07 GENERATE NAME occurs before ERROR 1). Exactly one exit is taken after execution of the actions of the selected rule.

In this experiment, we consider limited-entry type decision tables, in which the value of a condition is limited to yes or no. The alternative, extended entry tables, allows values that are numbers or number ranges. However, as Press [1] demonstrates, these can be converted into limited-entry tables, so that our methods apply to both types of decision tables.

The occurrence of a third value (-) in rule 04 of Fig. 1 does not contradict the two-value restriction. Rule 04 is actually a condensation of two rules (say 4a and 4b). which have identical action and exit entries, but whose condition entries are:

Condition	4a	46	4c
1	Y	Y	Y
2	Y	Y	Y
3	Y	N	-

Rules 4a and 4b mean that the specified actions and exits are to be performed when conditions 1 and 2 are both yes, regardless of the value of condition 3. Therefore in rule 04, condition 3 is immaterial-a don't-care condition. As will be shown later, in the code produced by the compiler, there will be no test for condition 3 in the flow path for which conditions 1 and 2 are both true.

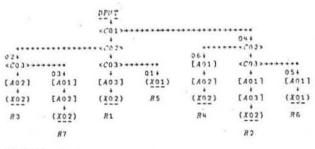
Because conditions in a limited-entry table are binary valued, there are 2" unique combinations of the values of n conditions, and therefore 2^n rule columns. In practice, the number of rules is greatly reduced by the introduction of don't-care entries and consolidation of the rules as shown for rule 04. If rules 4a and 4b had been counted as part of the table in Fig. 1, that table would have 8 (2") rules for 3 conditions. A detailed discussion of column combining is presented later in the section on pregeneration optimization.

With the 2" requirement on the rule count, one can program a verification of the rule entries that checks for both consistency and completeness. However, as will be seen, the check does not extend to an interpretation of the stub information and so it cannot be regarded as a complete check.

Decision-table code production

One of three methods is generally used to map decision table logic into procedural representation. In the rule mask method [2-4] every condition is first tested and then a selection mask is created. Next, for each action and exit, an action mask (created at compile time) is compared to the selection mask. If the masks coincide, the action (or exit) is performed.

In the second method, a variation on the rule-mask method, a unique power of 2 is assigned to each condition, which is then tested and its number added to a counter if the condition is true. At the end of condition testing, the counter is used as an index into a branch table and control is transferred to the appropriate actionexit sequence. (Note that the branch table must have 2"



Flowchart conventions

| Condition test n. (yes branch | Yes | horizontal, and no branch | Section | No | No | Label nn (generated by the compiler) | 1-nn | Astron n | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 | 1-3 |

Figure 2 Tree corresponding to sample decision table.

entries for n conditions.) These two methods require that all tests be performed regardless of the don't-care entries in the table. They tend to produce (when bit masks are used) a small program that runs longer than is usually necessary.

We use the third scheme, called the *condition tree* method, which causes the generation of a tree-structured program with condition tests at each node. Each action-exit sequence is placed at the leaves. Figure 2 shows an unoptimized tree corresponding to the decision table of Fig. 1. The rule numbers for each action-exit sequence are placed under the leaves for clarity.

Notice the one-to-one correspondence between the leaves of the tree in Fig. 2 and the rule columns of the decision table shown in Fig. 1. Notice also the large amount of redundancy in the code generated. In this case, the penalty for this redundancy is not lengthened execution sequences, but excessive storage consumption. Figure 3 shows the effect of the optimization algorithms on the tree of Fig. 2.

Figure 4 shows an *abstract* listing of the program diagrammed in Fig. 3. This listing is called abstract because nothing in it refers to the actual (concrete) conditions, actions, or exits that are described in the table stubs. In fact, the processing algorithms ignore the contents of the stubs except for listing purposes.

Because we are principally interested in compilation, the abstract format is the one used in this paper. However, with little difficulty, concrete listings can be produced for any procedural language.

Figure 5 is a concrete listing in PL/I format of the program shown in Fig. 4. Because the stubs in the original table (Fig. 1) do not follow PL/I conventions, the result is not a PL/I procedure.

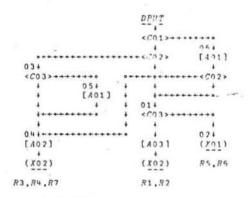
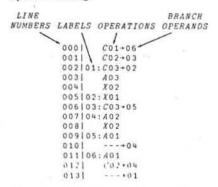


Figure 3 Optimized tree.

Figure 4 Optimized listing.



Note: Can + indicates a conditional branch, and --- indicates an unconditional branch

Figure 5 Concrete listing.

DPUT: IF NAME FIELD PRESENT THEN GO TO DPHTS: OPERAND 1 IN REG. NOTATION THEN GO TO DEUTS: DPUT1: IF FILE DEFINED THEN GO TO PUTTWO: ERROR 2 IF FILE DEFINED THEN GO TO DPUTS; DPUT3: DPUT4: ERROR 1: TO FINISH DPUTS: GENERATE NAME: CO TO DEUTH: DPUT6: IN REG. NOTATION THEN GO TO DPUTH: IF OPERAND 1 GO TO DPUT1:

Code generation

Various methods exist for the generation of tree-form code from decision tables [5-7]. Briefly, one selects (by criteria described later) a condition. The rule columns are then partitioned into two groups—a no-group and a yes-group—according to the values in each column of the selected condition. If the condition value is don't-care in a particular column, that column is placed in both

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each condition test, but do not take into account the oprequire that storage and time costs be associated with code with respect to time and space. These algorithms (supported by proofs) for generation of optimal object ing. Reinwald and Soland [10,14] give algorithms optimization concentrate on the order of condition test-Most published accounts concerned with object code

ing that it precedes. formed regardless of the outcome of the conditional testditioning, and is allowed when an action is to be perthe condition portion of the table. This is called precon-P.E.T. also allows the table writer to place actions into cares to provide adequate optimization of testing.) writer to order condition tests and to introduce don'tredundant testing sequences, (It relies upon the tablesequences (a space optimization) but does not remove The P.E.T. processor reduces redundancy in action timization of action sequences,

the separation of the logic required from the procedural tially reduces the principal benefit of the decision table timization burden on the user to a large extent. This par-In summary, previous processors have placed the op-

those presented here. A careful comparison of his methdifferent, his optimizations seem to overlap some of since this work was concluded. Although his approach is A recent paper by Dailey [11] has been published

test sequence. It could be further improved if frequency The methods described in this paper attempt to optiods to ours has not yet been carried out.

structure is suitable for systems programs. success in removing redundant code, and the program space requirements. Our experience shows considerable Most of our optimization methods are aimed at reducing information were provided (see [12] for the method), timization. It generates no unnecessary tests in a given duces excellent code from the point of view of time opoptimization process. The basic generation process proafter code generation, with major emphasis on the final Optimization processes are applied before, during, and mize with a minimum amount of guidance from the user.

Optimization techniques

mechanism that implements it.

paths. These procedures often leave dead code, branch quences of code and consolidation of duplicate flow zation procedures include removal of duplicate sewriting circles as "hoisting.") Postgeneration optimitioning in [8], but is more commonly known in compilering" of actions. (The latter technique is called precondiwith the ordering of the condition tests and the "factorare carried out during code generation are concerned rule portion of the table. The optimization methods that rule columns and introducing don't-care values into the Our pregeneration optimization consists of consolidating

Figure 6 Cross-linked action-exit sequences.

tion. Assume that the line groups. Code is then produced for the selected condi-

Cun + label - m

as follows: selected condition row). The resulting structure appears is re-entered with the yes-group columns (without the generated label (label-m) is produced and the generator selected condition row removed from them. Next, the consisting of the no-group of rule columns, but with the generator is then re-entered recursively with a subtable is generated, where lubel-m is a created label. The code

kez - Bronb rupel-m: uo - Econb (uu + pqpq + uu)

control will not flow from the no-group into the yes-Because every action sequence terminates in an exit,

recursion terminates, umn is found, its action-exit sequence is generated and of the conditions have don't-care entries. If such a colbranch. This consists of locating any column in which all must be made before generating each conditional the presence of don't-care conditions, a special check quence is then produced from that column. Because of exactly one rule column remaining. The action-exit semaining to be selected. At this time, there should be Recursion is terminated when there is no condition re- posed of substructures analogous to the main structure. It can be seen that the yes- and no-groups are com-

and optimization procedures, which will be discussed Other special tests are made for handling else rules

redundancy in the generated actions, providing better corresponding to it in the rule it is linked to. This reduces the first linked action is replaced by a branch to the action exits are marked by an asterisk. When code is generated, leftmard. For example, in Fig. 6, the cross-linked actionsponding elements in some rule to the left, they are linked and its selected successors exactly match the correto the netion-exit sequences to its left. If some action tions generated. Each action-exit sequence is compared processor [8] to reduce the number of instances of acused in the Preprocessor for Encoded Tables (P.E.T.) An extension of this algorithm, called cross-finking, is

code than other processors [8,9].

chains, and other dross in their wake, which is cleaned up by some seavenger optimization methods. A final optimization process was introduced into the program that we use to create a concrete ru/t listing. This process is an example of a scavenger procedure. Its purpose is to remove unneeded conditional branches to exits, which are themselves branches. These optimization methods are discussed in the succeeding sections.

· Pregeneration optimization

The first optimization method used is that of consolidating rules where possible so as to introduce don't-cares into the condition entries. We call this the *merge* process. Figure 7 shows the initial state of a decision table, which we will use to illustrate the process.

Two rule columns can be combined into one if:

- They are identical except for one condition entry, and
- In the differing entry, one column has Y and the other has N (neither is a don't-care).

The algorithm used first groups rule columns according to action-exit sequences. In Fig. 7 the grouping gives

(Note that groups containing only one column are ignored.) It then forms subgroups according to don't-care patterns, If two rule columns were

Y Y N Y ---Y N

they would have the same don't-care pattern and would be put into the same subgroup. Because there are no don't-cares in the table in Fig. 7, all elements of each group have the same don't-care pattern, so that the groups default to subgroups. Within each subgroup, the condition entries are compared to determine if a difference exists in exactly one position. Two columns from Fig. 7 that qualify can be combined as follows:

11		11		
11.5		4 5		
Y Y		Y		
NN		N		
YY		Y		
NY	(DIFFERENT)	-	(DON'T	CARE)

The value at the position of difference is replaced by a don't-care value and one of the rules is discarded. Once a pair of rules is consolidated, it is removed from the subgroup because its don't-care pattern has changed. The remaining columns in the subgroup are processed similarly until no further combinations can be found.

PUT1	1	0	0	0	0	0	0	ij	0	0	1	1	1	1	1	1	1
	1	1	?	3	4	5	6	7	ű	9	0	1	?	3	14	ţ	6
	+	-		-	+			*	-		-		+			-	-
C1	1	11	Y	11	Y	Y	Y	17	Y	11	11	11	"	11	Y	Y	Y
C2	1	*	22	Y	Y	Y	2		Y	Y	11	7	Y	y	11		Y
C3	1		7	:	**	Y	**	*	.:	*	J,	7	Y	y	Y	y	¥
C4	1	il	11	72	×	.7	Y	Y	Y	Y	2	Y	7	Y	77	Y	Y
	-	-		-						-		-	-	-	-		
11	ï										1	1	1	1	1	1	1
12	ï		1		1	1											
A3	1	1		í													
A 4	1			1	1	1											
AS	1							1	1	1							
46	i						1	1	1	1							
17	i	1	1	1	1	1	1	1	1	1							
A 8	i	ſ				1	100	7									
	-		-	-		-		-		-		-					
X1	1	Y	Y	Y	X	×	X	Y	Y	X	Y	y	Y	Y	y	Y	1

Figure 7 Decision table initial state.

After all subgroups have been processed, the combined columns are regrouped and reprocessed. This iterative process continues until no new combinations are possible. (Note that the particular combinations that occur depend upon the order in which the columns are matched.)

The effect of this optimization on basic code generation (in the absence of other optimization methods) is to remove unnecessary condition testing. It therefore improves both the space and time costs of the resulting code.

Figure 8 shows the results of the merge process and examples of its effects, as well as the effects of different ordering (in the absence of other optimization methods). On the other hand, in the presence of other optimization procedures, the effect is reduced because of overlap of optimization function. This pregeneration optimization gives the user a better insight into his decision table. It also reduces, at an early stage, the amount of work that the other optimizers have to do. This method generally results in a net reduction in compile time.

Both of the compilers that we examined [8,9] require that the user perform this optimization in order to produce better code. It is often convenient for the user to do part of this optimization himself when the situations giving rise to don't-cares are patently obvious. (It allows him to greatly reduce the number of rule columns he has to deal with.) The use of don't-cares also can be a trick that allows conditional test dependencies (a violation of decision-table ground rules); therefore the implemented merge function was designed to leave original don't-cares intact.

It is instructive to compare our merge process to the electrical engineering problem of circuit simplification. Recall that we identified two groups from Fig. 7 having the same action-exit sequence. Consider rewriting the condition entries of the second of these groups as follows so that they look like a Boolean expression.

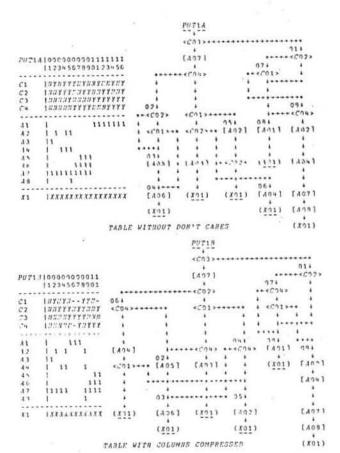


Figure 8 Effects of merge process.

1111111 0123456 NNNYYY NNYYNNY YYYYYYY NYNYNYY

The equivalent Boolean expression with A, B, C, and D representing conditions 1 through 4, respectively, is:

 $\bar{A}\bar{B}C\bar{D} + \bar{A}\bar{B}CD + \bar{A}BC\bar{D} + \bar{A}BCD + \bar{A}\bar{B}C\bar{D} + \bar{A}\bar{B}CD + \bar{A}\bar{B}CD + \bar{A}\bar{B}CD$ + $\bar{A}BCD$,

Note that each rule represents a term in the expression, and that all rules are in effect oxed (+) together. The elements within a rule column are ANDED. The decision table effectively states in this case, "If all of the conditions of any column are met, the common action/exit sequence is performed."

Now if we use the Quine-McCluskey technique [13], to find the optimal Boolean expression, the result is:

$$\bar{A}C + \bar{B}C + CD$$
.

The equivalent decision-table rule columns are:

N - -- N -Y Y Y - - Y

Investigation of the code generated from a table so optimized shows no improvement over that for the merge method. This is because the Quine McCluskey method introduces don't-cares that ultimately cause the code generator to place a rule column in two subtables. It can therefore be concluded that Quine-McCluskey optimization methods are not applicable to the decision-table optimization process. Note that the first two columns actually overlap (e.g., the condition sequence NNYY could apply to either one of them). Indeed, if it were not for the fact that both columns specify the same action-exit sequence, the decision table would be inconsistent.

· Optimization during code generation

The code generation process we used has already been outlined. However, the method of selecting the "next test" was deferred to this section since it has an impact on the quality of the code produced.

The most significant work published relating to code optimization is, in the author's opinion, that of Reinwald and Soland in Refs. [10] and [14]. Primary papers have also been published by Montalbano [6] and Pollack [7], and contributions made by King [2] and Press [1]. Most recently, Shwayder [12] extends the work of Pollack. King describes the rule-mask technique, which produces compact code but requires that all condition tests be performed regardless of the logic needed. The other papers deal with optimizing methods for sequential testing procedures (the condition-tree method).

Press takes advantage of an else column, reducing the number of instances of tests to a minimum (both statically-presence in storage-and dynamically-presence in a flow path). However, the Reinwald and Soland work is the most general and requires that the time and space costs of performing tests be included as input to the optimization process. Reference [10] describes time optimization and Ref. [14] describes space optimization. Both papers provide formulas for calculating the extra cost involved in performing test i after test j has been performed. They next demonstrate a means for determining a lower bound on the extra costs of all tests performed after test j. Then they provide an algorithm for searching a subset of all possible generated testing sequences to find the sequence with minimal lower bound. They prove this sequential procedure to be optimal in terms of the number of tests. The costs of actions were not considered. This, we presume, is because action sequences were considered by them to be atomic units and therefore could not be subjected to reorganization. It should be noted that the time consumed by the search algorithm goes up rapidly with the number of condition tests in the table. (This approach falls in the area of combinatorial mathematics and is akin to the "traveling salesman" problem.)

Our initial approach to test selection followed along the lines suggested by Pollack [7]. It was the simplest, and our attention was focused on postgeneration optimization—a subject treated only lightly elsewhere. The first criterion was the selection of the condition row with the fewest don't-care values. Beyond that, if a tie had to be broken, the row was selected that had the minimum difference between the number of Y's and N's (again on the advice of Pollack). Later this was compared to the Press method.

It was found that the Press method

- · Required more compile time,
- Did not improve object code in the presence of postgeneration optimization, and
- · Interfered with "hoisting" optimization steps.

We felt that concentration on postgeneration optimization should be continued, and no further effort was expended on enhancing condition test selection. Clearly, further investigation is warranted. The original selection algorithm is therefore retained. Because an else-column capability was desirable in a decision-table processor, it was provided. The implementation was simply to construct the missing condition rule entries and to supply them with a common action-exit sequence (specified by the user). These added columns are compressed via the merge routine and require no subsequent special treatment. Test selection and hoisting are applied equally to all columns.

Schwayder [12] shows how to incorporate frequency information into test selection. Although this was not incorporated into the processor, the impact on our current generation technique of including Schwayder's algorithm was investigated. If the frequency information specifies the rule frequencies, only one line of API, code need be added to take them into account. Another line of API, code would be required if condition test frequencies are given. More effort would be required to incorporate frequency specification in the decision table format than in making use of it in code generation. Inclusion of optional frequency information is recommended for follow-on work. (As will be described later, some postgeneration optimization methods may destroy some of the effectiveness of test selection.)

The second optimization performed during code generation is that of "hoisting," When one or more actions are to occur in all flow paths following from a single

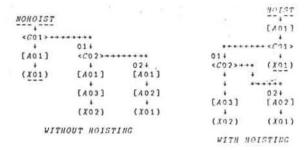


Figure 9 The effects of "hoisting."

condition test, it does not destroy program logic to move (hoist) these actions to a position in front of the condition test. This is a space optimization, because duplicate instances of these actions can be removed from all successor paths to the test. Figure 9 demonstrates the effects of hoisting.

It should be noted that an action cannot be hoisted past another action that must precede it in sequence.

Hoisting can most readily be performed during code generation. Just prior to selection of a condition test, the subtable is examined for an action that:

- · Is performed in all remaining rules, and
- Is not required to follow an action that is not performed in all the remaining rules.

Any actions that fulfill these criteria are immediately generated and removed from the subtable.

The action entries accepted by the processor allow the user to indicate an ordering, a *lack of* ordering, or a partial ordering requirement on the actions. This is a degree of freedom not provided in procedural descriptions.

It is not always easy for the user to recognize hoistable actions because he cannot easily recognize the subtables. However, he always has a clear picture of the ordering requirements (or their lack) on the action. Automatic hoisting lets the user specify his logic requirements in terms most easily understood by him.

Unfortunately, hoisting can, on occasion, exert a negative influence on duplicate sequence removal, an optimization procedure that occurs after code generation. This problem is discussed after the description of the affected optimization. But a way has not been discovered to detect the situation without exhaustive (time consuming) combinatorial analysis of the entire table. Fortunately, in the practical examples we have examined, hoisting is more often good than bad. (It seldom had any effect on duplicate sequence removal.) We explicitly resist taking the route of the P.E.T. processor, which requires that the user control this optimization. As mentioned earlier, post-generation hoisting might improve the situation, but was not investigated deeply.

0031	C02+16		0351	C02+02
0041	A01		0361	A01
0051	A02	×	0371	A02
00610	2: 03+03		038 1	5: C03+17
0071	X01		0391	X01
00810	3:407		040 1	7:407
0091	C07+25		0411	(07+13
0101	+18		04211	8:404

Figure 10 Possibly duplicate sequences.

· Postgeneration optimization

The two principal postgeneration optimization procedures are duplicate sequence removal (DSR) and duplicate path removal (DPR). In addition, "scavenger" optimization techniques remove

- · Dead (unreferenced) code,
- · Redundant condition tests,
- · Redundant unconditional branches (branch chains),
- · Redundant exits.

The major effect of these optimization procedures is to save space.

Duplicate sequence removal (DSR)

The code generation technique we use assures the performance of all testing required to isolate a rule. If don'tcares are inserted by the user or the merge process, then no tests are performed beyond those actually needed to -isolate a rule. Note that rule frequencies are not taken into account, and that a reduced execution time cannot be assured. Average performance can be varied by changing the order of testing, but the minimum amount of testing required to isolate a rule cannot be varied [10]. Beyond reducing the tests to a minimum, one can attempt to restructure the generated code so as to reduce the number of duplicated code sequences. This is done by replacing one of the duplicate sequences with a branch to its equivalent (DSR). In this way a small time loss is introduced (to execute the branch) to save storage space.

DSR is easy to perform because the code generator produces code containing easily noticed patterns of duplicated code. Pairs of code sequences with identical operation codes are first isolated and then more carefully scrutinized (longer sequences first) to assure logical equivalence. If they are logically equivalent, one of them is replaced with a branch to the other. The compared sequences are considered not equivalent for the following reasons:

- · They are dissimilar at any point,
- · One sequence overlaps a portion of the other, or
- A portion of either sequence has been previously removed (because it was equivalent to some other sequence).

If any of these conditions occur, the unequivalent portions of the sequences are masked off and the remaining "good" portions are individually compared by a recursive procedure. Any duplicated sequence longer than one line of code is removed if it is logically equivalent to another.

Figure 10 shows two possibly duplicate sequences. Note first that lines 010 and 042 are not included in the sequences being compared, but are shown because they are germane to the detailed comparison. Operation codes in lines 003 through 009 match those of lines 035 through 041. The flow paths following these sequences pass to lines 010 and 042, which are seen to be equivalent. The branch paths from lines 003 (035) and 006 (038) are readily compared because they lie within the two sequences being compared. These can be certified as being logically equivalent by comparing the offsets of the branch targets from the beginning of the sequences. Note that in the case of the 003-035 pair the branch targets are across sequences but that this fact does not matter in the comparison. A lengthy analysis must be performed only on the paths emanating from the pair 009-041. This requires a line-by-line comparison of the code starting at label 25 with that starting at label 13.

The cross-link process performed by the P.E.T. processor is an attempt to eliminate duplicate code. Its effect, however, is to eliminate only common trailing portions of action sequences (those that end in an exit). Our algorithms remove all trailing sequences of redundant code, including redundant test trees. In addition, all nontrailing redundant sequences are removed if they flow into logically equivalent code. In general, the results have been very good when applied to actual system programs, as shown by the examples in the Appendix.

It was mentioned earlier that the hoisting optimization can have a negative effect on DSR, because hoisting of an action may remove it from one of a pair of duplicate sequences. When this happens, the pair no longer qualifies for consolidation. It is possible, for example, for hoisting to remove two lines and prevent the removal of, say, ten or twelve lines of duplicated sequences. On the other hand, the removal of an action from a sequence could also cause that sequence to match another, when it would not have done so otherwise. Some cases like this were actually encountered, such as Tables 8 and 9 in the Appendix. Through use of compiler options, selective elimination of hoisting is allowed. Further work should be done to try to establish an effective method for predicting the effect of hoisting on DSR.

Deferment of hoisting until the post-generation phase was considered. Hoisting at that time is much less convenient and will definitely lead to longer compile time. More information must be carried and maintained to keep track of the rule column(s) that were the source of

a particular action. The problem is made more complex by the folding actions of the other optimizers. On the other hand, reasonably good results were obtained with the algorithm used, and we judged the effort-to-payoff ratio too high to implement delayed hoisting.

· Duplicate path removal (DPR)

The second major postgeneration optimization technique is duplicate path removal (DPR). When two flow paths that emanate from a condition test have identical leading logic, the leading portion of these paths can be consolidated by moving the condition test down the paths to the point(s) where they differ. Figure 11 illustrates DPR.

As can be seen, DPR may cause reordering of condition testing. The method is to isolate as potential candidates those flow paths emanating from the same condition test and that start with the same operation code. Then a test is selected whose branch target and successor lines contain the same operation code. For each such test, the flow paths are compared and points of difference (POD) are located. (Note that if there are no POD's; the test is redundant and can be removed immediately.) The two paths emanating from the test will be referred to as the "fall-through path" and the "branch path." Conceptually, the process is as follows. Locate the POD's in each of the two paths. Remove the test and place a copy of it just in front of each POD in the -fall-through path. The address of each test copied is changed to point to the corresponding POD in the branch path.

With the removal of the original test, it is expected (but not guaranteed) that a large portion of the branch path will become dead code. However, there is no simple way to determine this prior to performing DPR. Therefore a copy of the code is saved before performing DPR. If the code resulting from DPR shows improvement, it replaces the old code. If not the old code is restored. Some time was spent trying to develop some correlation between the amount of improvement and both number of POD's and number of lines in the duplicated paths. However, nothing developed that was useful. Furthermore, the conceptual algorithm described above did not work. This was because DPR was applied after DSR, and DSR could destroy the tree nature of the original unoptimized code. Therefore, DPR has to contend with the interesting possibility that the two flow paths being compared might merge, or cross over. In fact, some cases examined had a single line in the code turn up as POD's in two different paths. This caused the same test to be inserted twice in the same place.

The algorithm finally developed avoids the problems by making a separate copy of the fall-through path, and placing at each POD (in the duplicate) a copy of the original test with the target of the POD in the original

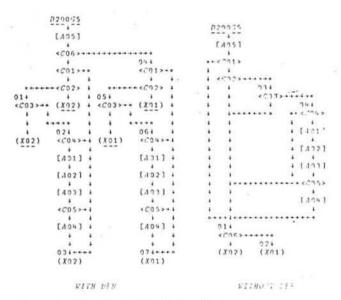


Figure 11 The effects of duplicate path removal.

branch path. This test is followed by an unconditional branch whose target is the POD in the fall-through path. At the end of the path comparison, the original test is replaced with an unconditional branch to the duplicated code, which is placed at the end of the code body. Both the branch and fall-through paths then become candidates for dead code removal.

Because DPR reorders condition testing, it may destroy the effectiveness of optimization procedures that depend upon test ordering. Since DPR never increases storage consumption, only time optimization can be adversely affected. This is a typical time-space trade-off situation. Unfortunately, the user may wish to trade off differently for different points of application of DPR within code from a single table. At present, sufficient information is not carried in the generated code to calculate the overall time costs. A recommended follow-on would be to try to include this.

DPR can recognize only those duplicate paths that emanate from the same condition test. Some tables that were processed contained duplicate paths that did not emanate from the same point and couldn't be removed by the processor. When these paths were removed manually, it was discovered that they could be removed only at the expense of inserting additional tests in the paths. The resulting code then would contain two tests for the same condition in a single path. This is typical of a situation in which the programmer would ordinarily set a switch for later testing. Table 6 in the Appendix contains this type of program structure. No algorithm was discovered that could expeditiously locate duplicate

0041+07	004+17
009107:08	009 07:+17
011108:17	011 08:+17
015/17:	015/17:
BEEGRE	AFTER

Figure 12 Branch removal.

Figure 13 Reordering.

0071	403	0071	A 0.3	
1800	* (16	1000		(REMOVE)
00010	4: C02 - 17	00910	6:404	
0151	XOI	0131		03
01610	6:A04	01410	4:002+	17
0201	+03	0201	X01	
02111	2:405	02111	2:405	
R	EFORE	A	FTER	
	17 4 4 11 10			

Figure 14 Backward branch movement.

0121	A03	0121	A03	
0131	A04			(REMOVE)
0141	+07	0131		0.7
0151	***	0191	13.1	
1		1		
0261	A 0.1	0.251	A01	
0271	A09	02610	7:A04	
05010	7:405	0271	405	
В	EFORE	A	FTER	

Figure 15 Dead code removal.

0121	07	0121		7
0131	* A06	0131		
		1		(REMOVE)
0211	X01	0211		
02210	9:402	022109	:402	
1	***	1		
0261	X 0 4	0261	1 124	
0211	* A00	0271	0.00	
1			4 - 4	(REMOVE)
0301	02	0301		
0311	12: 006+04	031112	: 006 .	3.4
ė	BEFORE	AF	TFR	

.NO LABEL PRESENT

paths of this nature. This is also an area that needs further exploration.

· Scavenger optimization

Scavenger optimization procedures, as their name implies, clean up the leavings of other optimizers. The scavenger procedures to be described in the paragraphs below are

- · Redundant branch removal,
- · Redundant exit removal.
- · Redundant test removal, and
- · Dead code removal.

Redundant branch removal is a procedure for seeking out branches that receive control from other branches and removing them. (Sequences of directly connected branches are called branch chains.) Labeled branches are readily detected. All references (in other instructions) to the label on a branch are replaced with the branch operand, effectively removing the branch from the chain. The labels can then be removed. Figure 12 illustrates branch removal.

The second step is to locate all branches that are preceded by either an unconditional branch or an exit and to remove them. All branches whose targets are exits are replaced with the exits themselves. If a branch has a target that can receive control only from the branch (i.e., the target is preceded by an unconditional branch or an exit), then the branch can be eliminated by reordering the code. This is done, thereby placing the target and all code physically following it (up to a branch or exit) in place of the branch. Figure 13 indicates the method.

While cleaning up branches, this optimizer can perform one additional optimization step, which, strictly speaking, is not redundant branch elimination. It is a type of reverse hoisting. If the predecessor line to a branch target is the same as the predecessor to the branch, the branch predecessor can be removed, and the branch address reduced by one, to refer to the predecessor of the former target. Figure 14 shows this optimization method.

Redundant exit removal is a procedure to remove all exits of any one type that are preceded by a branch or another exit. Care must be taken, however, that there is always one exit of each type left. All conditional branches that have exits as their targets are set so that all references to an exit of one type refer to the same exit. This reduces the requirements for instances of exits to a minimum. (Note that unconditional branches to exits were eliminated by the branch optimizer.) No further processing by the exit optimizer is necessary.

Redundant test removal requires that the flow paths emanating from each conditional branch be examined. If the fall-through path is logically equivalent to the branch path, the test is eliminated. No other clean-up is performed by the test optimizer.

Dead code removal is a procedure that first removes all unreferenced labels. It then locates any unlabeled lines that follow exits or unconditional branches. These

lines are dead and can be removed. Any unlabeled line following a dead line is also dead and can be removed. All dead lines are located and removed at one time. However, because a dead line may have been a branch (conditional or unconditional), the removal of dead lines may give rise to more unreferenced labels. Therefore, the process is iterated until there are no more such labels. Figure 15 illustrates dead code removal.

Interaction of the optimizers
 The order of all optimization procedures is:

Pregeneration

Merge

Generation

Test selection

Hoisting

Postgeneration

Duplicate sequence removal (DSR)

Scavengers

Redundant branch removal

Redundant exit removal

Redundant test removal

Dead code removal

Duplicate path removal (DPR).

The postgeneration optimization procedures are iterative whenever their application can possibly introduce new program structure that would be susceptible to their further application. Further, the scavenger steps are called by both DPR and DSR, and DSR calls DPR. Specifically, DSR processes all of the sequences it can, then calls the scavengers. It then determines whether code was reduced. If so, it repeats. In the case of DPR, after each test instruction is processed, the scavenger procedures are called, followed by DSR. (Calling the scavenger procedures before entering DSR speeds up the latter.) Then DPR checks for code improvement as described earlier.

Concluding remarks

About seventy decision tables taken from actual application areas have been compiled by the system. The code for only two of these could be improved by hand. Furthermore, the compiler running times under the APL system were short enough to allow the experimental model to operate as a production tool. Although there must be manual intervention between processing a decision table and the production of the final code, the processor as it stands has already proved to be a useful aid to some programmers by helping them organize their code. It can be concluded that the processor can be used *now* to gainful ends. If the algorithms described above were recoded into a more fully automated environment, program production could be improved even more.

As has been noted earlier, certain optimization problems are yet unsolved. These are:

- The interference among test-order selection, hoisting, DSR and DPR, and
- Duplicate path removal where the paths do not start at the same condition test.

Some additional optimization procedures could be added to the processor. These are:

- The use of frequency information in test-order selection
- The use of timing information in conjunction with space/speed priority setting for DPR, and
- · The reversal of condition tests.

An investigation of the benefits of post-generation hoisting should also be made.

The optimization methods described in this paper do not include all possible program optimization procedures. We have concentrated on optimization methods that are not usually done by higher-level language processors, namely, the gross arrangement of program flow structure. Specifically ignored are such optimization methods as loop analysis, common subexpression elimination, code motion (except for hoisting and forward code motion), and subsumption [15]. To perform these would require analysis of the information in the decision-table entries, and would require a restriction on the language permitted in the entries. It was felt that a wider service could be performed by providing a framework that would accept any language for entry statements. In this way, optimization procedures that were not performed could still be accomplished by passing the output from the decision-table compiler through another optimizing compiler. Minor revisions to the PL/1 printing program can cause it to produce output acceptable to FOR-TRAN, ALGOL, and COBOL compilers (some of which perform optimization), and even to an assembler macroprocessor.

It should be noted that if a decision table has n rulecolumns, then there are exactly n ways of traversing the generated program. This should suggest that exactly n test cases need be prepared to thoroughly test the produced program. Such a test battery would be guaranteed to execute every instruction in the program, and to execute every conditional branch for both yes and no conditions. Because the optimizers fold the program so that some paths through the program execute the (physically) same instructions, it is often possible to completely exercise the program with fewer test cases than the number of rule columns. A test-case generator could easily select a subset of the rule columns that would exercise all

of the code. It is recommended that such a generator be added to the system.

Acknowledgments

The author is indebted to D. H. Manning and P. C. Jacobs for first bringing the problems of decision table compiling to his attention. D. H. Manning, and R. E. Gaiduk are to be thanked for their enlightening discussions on the subject, and our thanks go to May Li and D. H. Manning for providing some "real" decision tables against which the processor was tested. R. H. Williams contributed to the solution of DPR problems.

Appendix: Samples of decision table compilations

This appendix contains decision tables and flow charts of code compiled from them. The reader is invited to browse them to obtain a subjective appraisal of the effectiveness of the compiler. The tables are representative of those examined during the development of the compiler. They were selected to demonstrate several points.

The range of table complexity,

Compile-time range,

The effects of various optimization procedures,

Comparison with other methods,

Some unsolved problem areas.

In addition to these points the reader should also be able to verify that:

There is a unique flow path through the object code for each rule column.

A given flow path contains no redundant tests.

Hoisting situations are not always readily seen in a decision table by the user,

The compiler produced correct code,

The code cannot be improved by hand except where noted.

Table 1 lists the sample decision tables, the time required to compile them on a System/360 Model 50, and the number of lines of object code produced.

In reference to Table 8, note that in some cases the order of condition row selection is arbitrary. When this happens, it is possible that different orderings will produce different amounts of final object code. The condition rules were reversed in D190F3 in Table 8 to create D190F3R. The code produced from D190F3R was 26 lines compared with 20 lines produced from D190F3. Careful examination of the produced code shows that the reversal of selection of conditions 3 and 5 (in the subtable for condition 2 = yes) caused a difference in the hoisting of action 4. When action 4 was hoisted (compiling D190F3), a pair of duplicate paths appeared, one of which was removed by the optimizer.

Table 1 Decision table compile times.

TABLE	TIME	LINES OF
NAME	(SECS)	CODE
CHECK	15	17
CHECKII	-	15
D190F3	25	20
D190F3R	26	26
D190F4	50	31
D190F4M		26
D190/12	11	9
D200C19	7	7
020003	. 6	4
D200C9	2	3
P20075	48	15
220067	1.9	1.8
0200/19	13	0
PTABCD	2.3 11	43
F E 27 1	19	23
FED2	13	26
H9PET		10

Table 2 Examples of simple decision tables and their corresponding flow charts.

D200C31000

(Xn1) IXXXX

(xn1)

				02000	4 1 14 17 17 1			
D200C910	P.				1123	D200C	3	
11	D200C	3						
		-		<001>	17""	**** 601	>	
«CO1» 1-	[401	1		«C12»	1-77			
						1 <002	>	* 2
[401] [1	EA02	1		[401]	1			
[402] 1						*****		4
	(Xnt	1		(xer)	1 1	014	2	7.4
(X01) 12				(XO?)	IX X		1 (1253
							2122712	1
							[301]	
22202					91505	12 100 10	<0.31>	
D200C1						11274		
	11234		D2000	1.2			[422]	
					< C01>	YNAH		
<c01></c01>	1 4 7 77		**** < 631		4502×	1 - Y.7 "	<0772×	
< C0 2 >	1 911	024			<003>	1 77		
<c03></c03>	11/1	[401]	*++ < C0 2				[ADD]	
					[401]	11111		
[401]	111	<c03>+</c03>	++ (X03)	[402]	1 222	e:9734	
					LAGT	1 1 33		
(X01)	1 x x				[ADA]	1 1 4	[404]	
(X02)	1 x		014		[Ans]	1 15555	4	

Table 3 A complex table.

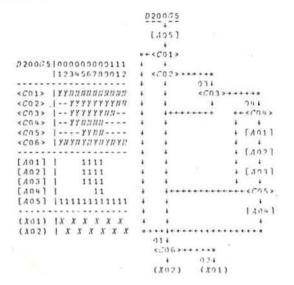


Table 4 Table compiled by our processor and by P.E.T.

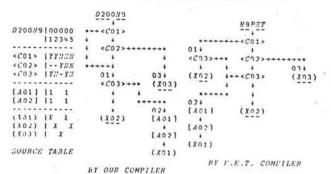
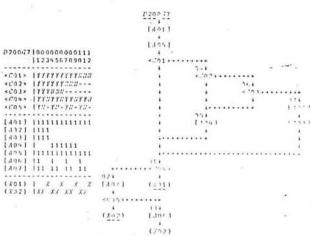


Table 6 Manually improved example.





```
D190F4M
                                      <C01>
                                                  (tot)
 024
 < C 0 5
                                     [AO4]
                         03+
 (X02)
                         [401]
                                     (X03)
          PRODUCED
          MANUALLY
                         [A02]
                          <C06>
 354
+ < (:07)
                          <C02>
                         <C03:
             08+
           ++<C09>
                         (X03)
                                     [405]
 06+
[403]
             (X04)
                                     (X03)
 · (103)
* < CO 3>
```

D190F4

<001> <002> + <003>

< C04>

[104]

(X03)

COUPLLED

02+

[101]

[402]

er'na

(103)

014

+ < CO5>

(X02)

034

<(1)7>++

<C08>

[AO3]

[A05]

04+++

(xou)



Table 7 Another example of manual improvement.

		CHECK			CAECZII		
		21,00					
		<c01> ***</c01>			< CO1>+		
		1	031				031
		[402]	[400]		[405]		[(00)]
		Luori	[400]		F1007		
CH3CK100000		[403]	(*** * *		[103]		(X01)
112345		[403]	(x_{01})				
112343		<c02>+++</c02>	SESSECTION OF		<002>+	••	
<001> Yunun		<0.02>+++				٠	
			02+		[vee]	+	
<c02> -YAHA</c02>		[304]	[404]		4		
<c03> Y!!!</c03>		•	+	.+	+<003>	+	
<c04> Yil</c04>	0+	+ <c03></c03>		+	•	4	
1 *** 11		*		+	[/07]		
[//01]] 6	+	[407]		+	+		
[/02] 1111	+		4	+	[ROB]	+	
[403]] 2222		[AOB]	÷	+		+	
[Anall 3 66					c 4 > .		
1/05/11 0477	4	LAGH 1			1		
140611 333	+			+	[401]	4	
148711 44	+ +	· « CO4 »		+		1	
130011 55							
[409][1	+	[401]	+	+	011+++		
	+	+++++	*****	+	[404]		
(XO1) XXXXX	7.0	****		+			
		01+			****		
		[/05]			02+		
					[105]		
		(XO1)			1,10,3,3		
		10000			(X01)		
	C	OHPILED			() ()		
				11	ARUALLY	1::	TROVER

(X04)

06 + < C05>

(X02)

001

09+ [403]

(X04)

COB>+

07+

[401]

[402]

<C06>

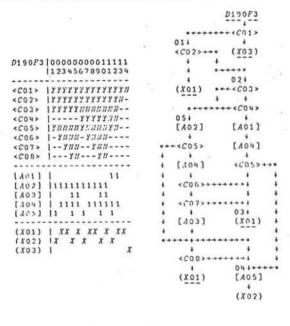
14051

(X03)

05+

(X04)

Table 8 Effects of arbitrary condition row selection.



D190F3R | 00000000011111 112345678901234 < COB> |---Y!!---Y!!----|--Y##--Y##----«C07» 1 - YUNG - YUGU - - - -«C06» YUNUUYUUUUYU--<C05> <C04> ----YYYYY##--<C03> IYYYYYNNNNNNN--*C02> ITTTTTTTTTTTTTTTT IYYYYYYYYYYYYYY <C01> ETC. 1 ...

D190F3 WITH CONDITIONS REVERSED

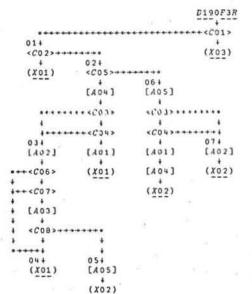


Table 9 Effects of detrimental hoisting.

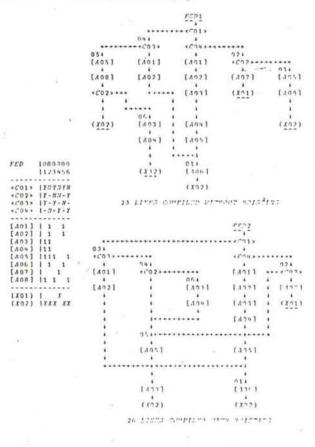
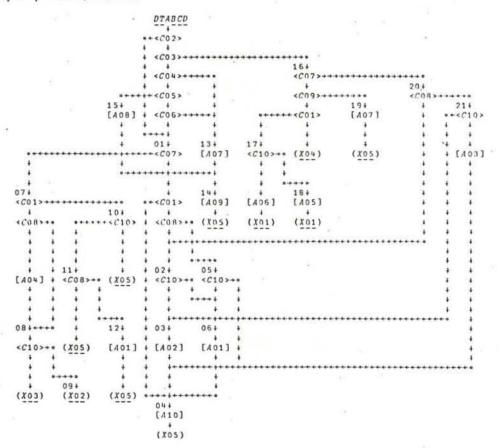


Table 10 A very complex table.

DTABCD	10000000	000111	11111	112222	22222	23333	33
	1123456	89012	34567	890123	145678	90123	45
<c01></c01>	YYYY-Y	YY	YYNNN	NNUNN.	HN	nanan	77
<c02></c02>	YYYYNN	NNNNN	NNYYY	YYYYY	THUNNA	NNSKN	111
<c03></c03>	1 YY	YNNNUN	INN		YYYYY	nnann	11.11
<c04></c04>	1	- Ynnni	11111		1	nnnnn	nn
<c05></c05>		Ynnn	11111			MARANI	MM
<c06></c06>	1	YIII	11111		/	manna	nn
<c07></c07>	1YYYNNN.	11 Y 1	YNYYY	YNNNN	YYYYN	YYYNY	N.N
<608>	YN	Y	YYN	NYYNN	YY##-1	YEVYY	:::
«(09)	1 YN	11			N-		٠.
«C10»	IYYNY	N Y 1	N-YNY	NYNYN	YNYN-1	NYNYN	YM
[401]	11	1		11	1	1	1
[402]	1			1	1 1		1
[A03]	1				1		
[404]	1 -		1	1		11	
[405]	1 1						
[A06]	1	1					
[A07]	1 1	1 1					
[AOB]	1	1					
[409]	1	111	100				
[410]	1 9		9	9399	9999	95	99
(X01)	1 X	X					
(X02)	1		X X	(-	XX	
(X03)	1		X	X		XX	
(204)	1				X		
(X05)	IXXXXX	XXXX	WWW.	XXXX		w	XXX

Table 10 A very complex table (continued).



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				CONDITIONS											1						-	-	-		-	ACTI		NR.	TO PAGE
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			ONDITIONS											1 .							ACTIONS	
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DATA RULES

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APPROVALS & DATES	PROCESS AF	REA CONTACT PERSONS	NEL		phen	ARED BY			DAT	E		H. Carlot	PROJECT NR DESCRIPTION . AREA
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	0	CONDITIONS											ACTIONS
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	-	1000				Bagne	337077				1	Name	ξ 5εςNO
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		THIS ENTRY MIC V	5 IS LASTABAGE		HI		54	122		HE	Eq		BEGIN AT RIGHTH END OF TABLE, LOOKING FOR IST HI.
		ENTRY SOUND IS	s teno		N	3	Y			N	N		IP NOT \$ - NOT RUIST AND NO ASSOCIATED TAG
	BAT						1						
	MOVE	(RUST Brown)	MESSAGE	X									MORE THAN 25 RUSTS - GIVE MESSAGE & TREAT AS VLIST
	Do	PRINT		×									
	SET	Entrey Segre &	R ZEILOS	X		1999							
	Ser	I. LESTA BONT			74	SENTRY	Thise	EHTHY.	NEXTERTRY	78/3.64	THE	THIS ENTRY	IF LOW - MOVE ITEM IN TODGE TO KIETH "
	SET	CASTABONT -	- 15					×					Shift to ADDRESS Item ON LEFT of This ONE
	SET	Westerns -	. 15					×				The Park	for NEXT COMP & BNO
	58 T	- LASTABARG -	- 15					×					SHIFT TO ADDRESS OF THIS I TENE FOR RCV.
	MOVE	(DUPHLATE)	MESSALE				×			1000	×		DUPLICATE THAS - OIVE ERROR MSG (CRITICAL), BUT
		BIGERROR					×				×		ENTER ITEM AS 14 HI.
	Do	PRINT					×				X		
		LASTAGENT	HE LASTENT		×	17	*	11		×	×		IF HI/EO - MOVE ENTILY TO TABLE , UPDATE ADDR
		TABLE COUNT +			. 7	2. 1	×			1	×		OF LAST ITEM IN TABLE 64 15
VI MAN		FUNTBL BASE +			2		×						
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	SET		LASTABENT		×		×			ger	*		
	SET	Derreater .			*		×			2	×		SET UP FIRST 'ROU BOOK for NEXT LOOP.
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	ALS & DATES	PROCESS	AREA CONTACT PERSON	INEL		PREPAR	ED BY		DA	DATA RULE			PROJECT NR C	LECRIFFION		
			CONDITIONS							VISION					PADCESS AREA	
					4-11									8	ACTIONS NR	TOF
			TABLE	904 -	DECIS	SIONS E	Moces		ED MESS							
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			ERROR TUD E	9	1	1		. 2	2			2				
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		DO	TAYUT			X	×			X						
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			TAB	LE 401- TESTA	TEMEDT	TAO	LE 405.		IST FIX		,					LE 40	7 - 0(pennon			IONS		
URRENT		WEEKLY	Aurian	NAME OF DAME 2			-	2 2 2		-										-			
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				SETUPTAT TO CHORILTA	4		Dexregan		Dextellial; 3							SETUPO		TZ Messi	100			12/2	
			MOVE	CTEST) TO WORK OPE											20	PRIM				23			
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			7													-	-	-	-				+
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-			TABLE	-404 - LINKAGE		TABLE	406	TEST	TFIN			The Party				1	-			-			1
-		-			9.000							THE STATE OF THE S				1							
			Action	Name 1 08 Name 2		, ACTION	NAMEL	00	DAME 2.	00		THE PARTY											
			MOVE	WORKTAL OUTTAL		10-	Serup FALSE	E4	BLENICS	4 1					FILE								1
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			MOUE	(TPLNK) TO OUTOPE A		Mout	(.)	TO D.	lestichair, 1		100												
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						ANDOR	(.)	to Ne	extebar, 1						1	Part .							
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						S-+	(.)	TO U	extern, 1						400					1-1-1			
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			1111				WORK		die et									-			-		
						GO TO	MITEI	64	DUANES					distribution of the same of th					-	-			
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					THE STREET STREET
			DATA RULES		
APPROVALS & DATES	PROCESS AREA CONTACT PERSONNEL	PHEPARETABY	DATE PROJECT NR.	DESCRIPTION	PROCESS NR. PAGE TO PAGE
			The state of the s		ACTIONS
	CONDITIONS				
	TABLE 402 -	CODE CONDITIONS TO TEST STATEMENTS		TABLE 403 MEMZCI	+<
CURRENT PRIOR FREQUENC	Action Name 1 OF LAME OB 3 &	000000000000000000000000000000000000000		Action NAMI OF NAMES DOB	9 9 9 0
	Literupuant to (#) Y Y Y N	NUNNNNNNNNNNNNNNNNNNNNNNNNNNNNNNNNNNNNN	ALKERDY A LITERAL	TAGEOUE BY BY Y	N N N N N
	Serviciams 15 Liumeric	YXNNNNNNNNNN	Need to ogineals		YYYY
3	Setug NAME IS EXPTABLE	Y Y Y Y Y Y Y Y X N N N	IF Y FUD = TAC FUDADOR	JETUGUINA 13 EXPTABLE NYY	N Y Y Y
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1	TALCODS EV 1	YYYNN	BENEVEZION	TACCODE IS I	YNN
3	7240000 89 4	YY	BOTATO ROLLATION		
. 9	SUTUPOP IS OPTABLE Y N				
7		YYNN	VLILE NAME		
1	TAGROWADER 15 ZERO	NNYNYNYNYN		REFLO SETUDIAMIL X X	× ×
1	BASEMENSON IS ON YNDY	The state of the s	INDICATES 1ST PASS.		××
		TH IN THE		MOVE DETURNMENTO MESSAGE X	
	SEJ Reporto			POT MESSAGE X	× ×
	SET IZLTH TO	(42) (43) TEL		MOVE SET-GRANTIL DENTHANTER X	××
Y.	GO TO MENTZCHE			SET DEXTHALL + TLATH X	xx
	DO LINKAGE	* * *	ENERTE LINK TO WAP.		x ×
	.Do. Openhon X	× X X	GIVE SAKEN MER, ASSUME EQ	Move (5) Dexilianty X	X
	MOVE ROODS TO MERTILIAN, RUTH XX			SET Nextchal + 1 X	× × ×
	SET DESTILATE + RETH X X	XXXXXXXXX			XXXX
		X x X X X X X X X X X X X X X X X X X X		COTO TABONOS XX	
	SET DENTILLER + 1 X X				
	MOUS CA) DEPOLAR, 1 X	××			
	SET VERTILIE + 1 >	XX			
	MOVE SOTOGRAMS VENTRARYTH X X X X	XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX			
	SET NEXTHER + TATH X X X X	XXXXXXXXX			
	MOVE (#) Wertchard >				
HER LAND	SET DENTCHAR + 1 >	XX			
EN CHAIR THE	Wood (1) Vertibally 2 X X	X			
	SET DEXTCHAIR + 1 X X	V			
	REFLD SETUPNAME SETUPNAME X		Revenses paines 1 to 2		
	SET BISEMEN TO OFF X	X			
	DO RLISTEIN				
FORM HOOS HEV AUG 1940	GO TO TEST FIN X X	XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX			THE RESERVE OF THE PARTY OF THE
	GO TO TABOVON X	X			

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			DATA RULES						
APPHOVALS & DATES	PROCESS AREA CONTACT PERSONNEL		RION		PROJECT NR DES	HIFTION		PROCESS AREA P	PAGE_
	CONDITIONS							IONS	10,540
	TABLE 500- ACTION			TAB 0503 -	- STATE				
LE NUMBER WEEKLY									
RENT PRIOR PREQUENCY									
	SET BASEMEMBER UP ON		4,50	ETUPACT +1 E9	(ON)	(or) (te)	(BL)	ELSE	
	MOVE SETUPTAG TO WERETAGE								
	MOVE SETUPACT TO ACTIONOP								
	MOVE SETUPOR TO ACTIONOPTS		JET WOL	exoper EU	(SETON)	(SETOF) (ZEKO)	(BLANK)		
	GO TO ACTORSUCH			TYPACT TO MESSAGE				X	
			90 P					X	
					V V	×	/		
	TAB 500 A - ACTOPSRCH		Most Sor	TOURNAL WESTLAN, TITH					
	THE SECTION THE TENT			ATCHARL + TLTH				× -	
	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1							2	
	ACTIONOR IS ACTORTBLE NYTY	FUN = 3 pos cope - 1 - Revise Fulling	INOVE C	(A) Wextelian, 1		-			
	ACTIONOD IS ACTOPTALE IN THE	2 - VALID Fait Op		TCHAR + 1	1			×	
		3-Valia ton op		202	X		×	X	
		5 pos MIXHO HOR OR IND. TIL.							
	OBNOSKED IS ON NYY W	INDITAL TOR DEEN COTO							
	REFLD SETURNAM 2 SETURNAM 2 X X								
	MOUE SETUDIAM 2 SETUPPAISE X X								
	IMOVE MACKOHEAD TO WORKSOUR XXXX								
	move (NOP) & worksper x								
	MOVE SETUPNAME TO WORKOPHE X								
	DO PUT X								
	MOUE ACTIONOP TO MESSAGE X								
	SET BIGERROX EN ON X								
	GO TO MITEI X								
	GO TO INMERCHAD X								
	GO TO TABBSOL Y XXX								
					TERRIES TO				
THE RESERVE TO SERVE									

DATA RULES

		DATA RULES		
APPROVALS & DATES	PROCESS AREA CONTACT PERSONNEL	PREPARED BY DATE	PROJECT NR DESCRIPTION	AREA PAGE
		REVISION		PROCESS TO PAGE
	CONDITIONS			ACTIONS
	TABLE 501 - ACTION CODE		TAB 502 - ENDACTOP	
RULE NUMBER WEEKLY URRENT PRIOR FREQUENCY	ACTION NAMES OF PAMEL QUB O O O	a plantain a		RULE
JERRENT PRIOR		0 0000000		NR.
		NUNN	BASEMEMBER IS ON YYNN	
			OPERBREY IS ON YN	
		YYYY	OPERBREY IS ON NY	
		N N N N N N N N N N N N N N N N N N N		
	TAGCODE EG (0)	YN	SET BASEMENSER EQ OFF XX	
	TA6C00E' E9 (-)		MOVE SETUPIENE TO SETUPIANIL X	
	POINTVALID IS OU		Move opr 10 og 1 x	
		YN	MOUE SETUPERISE TO WEXTELIAR 314 X	
	STATEVAND IS ON YN		SET DEKTCHAR + SD X	
	ACTIONOP EQ (SeT EQ)		MOVE (4) TO PERTURNEY 1 X	
	Day of Table and the State of t	THE THE THE LANGE & STORM AT THITH	MOVE WOILE TO OUTPUT XXX	
	Do TAGLOTH XXXXX	X X X X X FIND THE LAUGTH & STORE AT TLTH	Do POT XXX	
	MOUE DETURNAMI WESSAGE X	X X POT OPUS IN TEROE - MSG, CONSIDER	SET NEXTCHAIR BY LE WORKEYNS X X X	
			SET WORK BY BLANKS X Y X	
			00 to TAB0501 X	
	MOUS (INIPT) TO WORKOPEL	×	GO TO MITE XXX	
	MOUS TAGEUNADAL TO NORTCHAR, 3	×		
	SET NEXTCHARL + 3	X		
	MOUS (#) TO NEXTCHAR, 1 × × ×			
	SET DEXTENSE + 1 XXX	A ZILLE A LILLE A LILL		
	MOUS SETUPLISHE TO DESTREAR, TUTH X X X X X			
	DET NEXTURE + TUTH XXXXX			
	Mous (#) Nerthan, 1 × × ×			
	SET NEKTCHAR + 2 XXX			
	MOUS (4) Nextchan, 1 X X X X			
	SET NEATTHANK + 1 X X X X X	XXXX		
	GO TO EUDACTOP X X Y Y	XXXXX		
	GO TO STATE X			
ORN WOOLEN				
ORM HOOK NEV AUG THAD				

THE REAL PROPERTY.		THE RESERVE OF THE PERSON NAMED IN					CONTRACTOR OF STREET		distant and the second	marrow married	Stoom
					DATA RULES						
APPROVALS & DATES	PROCESS AREA CONTACT PERSONNEL	PREPARED BY	HI AL		TE		PROJECT NR	DESCRIPTION		PROCESS	
- 1000	CONDITIONS			10.5	73701	1 .				ACTIONS NR	TO PA
	TAB SON- GO TO . & DO ROUTINE		TAB	3 506-0	LOSE						
ULE NUMBER WEEKLY	ACTION NAMEL OF NAMES 000		Action)	NAME 1	P NAME 2				*		
ENT PRIOR	SETUPACT EQ (GO TO) Y Y N N										
	SETUPACT EQ (DO)			(10put)	ouropert.						
	SETUPLE OF DUMERIC Y N Y N		MOVE .	Serrymon 2	OUTOPUD						
-	Setup PARTE 13 POMETE 1			THE RESERVE OF THE PERSON NAMED IN	OUTCOI 33						1
	was tak a mand V V			CLSFINAL #)	OUTCOL35						-
	MOVE (LINK) OUTOPER XX			PUT							
			GO TO	MILFI							-
			1							-	
	MOVE SETUPNIMIL OUTBOOK X X										
	MOUE SETUPLEMED GUTER NO +3 X X										
	MAUE (4) OUTCOL 33 X X XX		-								
	DO PUT XXXX										
	GO TO MITTEL XXXX										
					1-1-1-16/10						1
							the second of				
											1 4 5
											1111
	TAB 505 - ODEN ROUTINE										
	ACTION NAMEZ OF NAMEZ										
	MOVE GOLDIC CSASKED OUTOPERL										
	MOVE GADIC CHIKA OUTOPEIL										1 1
	GO TO MITTE					the state of the	The Part of the				
								In the law of the law			
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					101 10 10 10	PER LEGIS					

		THE RESERVE OF THE PERSON NAMED IN THE PERSON
	DATA RULES	
APPROVALS & DATES PROCESS AREA CONTACT PERSONNEL PREPARED BY	DATE PROJECT NR. E	MESTRIFTION
	REVISION	PROCESS AREA PAGE TO PAGE
CONDITIONS		ACTIONS
TABLE 507 - MOUEV-INITIAL	TABLE 510- COMMA HUNT	TABOSII - CHECK MODIFIER FOR EXP
CUHRENT PRIOR FREQUENCY ACTION NAMES OF NAMES	ACTION NAME 1 OF NAME 2 (D D D D D)	The state of the s
MOVE (MOVEY) . MOKKOBER		ACTION NAMES OF NAMES DD. RULE NA
MOVE (TO) WORKNO		WORKCONTS, SO IS EXPTABLE Y N alex checks for woll 1
. GO TO TABOSOS	SETUPNEL EQ (3) N Y	MOVE WERE TAC TO OUT TAC X
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	TACCODE EY (-) Y N	MOVE (TPLNK) TO OUTUPER X
	The cost by	MONE WORKCOLSY, TOH TO OUTOPNA X
		move (4) OUTCOL 33 X
	SET SETUDULE + 1 X	DO PUT X
TABLE 508 - IVITIAL OPNOTEST	SET TLTH - 1 X	SET WORKTAL EQ BLANKS Y
THOLE SOO IPHINE OFFICE TEST	60 TO TAB 0510 X	40 TO () X X
ACTION NAMEZ OF NAMES Q B B B B	MOVE SETUPNICITUAN WESTERL 33	
	SET NEXTENAR + 12 X	
L, 5 e TUY NAM1 EY (#) (+) (-)	DET NEXTENAR + TOTH X	
SOTUPUME IS DUMERTIC YN	20 11 10 10 11	TABLE OSIZ - EXP TABLE SEAKCH
60 TO TAB 0509 XXXX	MOVE (4) I WERTCHAIL X	FOR POINT NAME
SET SETUPULL ES LOS ETYPLANT X	move (4) workers x x	
DO TAGLITH X	SET SETURNIC ED BLANK STUTH X	ACTION NAME 1 OF NAME 2 DOB
GO TO TABOSTO X	MOVE SETUPLAME TO WEEK OPWO X X	SETUDIAMIZ IS EXPTABLE NYY
	60 TO TABOSIV X	TACCODE EY (-) NY
TABLE 509 - END 200P	MOVE (4) WORKEDLBY X	
	MODE TAGGWAGE, 3 COSTERCOLDS X	DET I, NEXTCHAR EG (SER) X X
ACTUN NAMES OF NAMES 00	move (4) wanter 31 x	SET I, NEXTCHAR EY TAGGERNADON, 3 X
BASEMEMONIS ON YN	SET NEKTERAIL + 16 X	MOVE (4) TO I, PERTCHAR X X X
		MOVE (4) TO I, PERTCHAR X X X
SET WORKNU BY BLANKS X	SET BASEMERICES OFF X	SET NEXTEHAN + 1 XXX
REFLD SETUPNAME X X	GO TO. TABOSOO X	MOVE SETUDUAM 1 TO MESSAGE XX
SET BASEMENTER EQ DE E X	60 TO TAS 0509 XX	DO PRINT XX
MOVE ACTION OF MESSAGE X ERROR MISO-NO POINT		REFLD SETUPUAMA SETUPUAMA X X X
DO PRINT X		SET BASEMENBER EY OFF XXX
. SET BISERROR EX ON X CRITICOL GAROR		GO TO TAB OSOO XXX
SET SETUPACT EY (MOVE) X ASSUME MOVE		
GO TO TAB OSOR X		
FORM GOOD REV AUG 1980 GO TO TABOSOO X		
		A STATE OF THE PARTY OF THE PAR