

Revision	Filename	Author
90908	Assembly Test Timeline.xls	Paul Sakamoto
100408	100408 Assembly Test Timeline.xls	Garry Gillette
111708	100408 Assembly Test Timeline_PB.xls	Phil Burlison
112208	AT Timeline 11.22.08.xls	Paul Sakamoto

Notes

This is the original document compiled from memory and subsequently submitted for proof reading and alteration by the committee at large and anyone else who we could ask to help edit it.

Garry was a major participant in the development of Teradyne ATE on the West coast. He added a number of machine names and filled in most of the notes on the others, as well as correcting errors and adding specification details where appropriate.

Phil was a system architect at Megatest and GenRad as well as Trullium/LTX. Phil added data which he highlighted in green in this file. He added several systems and corrected or filled in many of the LTX/Trullium data points.

Added the Revisions sheet and the Notes sheet as well as re-titled the other tabs to Master list and ATE Timeline Overview. Added several early Fairchild testers per the request of Harry Sello from the SIG Steering committee, who is ex-Fairchild semiconductor. Added some of the key H-P, Agilent, Verigy machines as well as SPEA.