

This document refers to a box of technical reports in the Computer History Museum's collection, described under catalog number 102737736.

The following technical reports in this box have accompanying descriptions:

Performance implications of multiple pointer size by Jeffrey C. Mogul, Joel F. Bartlett, Robert N. Mayo, and Amitabh Srivastava

"This paper analyzes several programs and programming techniques to understand the performance implications of different pointer sizes. Many (but not all) programs show small but definite consequences, primarily due to cache and paging effects."

Drip : a schematic drawing interpreter by Ramsey W. Haddad

"This paper presents a design capture system in which schematics are translated into a procedural netlist specification language."

Recursive layout generation by Louis M. Monier and Jeremy Dion

"This paper presents a "recursive method for generating layout for VLSI chips which combines the flexibility of gate array and standard cell layout with the control and density of custom layout."

Contour : a tile-based gridless router by Jeremy Dion and Louis M. Monier

"Presented is an automatic maze router, based on a corner-stitching data structure, that is versatile enough to route dense VLSI chips automatically with comparable results to hand layout."