

THE ORIGIN OF

CAPTAINZILOG



SYSTEMS DESIGNER NICK STACEY WORKS LATE INTO THE NIGHT. UNKNOWN TO HIM, A SMALL EERIE COMET PASSES OVERHEAD...



















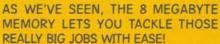




THE Z8000 HAS A REGULAR ARCHITEC-TURE! THAT BIG COMPUTER DESIGN FEATURE AVOIDS THE BOTTLENECKS INHERENT IN TRADI-TIONAL MICROPROC-**ESSOR DESIGNS!**

WITH 418 OP CODES. THE Z8000 PER-MITS COMPACT PROGRAMS! THROUGH-PUT RIVALS THAT OF LARGE COM-PUTERS!

THERE'S 32 BIT OP-**ERATION CAPABILITY** BUILT INTO THE Z 8000 THAT IN-CLUDES MULTIPLY AND DIVIDE! THINK OF THE ADDED FLEXIBILITY AND SPEED THAT MAKES POSSIBLE!





TWO OPERATING MODES (SYS-TEM AND NORMAL) PROTECT SYSTEM INTEGRITY AND MAKE IT NEARLY IMPOSSIBLE FOR THE USER TO "CRASH" THE OPERAT-ING SYSTEM!

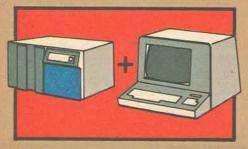
AND ONE OF THE Z8000 PE-RIPHERALS IS A MEMORY MAN-AGEMENT UNIT THAT GIVES YOU POWERFUL MEMORY MANAGE-MENT AND PROTECTION CAPABILITIES!



The Z8000 is at your Zilog distributor right now in two versions, the Z8001 with 8 MBytes of direct address at \$139.30* and the Z8002 with 64 KBytes of direct address for only \$107.10* If that isn't enough to set your imagination going, take a look at these new development systems from Zilog. They'll really give you a chance to set off some fireworks!

SMALL BUT POWERFUL

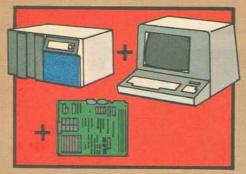
The Zilog PDS 8000 Model 10



A 64 K Byte Microcomputer CRT. Dual Floppies. 9-slot Card Cage. Power Supplies. Z8000 Software Development Package. Operating System, Editor, Linker, Utilities. \$10,500*

THE NEXT STEP UP

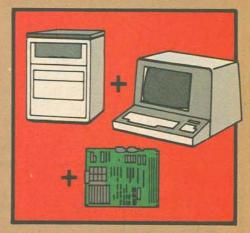
The Zilog PDS 8000 Model 15



Everything in the Model 10 plus a Zilog Z8000 Development Module! Just \$11,995*

FOR THOSE BIG IDEAS

The Zilog PDS 8000 Models 25 and 30



The features of Models 10 and 15 but with 10 megabytes of Hard Disc Drive replacing the Floppies. \$20,000 and \$21,500*

WOW! LOOK AT THIS!

The Zilog Z8000 Development Module

A Z8000-Based computer board with 2K to 8K words of EPROM. 16K to 32K words of Dynamic Ram. Dual Serial Interface. 32 Programmable I/O Lines. Four 8-bit Programmable Counter Timers, Lots of wire wrap area. \$1500*

FOR MORE INFORMATION CALL TOLL FREE!!!

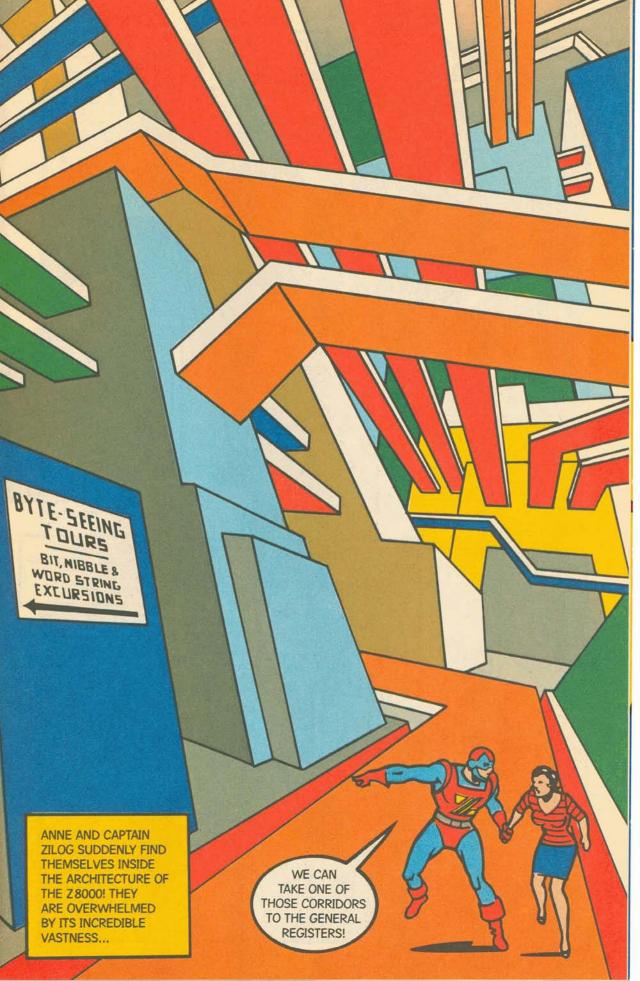
800-538-9367

In California, call (408) 446-4666

*Suggested Retail Price. Domestic U.S. Only. Prices for Z8001 and Z8002 are for quantity 100.



















Written, designed, and illustrated by Lou Brooks. Penciled by Joe Kubert.



MENT UNIT! THE Z-MMU Dynamic segment allocation and protection.

THE EXPANDABLE ONE-CHIP MICROCOMPUTER! THE Z-8 CPU. Memory and I/O on one chip.

THE UNIVERSAL PERIPHERAL CONTROLLER! The Z-UPC Data buffering, manipulation and device control.

THE VERSATILE SERIAL COMMUNI-CATIONS CONTROLLER! The Z-SCC Dual channel, multi-protocol, many new internal functions.

CALL 800-538-9367 TODAY! In California, call (408) 446-4666

THE POWERFUL COUNTER/TIMER AND PARALLEL I/O! The Z-CIO Counters, timers, priority interrupt capabilities.

THE ELASTIC I/O BUFFER! The Z-FIO Interface between asynchronous CPU's in parallel processing environments. THE BUFFER EXTENDED! The Z-FIFO. More depth for the Z-FIO. Use it independently as a bidirectional FIFO.

AND JOINING THEM ALL TOGETHER! THE TALENTED, FRIENDLY Z-BUSIN Easy to use, more address lines, powerful interrupt structure, asynchronous clocks, multiplexing, efficient multiple-CPU resource sharing.

For all the details on this exciting and growing family of 28000 peripheralsiii Or, write: ZILOG, 10460 Bubb Road, Cupertino, CA 95014.

