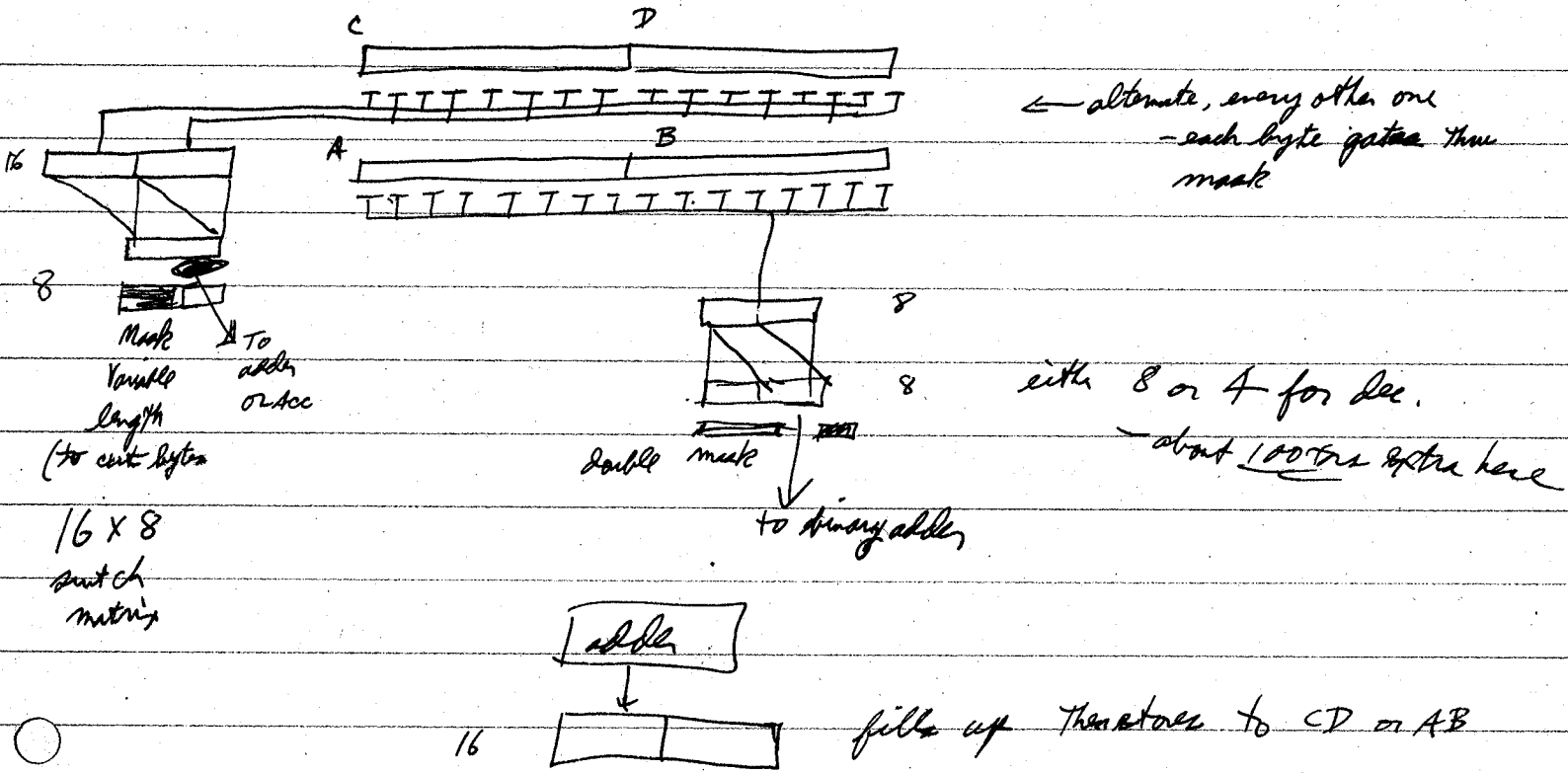


VFL logical flow in hardware



going to character only would save ~1000 ns,

~30% of total ns. are controls in Junior,

would be less for SIGMA (more regs. - simpler controls)

$$\text{Time for } \overset{\text{VFL}}{\text{load}} \text{ 64 bits} = (3.0 \mu\text{s})$$

$$0.26 \mu\text{s per byte}$$