SERIES 7000 CIRCUIT MEMO #27

SUBJECT:

MEMORY CROSS-SECTION

BY:

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DATE:

August 30, 1957

ABSTRACT:

Values of characteristic impedance and time delay for the drive lines in the memory must be known in order to design the proper driving circuits. Two model cross-sections, which simulate actual memory conditions, were built so that these measurements could be taken. The results of these measurements are given.

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