PROJECT SILO TECHNICAL MEMO #1

SUBJECT:

PRELIMINARY DESCRIPTION OF HIGH-SPEED PATH-

SWITCHING CORE

BY:

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DATE:

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ABSTRACT:

A core with means of obtaining high-speed operation without a high half-select back-voltage was conceived during work under contract with the IBM Airborne Computer Laboratory. This paper sets forth the original idea and gives the result of tests performed under Project SILO support.

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