

LA-IBM Meeting (2nd day)

Fri Nov 15, 1957

IN	OUT
✓	
	X
	X
	X
✓	
	X
✓	
✓	
	X
✓	
	X
✓	
✓	

Direct Compare Count

Direct Count Up

Direct Decrement

Modify V from C

Unconditional Branch (4-Bit Index)

return branch {
 1, 2, 3 ops depending:
 { enable (don't store IC)
 { disable

Form Address

Pathfinder (or something equiv.)

Store V to W (cut to length) [Misc format]

Smart

Reset

Transmit Index

Block Store Zero

Relative Branch

[go over interrupt]

(Page) Problem of interrupt on Transfer order - need where from + where to,
 disable?, ~~add~~ pathfinder IC,

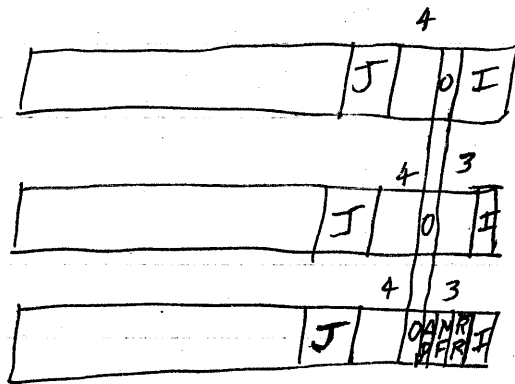
Something must be done here -- /

Supervisory Prog - assigns loc. of each main code - one doesn't know
 absolute loc. - (method proposed by LAR C)

Possible to reshuffle. Direct- C+Br formats:

2 Direct formats { 1. 4 bit I
 { 2. 1 bit I

put Loads & Stores in (1)
other² in (2)

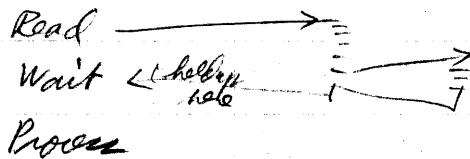


Load } Full
Store } V
 R

8 ops

} 8x8 = 64 ops

Serious question on



will analyze adjustment on zero noisy mode. ?

compare 135 ... 1 88.1
 135 ... 0 87.0000

} These look very different.
[48 subtract ??]

tentative conclusion

probably should be put in ---

cum Mpy? where to put in, (do in result same place)
single (" " " ")

- question of storage loc, which exchange cannot ~~keep~~ handle.

Packaging Question: leaving holes for future expansion.

Chapter 3: repeat what contract says about leaving open. --

Next Meeting 2-3 mos.

- revisions of manual

Report to Contracting office } what news have changed + why,

- several people drop out - at least every 30 days.