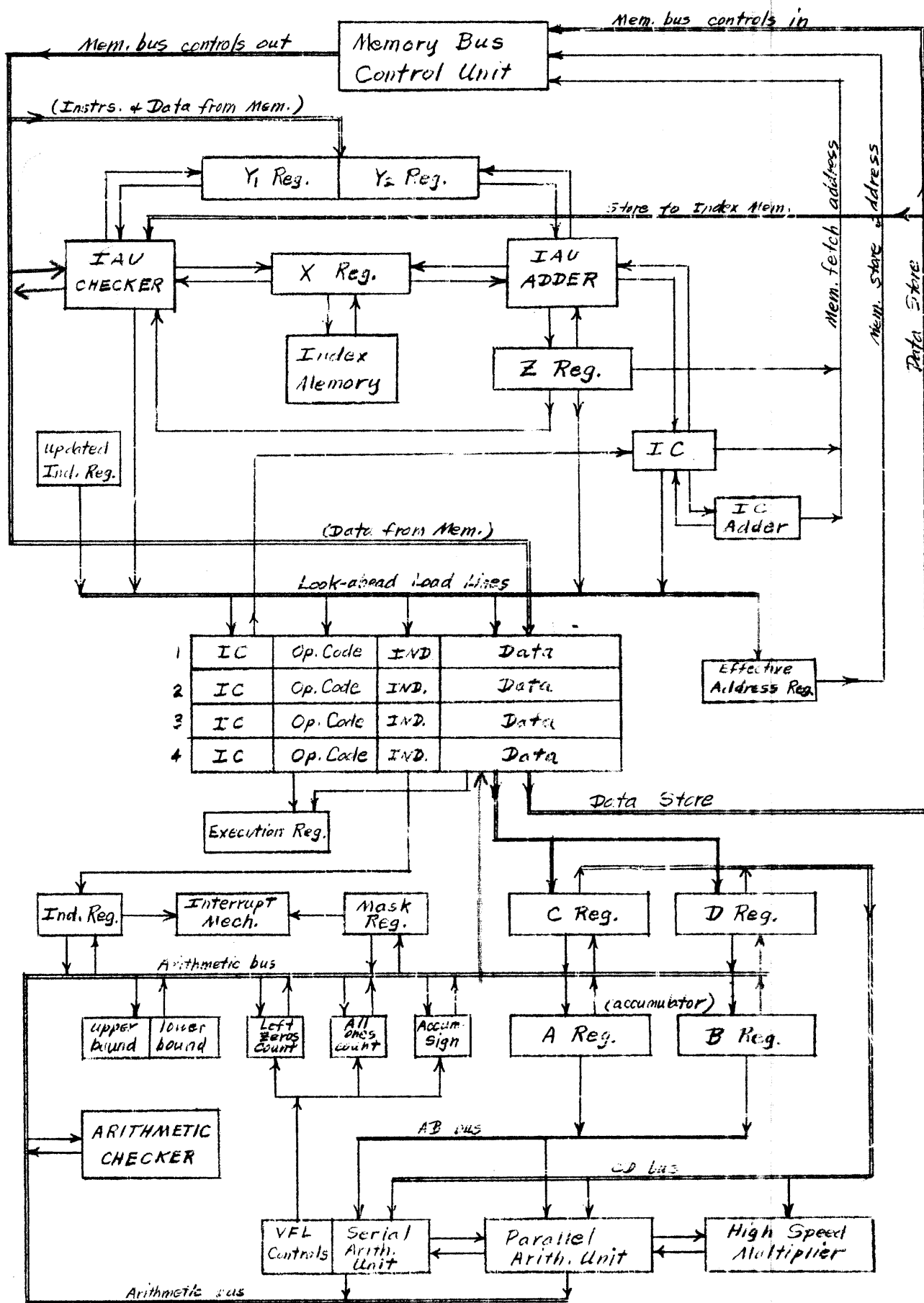


STRETCH Data Flow Schematic



memory bus

Indexing Arithmetic Unit

Lock-Ahead Unit

Arithmetic Registers

Arithmetic Unit

STRETCH Data Flow Schematic

