

February 12, 1959

MEMO TO: Dr. H. G. Kolsky
Dr. G. A. Blasuy
Dr. W. Buchholz
Dr. F. P. Brooks
Dr. A. Padegs
Mr. E. W. Coffin
Mr. E. F. Codd

SUBJECT: 7030 System School - February 16 - 27, 1959

Attached is the present schedule for the 7030 System School which is to be held for the next two weeks. The comments I have received to date indicate that this is going to be an outstanding session. I want to thank you for your cooperation in detailing the session in your charge in such an expeditious and efficient manner

I would appreciate your being in attendance throughout the day assigned to your charge to introduce the speakers, guide discussions, and handle the gaps and overruns which may occur. Mr. D. Thompson of the Education Department will be handling the mechanics of the class organization and will be available for consultation and advice. If special visual aids or other equipment is needed, please make sure that he receives your request with sufficient advance notice so that he can make arrangements. If you cannot be present during a portion of the day assigned to you, please inform Mr. Thompson

I am confident that this will be an outstanding school, and I am sure that the class will be able to return to the field with a full measure of the enthusiasm which we all share for this system.

Attachment
DWS:jcj

cc: Mr. J. C. Gibson
Mr. E. G. Law
Dr. R. A. Polivka
Mr. W. Y. Stevens
Mr. C. A. Scalzi
Dr. G. H. Swift
Mr. D. W. Pendery
Mr. D. Thompson
Mr. E. Cowen
Miss E. McDonough



D. W. Sweeney
Product Planning Coordinator
Project 7000

Dr. P. S. Herwitz
Mr. J. H. Pomerene
Mr. S. W. Dunwell
Mr. W. R. Stringfellow

7030 System School

Condensed Schedule

<u>Date</u>		<u>Coordinator</u>
Monday February 16	Introduction Laboratory Tour Concepts System Introduction	D. W. Sweeney
Tuesday February 17	Indexing and Branching	G. A. Blaauw
Wednesday February 18	Floating Point	D. W. Sweeney
Thursday February 19	Variable Field Length	W. Buchholz
Friday February 20	Miscellaneous Ops Registers Interrupt System	F. P. Brooks
Monday February 23	Timing and Simulation Installation Criteria Harvest	H. G. Kolsky
Tuesday February 24	Input-Output - Exchange	A. Padegs
Wednesday February 25	Input Output - Units - Console High Speed Disk File	E. W. Coffin
Thursday February 26	Multiprogramming Reliability Checking Error Recording	E. F. Codd
Friday February 27	System Expandability	D. W. Sweeney

7030 System School

Class Schedule

Monday February 16

9:00 - 10:00	Introduction	E. Cowen
10:15 - 12:15	Laboratory Tour	D. W. Sweeney
1:00 - 2:30	Asynchronism and Overlapping	H. G. Kolsky
2:40 - 3:20	Speed vs. Generality	D. W. Sweeney
3:30 - 4:30	System Introduction	W. Buchholz F. Brooks

Tuesday February 17

8:30 - 10:30	Indexing and Control Word Techniques	G. A. Blaauw
10:45 - 12:15	Operand Designation, Formats, Indexing and Data Transmission	C. A. Scalsi
1:00 - 2:00	Address Monitoring	F. P. Brooks
2:10 - 3:00	Examples on Indexing and Control Words	R. P. Polivka
3:10 - 4:30	Branching	G. H. Swift

Wednesday February 18

8:30 - 9:30	Design Objectives General Philosophy	D. W. Sweeney
9:40 - 11:20	Single and Double Precision Op Codes	J. C. Gibson
11:30 - 12:15	Multiple Precision Coding	D. W. Sweeney
1:00 - 2:00	Problems Requiring Very High Performance Computers	H. G. Kolsky
2:10 - 3:20	Programming	E. W. Coffin
3:30 - 4:30	Discussion	D. W. Sweeney

7030 System School
Class Schedule

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Thursday February 19

8:30 - 10:20	Processing Data in Bits and Pieces	W. Buchholz
10:30 - 12:15	Integer Arithmetic Operations	W. Y. Stevens
1:00 - 2:40		
2:50 - 3:30	Examples of Integer Arithmetic	W. Buchholz
3:30 - 4:30	Radix Conversion Operations	G. A. Blaauw

Friday February 20

8:30 - 9:30	Connective Operations	F. P. Brooks
9:30 - 10:00	Examples on Connective Operations	F. P. Brooks
10:10 - 11:10	A Program-Controlled Program Interrup- tion System	F. P. Brooks
11:20 - 12:15	Indicators	G. H. Swift
1:00 - 2:00	Execute Operations and Interruption Exceptions	F. P. Brooks
2:10 - 3:00	Registers	W. Y. Stevens
3:10 - 3:40	Programming Examples	R. P. Polivka
3:40 - 4:30	Programming Practice	R. P. Polivka

Monday February 23

note
→ 9:00

8:30 - 9:50	The Virtual Memory Concept	H. G. Kolsky
10:00 - 11:00	Timing Simulator Code	H. G. Kolsky
11:15 - 12:15	Installation Criteria	E. G. Law
1:00 - 2:00	Harvest System Engineering	J. H. Pomerene
2:10 - 3:20	Harvest System Programming	P. S. Herwitz
3:30 - 4:30	Timing Simulation Results	H. G. Kolsky

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7030 System School

Class Schedule

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Tuesday February 24

8:30 - 9:30	General Description of Exchange	W. Buchholz
9:40 - 10:30	Reading and Writing, Channel Addresses Instruction Format	A. Padegs
10:40 - 11:20	Input-Output Instructions	C. A. Scalzi
11:30 - 12:15	Use of Control Word Flags	A. Padegs
1:00 - 2:00	Indicators Reflecting Input-Output Operations	A. Padegs
2:10 - 2:50	Input-Output Status Reporting	G. H. Swift
3:00 - 3:20	Input-Output Programming Examples	E. McDonough
3:20 - 3:45	Initial Program Loading	G. H. Swift
3:55 - 4:30	Miscellaneous Exchange Items	A. Padegs

Wednesday February 25

8:30 8:30 - 9:50	729 Tape Units	E. W. Coffin
10:00 - 11:00	Card Equipment	E. W. Coffin
11:10 - 12:15	Usage with Exchange (Flow Charting)	E. W. Coffin
1:00 - 2:50	Console and IQS	F. P. Brooks R. P. Polivka
3:00 - 3:40	High Speed Disk Operation	E. W. Coffin
3:50 - 4:30	Disk Application	D. W. Sweeney

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7030 System School
Class Schedule

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Thursday February 26

8:30 - 9:30	Supervisory Programs Multiprogramming Objectives Principles Experiments	E. F. Codd
9:40 - 10:40	Machine Features for Multiprogramming Interrupt Handling Principles	C. A. Scalzi
10:50 - 12:15	I/O Handling Time Monitoring and Recording Other Interrupt Handling	E. McDonough
1:00 - 2:00	Multisupervisor Scheduling, Loading Expediting	E. F. Codd
2:10 - 3:30	Checking and Fault Location	W. R. Stringfellow
3:40 - 4:30	Programming	J. C. Gibson

~~J. C. Gibson~~
P. Horvath?

Friday February 27

8:30 - 9:30	System Expandability	D. W. Sweeney
9:40 - 10:30	Replaceable Costs and Job Costs	D. W. Sweeney

to 7 weeks