

## 204 classes of ops.

### Fixed-Point Operations

CLA	- load
ADD	add
ADM	add <del>add</del> + (mod)
CLS	- load -
SUB	add + (mod)
SBM	add + (mod)
MPY	} mpy
MPR	
RND	- special
DVH	} div with interrupt
DVP	
LDQ	- load (full)
STQ	store (full)
SLQ	store (partial)
STO	store (full)
STP	store (partial)
<del>STD</del> STD	store (partial)
STA	store (partial)

### Logical Operations

CLM	(SZ acc)
STZ	(storage den)
CHS	} sign manipulators
BSP	
SSM	

CAL - logical CLR (P bit + sign)

SLW - logical STO

ANA } and to Acc. n. to STG.

ANS

ORA } or

ORS

COM "not A"

ACL - for checksums

logical connectives

A	0	0	1	1
B	0	1	0	1
Result C	0	0	0	0
	0	0	1	0
	0	0	1	1
	0	1	0	0
	0	1	1	0
	0	1	1	1
	1	0	0	0
	1	0	0	1
	1	0	1	0
	1	0	1	1
	1	1	0	0
	1	1	0	1
	1	1	1	0
	1	1	1	1

Shifting operations

ALS "off set"

ARS

LLS

LRS

LGL - take sign of Q

RQL - rotate Q

areas of improvement

1. Faster circuit component.
2. asynchronous design
3. Control and I/O
4. Indicators handling
5. Interrupt system
6. VFL generalized <sup>fixed pt.</sup>
7. FL Point generalized <sup>fixed pt.</sup>
8. Subsetting <sup>fixed pt.</sup>
9. Checking.

Floating Point:

FAD

VFA add, unnormalized mod

FSB add + (mod)

VFS add + (mod)

FMP npx

VFM npx + (mod)

FDH } interrupt

FDP

25 people

Control operations:

NOP

HPR halt + pr.

ETM — } trapping mode - type of interrupt

LTM }

HTR halt

TRA

TZE } on + off Special indicators  
or results

TNZ

TPZ

TMI

TOV } on + off

TNO

TQP Q plus.

TQO Q overflow

TLQ } "compare" opn. { Q vs A

CAS } { A vs S

TSX branch link

TXI TX's

TXH

TXL

TIX

TNX

TTR Trap transfer

PBT p bit test

LBT low bit test

DCT divide check

RTT tape check

— machine result  
failure

Index loading + storing

LXA partial fields.

LXD

SXD

PAX

PDX

PXD

(no SXA + PXA)

Input-output

( RDS                      CRP )  
  WRS

BST - tape

WEP - tape

REW - tape

ETT - tape

LDA Drum

PSE } Test switches

MSE } "on + off"

generally of