

Secher Talk Mar 17, '58

Auto prog: Fortran 20 man-years
4000 instr
SORT 53A 108 pages 50 pages doc

"Macro coding" - Multiple address - Tagging,

ASCC 1944
SSEC 1948
701 1953
NORC 1954
650 1954
702 1954
705 I, II 1955
704 1955
709 1959
705 III 1959

No addresses
Sequential prog
Loc address
Table lookup
Incremental tape coding
Incremental tape memory
Branching
Half word addressing
sub-routines
Programmed indexing
Program Block Read & Write
auto Block Read & Write
automatic indexing
Variable field length
Indirect addressing

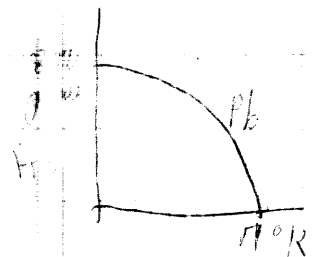
Tag memory - associative,

Store wd + tag,
call wd by giving tag (not addr)

Transfluxer Tag Mem:
Core tag Mem:

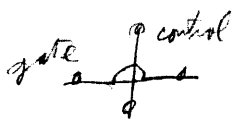
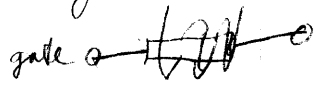
Cryogenics: alloys ~ 20°K
elements 8°K down

1. Temperature
2. Magnetic Field



D. Buck MIT article PractICE Apr. 1956.

cryston - has amplification.



cannot have control
inserts resistance in gate circ.

Switch

relay

Flip flop

Flip Flop with Entry - Exit

Time const $\frac{L}{R}$

Logic - transistor

100

Mem - core

1 cost

compar. 2.

1 cost

1