

Thurs Feb 6

11

22-10

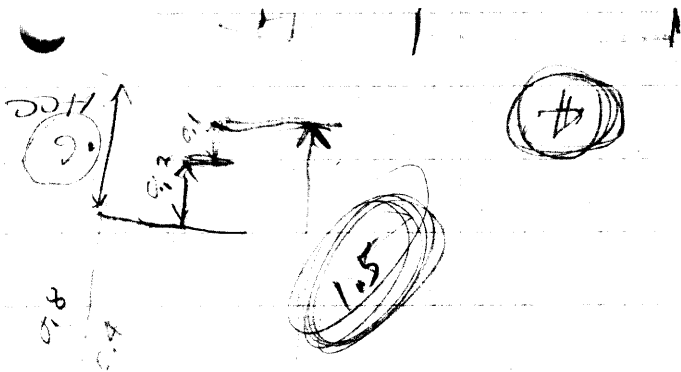
1.5
7.64

(D)

2.2

11

11



1.5 → 2.2 total time



look into increase return per time.

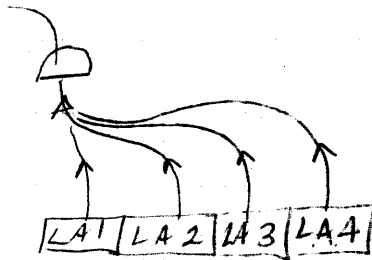
Depth of LA.

No. gates into reg. etc.,
question of sharing functions, - used serially.

Store LA-Mem

- (1) Data from LA → Mem Bus (eg store)
LA → Execution units
LA → Index Arithmetic
LA → checker —

would also have
to check?
Switch
{ available,
one slot
width
0.2 0.3



- (2) also { Store from I → index unit
occurring at same time " " AU → mem. (Takes priority)
holds up I for one slot width.

AU 1.5 μ s